

# **PADERBORN UNIVERSITY**

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**FACULTY FOR COMPUTER SCIENCE, ELECTRICAL ENGINEERING AND MATHEMATICS**  
**DEPARTMENT OF ELECTRICAL ENGINEERING AND INFORMATION TECHNOLOGY**

**MODULE HANDBOOK**  
**MASTER'S PROGRAM COMPUTER ENGINEERING v4 (CEMA v4)**

**DATE: 5. MÄRZ 2026**

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# 1 Preamble and references

For technical reasons, the preamble of the module manual has been moved. It can be found under Examination Regulations and Module Handbooks under the item “Module Handbooks” on the pages of the Institute EIM-E. We kindly ask you to pay attention to this preamble.

If you have any questions regarding this module handbook or the preamble, please contact either.

- to the Examination Board Computer Engineering,
- to the Computer Engineering Student Advisors,
- to the Electrical Engineering Student Advisor, or
- to the PAUL Electrical Engineering Student Services.

Please also note that

1. this module handbook lists all modules provided according to the examination regulations, even if they are not offered in the corresponding semester.
2. this module manual contains the data of the date of creation. All information is without guarantee.

## 2 Compulsory Area

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Compulsory Area	
Modules	<ul style="list-style-type: none"> <li>* Advanced Networked Systems</li> <li>* Advanced Computer Architecture</li> <li>* Statistical Signals</li> <li>* Analysis and Design of Electronic Circuits</li> <li>* Project Group</li> <li>* Scientific Work Style</li> </ul>
Catalogue advisor	
Credits ECTS	6
Learning objectives	

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Advanced Networked Systems							
Advanced Networked Systems							
<b>Module number:</b>	<b>Workload (h):</b>	<b>Credits:</b>	<b>Regular Cycle:</b>				
M.079.4096	180	6	summer term				
<b>Language:</b>	<b>Semester number:</b>	<b>Duration (in sem.):</b>	<b>Module status (P=C/WP=CE)</b>				
en	1-3	1	P				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05820 Advanced Networked Systems	L2 Ex3	75	105	C	50/25	
2	<b>Options within the module:</b>						
	none						

## 2 Compulsory Area

3	<p><b>Admission requirements:</b></p> <p>none</p> <p><i>Prerequisites of course Advanced Networked Systems:</i></p> <p><b>Recommended Proficiencies</b></p> <p>Knowledge of computer networks, operating systems, programming languages, C/C++ and Python programming in the Linux environment, and a keen interest in understanding how things work under the hood. Ability to read scientific papers professionally. Ability to code in a complex setting.</p>										
4	<p><b>Contents:</b></p> <p><i>Contents of the course Advanced Networked Systems:</i></p> <p>The course will cover concepts and designs for modern networked systems adopted by the Internet and cloud data centers to meet the ever-increasing demands of data transfer and computation driven by big data and machine learning applications.</p> <ul style="list-style-type: none"> <li>• Networking fundamentals (refresher)</li> <li>• Data center networks (architectures, congestion control)</li> <li>• Software-defined networks (SDN, OpenFlow)</li> <li>• Programmable networks (P4, eBPF/XDP)</li> <li>• Programmable network device architectures (RMT, SmartNICs)</li> <li>• In-network computing (caching, aggregation)</li> </ul>										
5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of this course, students will be able to</p> <ul style="list-style-type: none"> <li>• gain knowledge of current research topics in networked systems.</li> <li>• understand the design of these new networked systems technologies and reason about the design choices therein.</li> <li>• build complex networked systems by applying some of these designs, analyze and evaluate the merits and limitations of these designs, and explain the design choices for the built systems.</li> </ul>										
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of achievement</th> <th style="width: 20%;">Duration or Scope</th> <th style="width: 20%;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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a)	Assignments, course paper or progress reports		CA								

## 2 Compulsory Area

8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4
12	<b>Module coordinator:</b> Prof. Dr. Lin Wang
13	<b>Other Notes:</b> <i>Remarks of course Advanced Networked Systems:</i> <b>Implementation Method</b> The course content will be taught with slides-based lectures, interactive exercises, and programming-based project assignments. Much of the course will be based on discussions of cutting-edge research topics, complemented with hands-on programming assignments. <b>Learning Material, Literature</b> <ul style="list-style-type: none"><li>• Lecture slides and exercise sheets</li><li>• Project description documents for the programming assignments</li><li>• Additional literature (e.g., research papers) on the course website and in the lecture slides</li></ul>

## 2 Compulsory Area

Advanced Computer Architecture							
Advanced Computer Architecture							
<b>Module number:</b> M.079.4005	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05724 Advanced Computer Architecture	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Advanced Computer Architecture:</i> <b>Recommended Proficiencies</b> Knowledge from the Bachelor course Computer Architecture is helpful.						
4	<b>Contents:</b>  <i>Contents of the course Advanced Computer Architecture:</i> The course teaches the essential concepts and methods used in the design of modern processors. In particular, advanced aspects of optimizing access times and throughput in the memory hierarchy, as well as approaches to exploiting parallelism at the instruction, data, and thread levels are discussed. The course covers the following topics: <ul style="list-style-type: none"> <li>• Fundamentals of computer architectures (refresher)</li> <li>• Memory hierarchy design</li> <li>• Instruction-level parallelism</li> <li>• Data-level parallelism: Vector, SIMD and GPU architectures</li> <li>• Thread-level parallelism</li> <li>• Warehouse-scale computer</li> <li>• Domain-specific computer architectures</li> </ul>						

## 2 Compulsory Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of this module, students will be able to</p> <ul style="list-style-type: none"> <li>• explain the architecture of modern multi-level storage systems, mathematically model the average access time, and qualitatively describe and evaluate the influence of the main design parameters,</li> <li>• explain the concepts of parallel processing at the data, instruction, thread and task levels and contrast algorithms for out-of-order execution,</li> <li>• examine the limits of computing power for specific applications and architectures using the Roofline model,</li> <li>• explain the common approaches and protocols for cache coherence in multiprocessor systems and demonstrate how they work with examples, and</li> <li>• quantitatively evaluate different characteristics of modern computer systems through computer simulation and interpret the results.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">ZU</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			ZU	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Christian Plessl, Prof. Dr. Marco Platzner</p>										

## 2 Compulsory Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Computer Architecture:</i></p> <p><b>Implementation method</b></p> <p>The course consists of a lecture and paper&amp;pencil as well as practical exercises. The lecture is held with a beamer and blackboard. In the paper&amp;pencil exercises, assignments are handed out and their solutions are presented and discussed in an exercise session. In the practical exercises, the effects of design decisions and optimisation options at the hardware and software level are examined and deepened on the computer with simulators of processor and memory systems using case studies.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides and exercise sheets</li><li>• Exercise sheets and technical documentation for the for the computer-based exercises</li><li>• Hennessey, Patterson: Computer Architecture: A Quantitative Approach (6th edition), Morgan Kaufmann, 2017.</li><li>• Information about alternative and additional literature as well as teaching material on the course's website and in the lecture slides</li></ul>
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2 Compulsory Area

Statistische Signale						
Statistical Signals						
<b>Module number:</b> M.048.210XX	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.21004 Statistical Signal Processing	2L 2Ex, WS	60	120	C	60/30
b)	L.048.24014 Statistical Signal Processing	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> 1 of 2 resp. Course a) or b)					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Verarbeitung statistischer Signale:</i> <b>Recommended:</b> Basic knowledge of statistical signal description as learned in a bachelor's degree program in electrical engineering or related disciplines. <i>Prerequisites of course Statistical Signal Processing:</i> <b>Recommended:</b> Undergraduate courses in signal processing and probability					

## 2 Compulsory Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Verarbeitung statistischer Signale:</i></p> <p><b>Short description</b></p> <p>With the course Processing of Statistical Signals, students gain an understanding of the importance of descriptive and inferential statistics for many areas of electrical engineering. They consolidate their basic knowledge of probability calculus and statistics and gain an insight into estimation and detection theory, as well as statistical time series analysis. In addition, procedures are presented with the help of which estimated values obtained from data can be evaluated with regard to statistical significance. Knowledge of detection and estimation theory, as well as time series analysis, and critical evaluation of experimental results are essential for understanding and critically applying modern signal processing techniques.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Random experiment, axiomatic notion of probability.</li><li>• Concept of random variables, distribution function, important distributions of discrete and continuous random variables, random variable transformation.</li><li>• Maximum likelihood parameter estimation, linear estimators, quality assessment of estimators, Cramer-Rao bound.</li><li>• Bayesian estimation, (L)MMSE estimation, special case Gaussian distribution</li><li>• Stochastic processes, stationarity, ergodicity, correlation function and power density spectrum, white noise, Markov chains</li><li>• Optimal filter according to Wiener, autoregressive processes</li><li>• Maximum-a-Posteriori and Neyman-Pearson decision rule, receiver operating characteristic, statistical hypothesis tests</li></ul> <p><i>Contents of the course Statistical Signal Processing:</i></p> <p><b>Short Description</b></p> <p>Statistical signal processing comprises the techniques that engineers and statisticians use to draw inference from imperfect and incomplete measurements. This course covers a selection of topics from the major domains of detection, estimation, and time series analysis.</p> <p><b>Contents</b></p> <p>Topics that may be covered in this course include correlation analysis, linear minimum mean-squared error estimation, performance bounds for parameter estimation, Neyman-Pearson detectors, wide-sense stationary, nonstationary and cyclostationary time series, and complex-valued random signals.</p>
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## 2 Compulsory Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the module, students will be able to,</p> <ul style="list-style-type: none"> <li>• describe random variables or signals with methods of statistical signal processing</li> <li>• independently perform calculations regarding reliability, hit frequency, etc.</li> <li>• Design and apply estimation methods for simple parameter estimation problems.</li> <li>• Construct statistical hypothesis tests and apply them to concrete problems</li> <li>• Define the boundary conditions for experimental investigations in such a way that the results lead to reliable conclusions</li> <li>• Compare newly obtained experimental data with existing models</li> <li>• To apply a correlation or spectral analysis to time series</li> <li>• To design optimal filters for given problems.</li> </ul> <p>After attending this module, students will be familiar with the basic principles of statistical signal processing. They will understand how to use statistical signal processing techniques in electrical engineering and they will be able to apply them to relevant areas (such as in communications engineering). Students will develop the confidence to solve mathematical problems in analysis and design. The principles learned in this course can be applied to other fields.</p> <p><b>Key qualifications:</b> Students will</p> <ul style="list-style-type: none"> <li>• Are able to apply the methods of describing quantities and signals as random variables or random processes to a wide variety of problems in the field of electrical engineering and information technology.</li> <li>• Are able to assess the power, but also the limitations of statistical methods in various applications.</li> <li>• Are able to critically evaluate results of experimental investigations from various fields of application and to design experiments in such a way that their results allow reliable conclusions to be drawn.</li> <li>• Are able to evaluate measurement results using modern program systems</li> <li>• Are able to analyze extensive tasks in a group, break them down into subtasks and work on them in a solution-oriented manner.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 20%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a) - b)</td> <td style="text-align: center;">Written or Oral Examination</td> <td style="text-align: center;">120-180 min or 30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a) - b)	Written or Oral Examination	120-180 min or 30-45 min	100%
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a) - b)	Written or Oral Examination	120-180 min or 30-45 min	100%						
7	<p><b>Study Achievement:</b> none</p>								
8	<p><b>Prerequisites for participation in examinations:</b> None</p>								
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>								

## 2 Compulsory Area

10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Häb-Umbach</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Verarbeitung statistischer Signale:</i></p> <p><b>Course Homepage</b>  <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/statistical-signal-processing">[https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/statistical-signal-processing]</a>(<a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/statistical-signal-processing">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/statistical-signal-processing</a>)</p> <p><b>Methodical implementation</b></p> <ul style="list-style-type: none"> <li>• lectures with predominant use of blackboard, occasionally slide presentation</li> <li>• Classroom exercises with exercise sheets and demonstrations on the computer</li> <li>• Practical exercises with Matlab, in which students independently develop and implement an experimental setup, and apply statistical analysis methods to the obtained results</li> </ul> <p><b>Learning materials, references.</b>          Provision of a detailed script and keyword summary slides for each lecture. Provision of exercise problems including sample solutions and example implementations in Matlab.          Further literature:</p> <ul style="list-style-type: none"> <li>• N. Henze, Stochastik für Einsteiger, 8th edition, Vieweg-Teubner Verlag, 2010.</li> <li>• E. Hänsler, Statistical Signals — Fundamentals and Applications, 3rd edition, Springer, 2001</li> <li>• S. M. Kay, Fundamentals of Statistical Signal Processing — Estimation Theory, Prentice Hall, 1993</li> <li>• J. L. Melsa, D. L. Cohn, Decision and Estimation Theory, McGraw-Hill, Kogakusha, 1987.</li> <li>• A. Papoulis, Probability, Random Variables, and Stochastic Processes, 2nd edition, McGraw-Hill, New York, 1984.</li> </ul> <p><i>Remarks of course Statistical Signal Processing:</i></p> <p><b>Course Homepage</b>  <a href="http://sst.upb.de/teaching">http://sst.upb.de/teaching</a></p> <p><b>Implementation</b>          Lectures and tutorials</p> <p><b>Teaching Material, Literature</b>          Literature references are given in the first lecture.</p>

## 2 Compulsory Area

Analysis and Design of Electronic Circuits						
Analysis and Design of Electronic Circuits						
<b>Module number:</b> M.048.90107	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.90107 Analysis and Design of Electronic Circuits	2L 2Ex, WS	60	120	C	90/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Analysis and Design of Electronic Circuits:</i> <b>Recommended:</b> Good knowledge in differential equations, Laplace transform, Fourier transform, electrical network analysis (Kirchhoff's laws, Norton equivalent, Thevenin equivalent, transfer functions, Bode diagram etc.), semiconductor device physics (band diagram, conduction mechanisms in semiconductors, minority and majority charge carriers, n-type, p-type semiconductor, physics of pn junction, physics of MOS capacitance), semiconductor devices (physical operation and device equations of pn-diode, MOS transistor, and bipolar transistor), basic digital design (boolean algebra, truth tables, combinational logic)					

## 2 Compulsory Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Analysis and Design of Electronic Circuits:</i></p> <p><b>Short Description</b></p> <p>The lecture gives an introduction to analysis and design of analog and digital circuits and systems. It builds on basic knowledge of electron devices (bachelor-level) and the compulsory lectures “Advanced System Theory” and “Modeling and Simulation”. The lecture presents a modern approach for analysis and design of electronic circuits and system which combines mathematical analysis and circuit simulation.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"> <li>• Nonlinear, large-signal modeling of pn diode, bipolar junction transistor (BJT), and MOS transistor</li> <li>• Nonlinear, large-signal analysis of circuits with diodes, BJTs, MOS transistors</li> <li>• Linear modeling and one-/two-port representations of diodes, transistors, and amplifiers</li> <li>• Linear small-signal analysis of BJT and MOS transistor amplifiers</li> <li>• Single-transistor amplifier analysis</li> <li>• Differential amplifier analysis</li> <li>• Modeling and analysis of operational amplifier circuits</li> <li>• CMOS logic</li> <li>• Analysis and design of combinational logic circuits</li> <li>• Analysis and design of sequential logic circuits</li> <li>• Application examples</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>The students will be able to</p> <ul style="list-style-type: none"> <li>• describe appropriate methods for analysis and design of analog systems</li> <li>• describe appropriate methods for analysis and design of digital systems</li> <li>• assess the limitations of the different methods</li> <li>• understand and calculate the behaviour of simple analog and digital circuits</li> <li>• use a numeric simulation tool for electronic systems and circuit simulation</li> <li>• describe typical components and subsystems</li> </ul> <p><b>Key qualifications:</b></p> <p>The lecture conveys an understanding of the interaction of different modeling techniques, mathematical analysis approaches, and numerical simulation, as well as how to apply these effectively to the design of electronic systems. The methods for analog electronic design are transferrable to the design of continuous-time, continuous-amplitude systems. The methods for digital design are transferrable to the design of discrete-time, discrete-amplitude systems.</p>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						

## 2 Compulsory Area

7	<p><b>Study Achievement:</b> none</p>
8	<p><b>Prerequisites for participation in examinations:</b> None</p>
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>
12	<p><b>Module coordinator:</b> Prof. Dr.-Ing. J. Christoph Scheytt</p>
13	<p><b>Other Notes:</b> <i>Remarks of course Analysis and Design of Electronic Circuits:</i> <b>Course Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/circuit-and-system-design/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/circuit-and-system-design/</a> <b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lecture with Powerpoint presentation and handwritten mathematical derivations using tablet and beamer</li> <li>• One part of the exercises as handwritten calculation exercises using tablet and beamer</li> <li>• Other part of exercises as practical design tasks using using LTspice simulation</li> </ul> <p><b>Teaching Material, Literature</b> Lecture slides and videos; Exercise slides. Additional literature references will be given in the first lecture</p> <ul style="list-style-type: none"> <li>• Richard C. Jaeger, Travis N. Blalock, "Microelectronic Circuit Design", McGraw Hill, 4th edition, 2010</li> <li>• Neil H. E. Weste, David Money Harris, "CMOS VLSI Design", Addison Wesley, 4th edition, 2010</li> </ul>

## 2 Compulsory Area

<b>Projektgruppe</b>							
Project Group							
<b>Module number:</b> M.079.01290	<b>Workload (h):</b> 540	<b>Credits:</b> 18		<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 2-3	<b>Duration (in sem.):</b> 2		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.07099 Project Group	PG	240	300	C	16	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> The module Project Group cannot be chosen as an early performance according to §12 (4) of the General Regulations.  <i>Prerequisites of course Projektgruppe:</i> <b>Recommended Proficiencies</b> Depending on the topic.						
4	<b>Contents:</b>  <i>Contents of the course Projektgruppe:</i> In a project group, a group of usually 8-16 students works together over a period of one year (two semesters) on a research topic determined by the group organizer. Project groups introduce students to current research topics that are usually related to the group organizer's special area of interest, and the team working of the project group should be a preparation for industrial practice. Topics of project groups cover the whole range of research interests of the research groups in the Department of Computer Science. Typically, the project group members are divided into subteams. The team selects a project group leader who controls the work of the whole team. The leader is responsible for internal communication and reporting potential issues to the group organizer. The team members meet regularly to discuss their progress with the group organizer and report on their current progress status. The team members successfully pass this module after submitting the source code and the group report, and providing the final presentation. Further constraints can be defined by the group organizer.						

## 2 Compulsory Area

5	<p><b>Learning outcomes and competences:</b></p> <p>In project groups, participating students gain first-hand practical experience in working in a team and organizing a project; in doing so, they become prepared for daily work in their later professions. The students personally experience how to carry out extensive development processes in a team. Since the tasks are divided among the individual team members, the participating students become skilled in reporting their progress and research findings to the other group members.</p> <p><b>Non-cognitive Skills</b></p> <ul style="list-style-type: none"> <li>• Commitment</li> <li>• Team work</li> <li>• Learning competence</li> <li>• Learning motivation</li> <li>• Motivation</li> <li>• Literacy (scientific)</li> <li>• Self-monitoring</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input checked="" type="checkbox"/> Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Software with documentation, presentation</td> <td></td> <td style="text-align: center;">75%, 25%</td> </tr> </tbody> </table> <p>Im Modul Projektgruppe sind regelmäßig Berichte über den Arbeitsfortschritt zu erstellen. Außerdem ist die erfolgreiche Bearbeitung von Projekten durch die Abgabe von Software und Dokumentation nachzuweisen. Die Ergebnisse der Projektarbeiten sind in einer Präsentation vorzustellen. Es wird eine Note für die Gesamtheit der Teilleistungen vergeben. Die Softwareprojekte mit Dokumentation bilden 75% der Modulnote, das Referat bildet 25% der Modulnote.</p>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Software with documentation, presentation		75%, 25%
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a)	Software with documentation, presentation		75%, 25%						
7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of achievement</th> <th style="text-align: center;">Duration or Scope</th> <th style="text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Progress reports or presentations</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table> <p>Within the first three weeks of the lecture period each respective lecturer will specify the manner in which the course achievement will be conducted.</p>	zu	Type of achievement	Duration or Scope	SL / QT	a)	Progress reports or presentations		CA
zu	Type of achievement	Duration or Scope	SL / QT						
a)	Progress reports or presentations		CA						
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Prerequisite for the participation in the module final examination is the passing of the academic achievement on the course "Project Group".</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4)</p>								

## 2 Compulsory Area

12	<b>Module coordinator:</b> Prof. Dr. Marco Platzner
13	<b>Other Notes:</b> <i>Remarks of course Projektgruppe:</i> <b>Implementation method</b> <ul style="list-style-type: none"><li>• Developing knowledge on the selected systematic approaches, methods and tools relevant to the research topic, usually done in an introductory seminar phase.</li><li>• Logical assigning “jobs” (assigning responsibilities to the individual group members).</li><li>• Discovering and promoting the participants’ special individual talents, which are either already apparent or which can be developed throughout the project - such as through seminar presentations or appropriate job assignments.</li><li>• Setting up a process-oriented personnel structure, similar to the structure of an industrial design team; delegating subtasks to smaller subgroups who report their findings.</li><li>• Regular progress reports made by individuals and subgroups.</li><li>• Writing a highly distributed interim report and final report.</li></ul> <b>Learning Material, Literature</b> Depending on the topic.

## 2 Compulsory Area

Wissenschaftliches Arbeiten						
Scientific Work Style						
<b>Module number:</b> M.048.42941	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de	<b>Semester number:</b> 2. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.90801 Languages, Writing and Presentation Techniques		30	30	C	15
	b) Seminar (CE)	S2	30	90	C	15
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Sprachen, Schreib- und Präsentationstechnik:</i> None <i>Prerequisites of course Seminar (CE):</i> <b>Recommended Proficiencies</b> Depending on the seminar topic.					
4	<b>Contents:</b> <i>Contents of the course Sprachen, Schreib- und Präsentationstechnik:</i> Depending on their previous knowledge and interest, students choose a course from the range of courses offered by the University of Paderborn in the field of modern languages, scientific writing or presenting scientific topics. <i>Contents of the course Seminar (CE):</i> A seminar is intended for in-depth, independent familiarization with a complex scientific issue, the necessary literature research, and the presentation of the results in spoken and written form. It also helps to familiarize students with the essential mechanisms of the scientific community (conferences, reviewing principles, ...). Seminars are offered by all lecturers; topics change from semester to semester and originate from the research area of the respective lecturer.					

## 2 Compulsory Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The goal of this module is to enable students to autonomously familiarize themselves with complex technical and scientific material and to effectively and efficiently communicate such material in speech and writing. To this end, the module comprises a seminar on scientific topics from computer engineering and an elective class on language, technical writing, presentation techniques, etc.</p> <ul style="list-style-type: none"> <li>• Commitment and dedication</li> <li>• Cooperation competence</li> <li>• learning competence</li> <li>• media competence</li> <li>• Writing and reading competence (scientific)</li> </ul>														
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td>a) - b)</td> <td>Presentation</td> <td>30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a) - b)	Presentation	30 min	100%				
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of achievement</th> <th style="width: 20%;">Duration or Scope</th> <th style="width: 20%;">SL / QT</th> </tr> </thead> <tbody> <tr> <td>a)</td> <td></td> <td></td> <td>QP</td> </tr> <tr> <td>b)</td> <td></td> <td></td> <td></td> </tr> </tbody> </table> <p>Qualified participation in the course a) of the module according to § 39 Special Regulations. Details on the form and scope or duration will be announced by the instructor within the first three weeks of the lecture period at the latest.</p>			zu	Type of achievement	Duration or Scope	SL / QT	a)			QP	b)			
zu	Type of achievement	Duration or Scope	SL / QT												
a)			QP												
b)															
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>														
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after passing the module examination (MAP) and providing proof of the qualified participation.</p>														
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>														
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4)</p>														
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Katrin Temmen</p>														

## 2 Compulsory Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Sprachen, Schreib- und Präsentationstechnik:</i> Registration Note: For the course “Languages, Writing and Presentation Techniques”, please proceed as follows for pragmatic reasons: Select a course from the overall university course program matching the conditions specified in the module handbook, obtain a written confirmation of your successful participation and pass this proof on to me (letterbox next to room P1.6.09.2 or pdf-file to <a href="mailto:Katrin.Temmen@upb.de">Katrin.Temmen@upb.de</a>) before start of</p> <ul style="list-style-type: none"><li>• Winter semester: by 31 March or</li><li>• Summer semester: by 30 September. I will then have this registered in PAUL. Please ensure that besides your matriculation number the respective module (Bachelor v2: L.048.90802 / M.079.01116; Bachelor v3 &amp; v3b: L.048.90802 / M.079.01209; Master v3: L.048.90801 / M.048.42941) is also mentioned on the proof of registration. Katrin Temmen</li></ul> <p><i>Remarks of course Seminar (CE):</i> <b>Implementation method</b> Seminars are based on a list of given topics from which students can make a selection. After a topic is assigned, there are usually a few appointments to discuss literature research, literature selection, presentation technique, technical writing, etc. At the same time, students begin the literature search. In constant interaction with the supervisor and the other seminar participants, a seminar paper and a presentation are developed through some milestones, which are then presented to the group and discussed.</p> <p><b>Learning Material, Literature</b> Scientific publications.</p>
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# 3 Specialisation Area

## 3.1 Specialisation Area “Communication and Networks”

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Specialisation Area	Communication and Networks
Modules	<ul style="list-style-type: none"><li>* Advanced Distributed Algorithms and Data Structures</li><li>* Information Theory</li><li>* Integrated Circuits for Wireless Communications</li><li>* Machine Learning I</li><li>* Optical Communication A</li><li>* Optical Communication B</li><li>* Optical Communication C</li><li>* Optimal and Adaptive Filters</li><li>* Optoelectronics</li><li>* Fast Integrated Circuits for Wireline Communications</li><li>* Theory and Design of Phase-locked Loops</li><li>* Topics in Signal Processing</li><li>* Web Security</li><li>* Wireless Communications</li></ul>
Catalogue advisor	Hellebrand, Sybille, Prof. Dr.
Credits ECTS	6
Learning objectives	

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The modules from this specialisation area enable specialisation in the field of communication and networks.

<b>Advanced Distributed Algorithms and Data Structures</b>
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Advanced Distributed Algorithms and Data Structures
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### 3 Specialisation Area

<b>Module number:</b> M.079.4006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05700 Advanced Distributed Algorithms and Data Structures	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Advanced Distributed Algorithms and Data Structures:</i> <b>Recommended Proficiencies</b> Algorithms and data structures, distributed algorithms and data structures					
4	<b>Contents:</b>  <i>Contents of the course Advanced Distributed Algorithms and Data Structures:</i> After a short introduction of the foundations of graph and network theory as well as distributed programs, the lecture presents advanced methods in the area of distributed algorithms and data structures. Topics covered in the course are access control, synchronization, consensus, information dissemination, hybrid networks, scheduling, and optimization. In addition to presenting solutions to these topics, also concrete applications will be presented. The lecture gives an introduction to state-of-the-art advanced distributed algorithms and data structures. In addition to the presentation of the corresponding protocols, their correctness and efficiency will be shown in a rigorous way. The lecture is structured as follows:  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations of graph and network theory</li> <li>• Access control</li> <li>• Synchronization</li> <li>• Consensus</li> <li>• Information dissemination</li> <li>• Hybrid networks</li> <li>• Scheduling</li> <li>• Optimization</li> </ul> In addition to presenting solution to these topics, also concrete applications will be presented.					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply basic analytical techniques,</li> <li>• explain and use basic algorithmic approaches,</li> <li>• judge which effects these approaches have, and</li> <li>• know the limits of using these approaches.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of achievement</th> <th style="width: 20%;">Duration or Scope</th> <th style="width: 20%;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Christian Scheideler</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Distributed Algorithms and Data Structures:</i></p> <p><b>Implementation Method</b></p> <p>The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Slides of the lecture; exercise sheets</li> <li>• Additional literature will be announced in the course</li> </ul>										

### 3 Specialisation Area

Information Theory							
Information Theory							
<b>Module number:</b> M.048.24021	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2.-4. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.24021 Information Theory	2L 2Ex, SS	60	120	CE	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Information Theory:</i> **Recommended:* Prior knowledge from the modules Signal and Information Transmission, and Probability for Engineers.						
4	<b>Contents:</b>  <i>Contents of the course Information Theory:</i> Information theory is the mathematical foundation of modern communication, data compression, and statistical inference. Originally developed by Claude Shannon, information theory quantifies information, uncertainty, and the fundamental limits of data transmission and storage. This course provides a first rigorous introduction to information theory. Students will learn the core concepts of entropy, mutual information, relative entropy, and typicality, and understand their operational meaning in compression and communication systems. The course covers lossless and lossy source coding, channel capacity, and the fundamental limits of reliable communication over noisy channels. Classical results such as Shannon's source coding theorem and channel coding theorem are derived from first principles. Strong emphasis will be placed on an intuitive understanding of mathematical concepts and the practical interpretation of these results in the context of real-world systems. Beyond communication systems, the course highlights connections to machine learning, statistics, signal processing, and networked systems.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to</p> <ul style="list-style-type: none"> <li>• explain fundamental questions, models, tools, and results of Shannon's information theory;</li> <li>• apply information-theoretic measures to assess the quality processing blocks of communication systems and explain the operational meaning;</li> <li>• explain the relationship between abstract communication-theoretic models and realistic physical systems and apply them purposefully in system design;</li> <li>• understand scientific documents in information theory.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can abstract complex problems and decompose them into solvable subproblems;</li> <li>• are able to understand and apply mathematics as a universal language for describing the physical world;</li> <li>• can transfer information-theoretic concepts and results to problems beyond the field of communications engineering.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Bho Matthiesen</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/nt">https://www.hni.uni-paderborn.de/en/nt</a></p> <p><b>Implementation</b> Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b> Handouts and tutorial questions; literature references will be given in the first lecture</p>
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### 3 Specialisation Area

<b>Integrierte Schaltungen für die drahtlose Kommunikation</b>						
Integrated Circuits for Wireless Communications						
<b>Module number:</b> M.048.25017	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
<b>1</b>	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25017 Integrated Circuits for Wireless Communications	2L 2Ex, SS	60	120	C	40/40
<b>2</b>	<b>Options within the module:</b> None					
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Integrierte Schaltungen für die drahtlose Kommunikation:</i> <b>Recommended:</b> Lecture Schaltungstechnik resp. Circuit and System Design. Helpful supplement: Lecture "Wireless Communications" of Prof. Hab-Umbach.					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Mobile communications, wireless networks, and RFID technology are application examples of wireless communications. Wireless communications has found widespread use in everyday life and will become even more important in the future. The design of electronic circuits for radio frequencies requires a good system knowledge with respect to typical transmitter and receiver architectures in wireless communications, components, and radio signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to convey a methodical approach to the design of integrated circuits for wireless communications. A part of the exercises will pertain to calculation of circuit design problems another will be performed in small teams as a hands-on exercise using modern IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of radio frequency integrated circuits for wireless communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures “Schaltungstechnik” resp. “Circuit and System Design”. The following topics will be addressed:</p> <ul style="list-style-type: none"> <li>• Transmitter and receiver architectures for wireless communications</li> <li>• System Theory Basics             <ul style="list-style-type: none"> <li>– Signals and noise</li> <li>– Modulation and demodulation</li> <li>– Transmission properties of wireless communications systems</li> </ul> </li> <li>• Semiconductor technologies and integrated high-frequency devices</li> <li>• Amplifiers (low-noise and variable-gain amplifiers)</li> <li>• Mixers</li> <li>• Oscillators</li> <li>• Frequency synthesizer PLLs</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>The students will be able</p> <ul style="list-style-type: none"> <li>• to describe architectures and circuits of wireless communication systems</li> <li>• to describe and calculate fundamental signal transmission properties of wireless systems</li> <li>• to apply design methods to design components of radio frequency ICs</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)                  <input type="checkbox"/>Module exam (MP)                  <input type="checkbox"/>Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Oral Examination</td> <td style="text-align: center;">30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Oral Examination	30-45 min	100%
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a)	Oral Examination	30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								

### 3 Specialisation Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. J. Christoph Scheytt</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Course Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lecture with Powerpoint presentation and handwritten mathematical derivations using tablet and beamer</li> <li>• Exercises partly as handwritten calculation exercises using tablet and beamer and partly as practical IC design exercises using IC design software</li> </ul> <p><b>Teaching Material, Literature</b></p> <p>Lecture slides and videos as well as exercise slides will be made available.</p> <ul style="list-style-type: none"> <li>• Behzad Razavi "RF Microelectronics", Prentice Hall, 2011</li> <li>• Thomas Lee "The Design of CMOS Radio-Frequency Integrated Circuits", Cambridge University Press 2003</li> </ul>

### 3 Specialisation Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)                <input type="checkbox"/>Module exam (MP)                <input type="checkbox"/>Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 20%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td style="text-align: center;">90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 20%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismail, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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### 3 Specialisation Area

Optical Communication A							
Optical Communication A							
<b>Module number:</b> M.048.92019	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92019 Optical Communication A	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication A:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication A:</i> <b>Short Description</b> The lecture Optical Communication A gives basic knowledge in Optical Communication and the components used in this field. <b>Contents</b> Maxwell's equations, wave propagation, polarization, dielectric slab and cylindrical waveguides, dispersion, laser, photodiodes, optical amplifiers, modulation, signal formats, optical receivers, noise, regenerators, wavelength division multiplex. Here the most important knowledge is taught.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication A:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• R. Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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### 3 Specialisation Area

Optical Communication B							
Optical Communication B							
<b>Module number:</b> M.048.92020	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92020 Optical Communication B	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication B:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication B:</i> <b>Short Description</b> The lecture Optical Communication B gives some knowledge about mode coupling in Optical Communication and explains the function of many optical components. <b>Contents</b> Mode Coupling: Polarization mode dispersion, moden orthogonality, constant and periodic, co- and counterdirectional mode coupling, profiles of differential group delay, electrooptic effect. The function of many passive and active optical elements is thereby explained, among others amplitude and phase modulators, broadband and wavelength-selective couplers, Bragg gratings, polarization-maintaining fibers, polarization transformers, equalizers for polarization mode dispersion and chromatic dispersion.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication B:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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### 3 Specialisation Area

Optical Communication C							
Optical Communication C							
<b>Module number:</b> M.048.92021	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92021 Optical Communication C	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication C:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication C:</i> <b>Short Description</b> The lecture Optical Communication C gives knowledge in various optical modulation and demodulation techniques. <b>Contents</b> Modulation Formats: Data transmission by differential binary and quaternary phase shift keying in the presence of optical amplifiers, polarization division multiplex, coherent optical data transmission, synchronous and asynchronous demodulation, coherent baseband receivers, polarization diversity, electronic compensators of optical distortions like electronic polarization control and electronic compensation of polarization mode dispersion and chromatic dispersion, phase noise, other modulation formats. Advanced modulation formats are an important possibility for the upgrading of high-performance optical information transmission systems.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b>          After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b>          The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication C:</i></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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### 3 Specialisation Area

Optimale und Adaptive Filter						
Optimal and Adaptive Filters						
<b>Module number:</b> M.048.24010	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24010 Optimal and Adaptive Filters	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Optimale und Adaptive Filter:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics and Digital Signal Processing.					

4	<p><b>Contents:</b></p> <p><i>Contents of the course Optimale und Adaptive Filter:</i></p> <p><b>Short Description</b></p> <p>The course “Optimal and adaptive filters” gives an introduction to the basic techniques and theories of adaptive filters. Based upon the basics of estimation theory optimal filters are discussed. Subsequently the topics Wiener filter theory, deterministic optimization under constraints and stochastic gradient methods are regarded. Concluding the Least Squares approach for solving filter tasks and the Kalman filter are introduced. The latter is regarded as a brief introduction to state based filters.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Classic parameter estimation</li><li>• Estimators</li><li>• MMSE-Estimation</li><li>• Linear estimators</li><li>• Orthogonality principle</li><li>• Evaluation of estimators</li><li>• Wiener filter</li><li>• Wiener-Hopf equation</li><li>• AR- and MA processes</li><li>• Linear prediction</li><li>• Iterative optimization methods</li><li>• Gradient ascent/descent</li><li>• Newton method</li><li>• Linear adaptive filters</li><li>• LMS algorithm</li><li>• Least-Squares method</li><li>• Blockwise and recursive adaptiv filters</li><li>• Realization aspects</li><li>• Statemodel based filters</li><li>• Kalman filter</li><li>• Applications</li><li>• System identification</li><li>• Channel estimation and equalization</li><li>• Multi-channel speech signal processing</li><li>• Noise and interference suppression</li></ul>
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### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• analyze task on the field of adaptive filters and to formulate requirements mathematically,</li> <li>• develop filter using cost functions and</li> <li>• implement selected adaptive filters in the frequency or time domain.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to check theoretical results using practical realizations,</li> <li>• are able to undertake theoretical approaches a systematic analysis using methodical procedures and</li> <li>• are, due to the precise treatment of the contents, in a position to continue their learning themselves.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Jörg Schmalenströer</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optimale und Adaptive Filter:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/optimal-and-adaptive-filter">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/optimal-and-adaptive-filter</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures using the blackboard and presentations,</li><li>• Alternating theoretical and practical exercises classes with exercise sheets and computer and</li><li>• Demonstration of real technical systems in the lecture hall.</li></ul> <p><b>Teaching Material, Literature</b> Allocation of a script; information on textbooks; matlab scripts</p>
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### 3 Specialisation Area

Optoelectronics							
Optoelectronics							
<b>Module number:</b> M.048.26011	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.26011 Optoelectronics	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optoelectronics:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optoelectronics:</i> <b>Short description</b> The lecture Optoelectronics covers the fundamental aspects of optoelectronic devices, starting with semiconductor materials and their interaction with light and photons, to the electronic aspects of the components, and finally to the use of quantum mechanical effects to optimise modern components for their respective areas of application, such as in lighting systems, renewable energy, broadband optical communication systems or in medical technology. <b>Contents</b> In the first part of the lecture, the basics of semiconductors (lattice structure, band structure, direct-indirect semiconductors, doping, degenerate and non-degenerate semiconductors, heterostructures, quantum effects in low-dimensional semiconductors) are recapitulated. The elementary interactions between light and semiconductors (absorption, stimulated emission, spontaneous emission) and the electronic aspects of the components (p-n junction, heterojunctions) are then covered. Finally, the most important devices such as solar cells, photodiodes, light-emitting diodes and semiconductor lasers are discussed in detail and their most important parameters and optimisation strategies are explained.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• explain the basic physical properties of optoelectronic semiconductor devices based on classical and fundamental quantum mechanical descriptions,</li> <li>• to describe the main concepts of optoelectronic semiconductor devices (photodiodes, solar cells, light emitting diodes, semiconductor lasers),</li> <li>• categorize different device designs according to their application requirements.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can use of methodic knowledge for systematic problem analysis for a wide range of disciplines,</li> <li>• will be in position to familiarise themselves independently with new generations of semiconductor devices, thanks to the comprehensive fundamental training received,</li> <li>• get familiar to rate-equation models to simulate steady-state and dynamic characteristics in coupled systems,</li> <li>• and gain foreign language competences related to the field.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								

### 3 Specialisation Area

12	<b>Module coordinator:</b> Prof. Dr.-Ing. Nils Christopher Gerhardt
13	<b>Other Notes:</b> <b>Module Homepage</b> to be announced at the start of the lecture <b>Implementation</b> Lectures and exercises (including some computer simulations) <b>Teaching Material, Literature</b> Lecture notes and handouts for the tutorial; literature references will be given in the first lecture

### 3 Specialisation Area

Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation						
Fast Integrated Circuits for Wireline Communications						
<b>Module number:</b> M.048.25019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25019 Fast Integrated Circuits for Wireline Communications	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Recommended:</b> Module "Schaltungstechnik" of the Bachelor Electrical Engineering or module "Circuit and System Design" of the Master "Electrical Systems Engineering" or comparable modules / lectures					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Nowadays commercial fiber-optic communication systems reach very high data rates of 100 Gb/s per optical channel and several Tb/s in a single fiber. In a similar way very high data rates of more than 10 Gb/s occur at a single package pin of electronic chips. These signals are to be transmitted over printed circuit boards and inexpensive serial cables. In the future the progress of CMOS technology and communication technology will push speed of fiber-optic and wire-line communication continuously to ever higher data rates. The design of electronic circuits for high bandwidth resp. data rates requires a good system knowledge with respect to typical transmitter and receiver architectures, components, and signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to enable the student to utilize a methodological approach for the design of fast integrated electronic circuits for digital wired communications. A part of the exercises will be carried out using modern industry-standard IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of fast integrated electronic circuits for digital broadband communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures "Schaltungstechnik" resp. "Circuit and System Design". The lecture deals with:</p> <ul style="list-style-type: none"><li>• Transmitter and receiver architectures for fiber-optic communications</li><li>• Transmitter and receiver architectures for chip-to-chip communications</li><li>• System design</li><li>• Semiconductor technology and integrated high-frequency devices</li><li>• Broadband amplifiers</li><li>• Current-mode logic</li><li>• Transmitter and receiver circuits</li><li>• PLLs for frequency synthesis and clock recovery</li><li>• Measurement methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>The student will be able to:</p> <ul style="list-style-type: none"><li>• describe and analyze transmitter and receiver architectures for broadband communication links</li><li>• understand and describe semiconductor technologies and integrated high-frequency devices for broadband circuits</li><li>• to analyze circuit design techniques for transmitter and receiver circuits and describe ways to optimize them</li><li>• to describe circuits in PLL technique for frequency synthesis and clock recovery</li><li>• to describe measurement methods</li></ul> <p><b>Key qualifications:</b></p> <p>The students will learn how different interdisciplinary scientific domains and their methods - like mathematical signal and system analysis, non-linear and linear circuit analysis, semiconductor physics, semiconductor devices and high-frequency engineering - are applied together for the development of communications application.</p>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Oral Examination	30-45 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. J. Christoph Scheytt		
13	<b>Other Notes:</b> <i>Remarks of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Course Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/</a> <b>Implementation</b> Lecture with Exercises (including computer-aided design using electronic design software) <b>Teaching Material, Literature</b> Handouts and literature references will be given in the lecture. <ul style="list-style-type: none"> <li>• E. Säckinger, "Broadband Circuits for Optical Fiber Communication", Wiley, 2005</li> <li>• B. Razavi, "Design of Integrated Circuits for Optical Communications", McGraw-Hill, 2003</li> </ul> <b>Comments</b> As part of the lecture a 2-day excursion to IHP Leibnizinstitute for High-Performance Microelectronics in Frankfurt (Oder) is offered which includes the visit of a modern chip fabrication facility (participation in the excursion is voluntary).		

### 3 Specialisation Area

<b>Theory and Design of Phase-locked Loops</b>							
Theory and Design of Phase-locked Loops							
<b>Module number:</b> M.048.24020	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.24020 Theory and Design of Phase-locked Loops	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Theory and Design of Phase-locked Loops:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• Advanced Signal Theory</li> <li>• Statistical Signal Processing (or another course with comparable syllabus in their bachelor)</li> <li>• Circuit and system design (or another course with comparable syllabus in their bachelor)</li> </ul>						

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Theory and Design of Phase-locked Loops:</i></p> <p>Chapter 1: Motivation</p> <ul style="list-style-type: none"> <li>• Time and frequency definition – definition in SI units – static frequency error</li> <li>• random fluctuations – Amplitude/Phase (AM/PM) noise</li> </ul> <p>Chapter 2: Mathematical formalism of signals</p> <ul style="list-style-type: none"> <li>• baseband and bandpass signals</li> <li>• time and frequency domain</li> </ul> <p>Chapter 3: introduction to random processes</p> <ul style="list-style-type: none"> <li>• baseband random processes and noise – correlation functions in time and frequency domain – some basedband random processes (thermal noise, shot noise, flicker noise . . .)</li> <li>• bandpass random processes – correlation functions – relation to baseband processes – phase noise and amplitude noise</li> </ul> <p>Chapter 4: PLL building blocks</p> <ul style="list-style-type: none"> <li>• Phase detector – Phase detector model – phase noise of phase detector</li> <li>• VCO – VCO model – phase noise of VCO</li> <li>• Frequency translators – frequency divider – frequency multiplier – phase noise of frequency translators</li> <li>• transistor level design of PLL blocks</li> </ul> <p>Chapter 5: Integer N PLLs: – Time domain – frequency domain – phase noise – spurious frequencies</p> <p>Chapter 6: Fractional PLLs – Time domain – frequency domain – phase noise – spurious frequencies</p>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Understanding of static/dynamic error in frequency standards. Mathematical modeling of amplitude/phase noise. Modeling random processes in time and frequency domain. Basic and modern PLL architecture and its building blocks. Systematic design of PLLs. Design and Modeling of Integer/Fractional N PLL.</p>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								

### 3 Specialisation Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr. Meysam Bahmanian</p>
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls">https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>

### 3 Specialisation Area

Topics in Signal Processing							
Topics in Signal Processing							
<b>Module number:</b> M.048.92014	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92014 Topics in Signal Processing	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Topics in Signal Processing:</i> <b>Recommended:</b> Signal and system theory, at least a basic understanding of probability and linear algebra						
4	<b>Contents:</b>  <i>Contents of the course Topics in Signal Processing:</i> <b>Short Description</b> This course covers a selection of current topics in signal processing. One part of this course will follow a regular lecture format, while the other part will require active student participation. <b>Contents</b> This course will first review relevant aspects of linear algebra and probability theory. Then students will learn how to read, analyze, and present recent papers from the signal processing literature.						
5	<b>Learning outcomes and competences:</b>  In this course, students will familiarize themselves with some current research topics in signal processing. They will learn to read and understand scientific publications and to critically evaluate results. Students will develop confidence in their ability to solve mathematical problems of analysis and design. They will be able to apply the principles they have learnt in this course to other areas.						

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> Prof. Dr. Peter Schreier		
13	<b>Other Notes:</b> <i>Remarks of course Topics in Signal Processing:</i> <b>Course Homepage</b> <a href="http://sst.uni-paderborn.de/teaching/courses/">http://sst.uni-paderborn.de/teaching/courses/</a> <b>Implementation</b> Lectures and tutorials with active student participation, student presentations <b>Teaching Material, Literature</b> References will be given in the first lecture.		

### 3 Specialisation Area

Web Security							
Web Security							
<b>Module number:</b> M.079.4073	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05809 Web Security	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Web Security:</i> <b>Recommended Proficiencies</b> Knowledge in programming, IT security and basic knowledge in cryptography						

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Web Security:</i></p> <p>Modern web applications and web services usually consist of multiple layers. They are based on different (often complex) technologies that are constantly being developed. Their complexity is often the reason for new types of attacks that can be observed on the web every day.</p> <p>In this lecture, we will focus on the most important technologies and learn what you have to consider while securing your web applications. We will introduce prominent and widespread attacks and show how to prevent them. These range from typical attacks from the OWASP Top 10 list, such as XSS or SQL Injection, to attacks on web services and Single Sign-On standards (e.g., on SAML and OpenID Connect). Based on many cases, we will learn what is important in the design and implementation of secure web applications.</p> <p>The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• Introduction to web technologies</li> <li>• Web Attacks             <ul style="list-style-type: none"> <li>– Cross-Site Scripting (XSS)</li> <li>– Cross-Site Request Forgery (CSRF)</li> <li>– Clickjacking</li> <li>– SQL injection</li> </ul> </li> <li>• XML and SAML             <ul style="list-style-type: none"> <li>– Attacks on XML parsers</li> <li>– Attacks on XML Signature</li> </ul> </li> <li>• JSON and OpenID Connect (OIDC)             <ul style="list-style-type: none"> <li>– Attacks on OIDC</li> </ul> </li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• Understand security concepts behind web applications</li> <li>• Understand and prevent common attacks on web applications</li> <li>• Carry out practical analyses of web applications with common tools</li> <li>• Identify and assess implementation errors and security problems in web applications</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP)</p> <table border="1" data-bbox="279 1590 1420 1803"> <thead> <tr> <th data-bbox="279 1590 359 1691">zu</th> <th data-bbox="359 1590 901 1691">Type of examination</th> <th data-bbox="901 1590 1125 1691">Duration or scope</th> <th data-bbox="1125 1590 1420 1691">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td data-bbox="279 1691 359 1803">a)</td> <td data-bbox="359 1691 901 1803">Written or oral examination or report</td> <td data-bbox="901 1691 1125 1803">120-180 min or 30-45 min or 30 min</td> <td data-bbox="1125 1691 1420 1803">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%						

### 3 Specialisation Area

7	<b>Study Achievement:</b>			
	zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
	a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Juraj Somorovsky			
13	<b>Other Notes:</b> <i>Remarks of course Web Security:</i> <b>Implementation method:</b> The topics are conveyed through lecture presentations. They are further deepened through individual practical tasks. <b>Learning Material, Literature:</b> <ul style="list-style-type: none"> <li>• Lecture slides and exercise sheets</li> <li>• Scientific literature</li> <li>• Additional literature will be announced in the course.</li> </ul>			

### 3 Specialisation Area

Wireless Communications						
Wireless Communications						
<b>Module number:</b> M.048.24022	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2.-4. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.24022 Wireless Communications	2L 2Ex, SS	60	120	CE	30 / 30
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> <i>Prerequisites of course Wireless Communications:</i> <b>Recommended:</b> Prior knowledge from the modules Signal and Information Transmission, Signal Theory, and Probability for Engineers.					
4	<b>Contents:</b> <i>Contents of the course Wireless Communications:</i> The course provides students with important basic knowledge necessary for understanding the transmission concepts of modern mobile communication systems. Starting with a physical understanding of the mobile communications channel, in particular the phenomena of multipath propagation and the Doppler effect, the physical and statistical modeling of time-varying communication systems is presented. Subsequently, important diversity techniques for channel-adaptive transmission and multiple access techniques are introduced.					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to</p> <ul style="list-style-type: none"> <li>• understand the fundamental propagation mechanisms in mobile radio channels;</li> <li>• describe and apply physical and statistical modeling principles to time-varying communication channels;</li> <li>• analyze fading processes and assess their impact on transmission reliability;</li> <li>• explain diversity techniques, apply them to design physical layer signal processing algorithms, and assess their impact on transmission reliability;</li> <li>• explain, compare, and evaluate common multiple access schemes;</li> <li>• assess transmission concepts employed in contemporary mobile communication systems;</li> <li>• connect theoretical channel models with practical system design considerations.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• have strong analytical and mathematical reasoning skills;</li> <li>• have the ability to abstract complex physical phenomena into engineering models;</li> <li>• can interpret analytical results at the system level;</li> <li>• have experience in clear and precise technical communication (oral and written)</li> <li>• can learn independently and engage critically with technical literature.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5)</p>								

### 3 Specialisation Area

12	<b>Module coordinator:</b> Dr.-Ing. Bho Matthiesen
13	<b>Other Notes:</b> <b>Module Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/nt">https://www.hni.uni-paderborn.de/en/nt</a> <b>Implementation</b> Lectures and exercises <b>Teaching Material, Literature</b> Lecture slides and reading assignments for each lecture; additional reading suggestions.

### 3.2 Specialisation Area “Computer Systems”

Specialisation Area	Computer Systems
Modules	<ul style="list-style-type: none"> <li>* Advanced VLSI Design</li> <li>* Algorithms and Tools for Test and Diagnosis of Systems on a Chip</li> <li>* Approximate Computing</li> <li>* Introduction to Quantum Computation</li> <li>* Machine Learning I</li> <li>* Reconfigurable Computing</li> <li>* VLSI Testing</li> </ul>
Catalogue advisor	Hellebrand, Sybille, Prof. Dr.
Credits ECTS	6
Learning objectives	

The modules from this specialisation area enable specialisation in the field of computer systems. The focus is on the analysis and evaluation of computer architectures, systematic methods for the design and optimisation of computer systems, in particular the interaction of hardware and software, as well as programming models and methods for the parallel and specialised computer architectures that are gaining strongly in importance.

Advanced VLSI Design						
Advanced VLSI Design						
<b>Module number:</b>	<b>Workload (h):</b>	<b>Credits:</b>	<b>Regular Cycle:</b>			
M.048.92043	180	6	summer term			
<b>Language:</b>	<b>Semester number:</b>	<b>Duration (in sem.):</b>	<b>Module status (P=C/WP=CE)</b>			
en	1.-3. Semester	1	WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.92043 Advanced VLSI Design	2L 2Ex, SS	60	120	C	30/30

### 3 Specialisation Area

2	<p><b>Options within the module:</b></p> <p>None</p>
3	<p><b>Admission requirements:</b></p> <p>None</p> <p><i>Prerequisites of course Advanced VLSI Design:</i></p> <p><b>Recommended:</b> Fundamentals of Digital Circuits / Fundamentals of VLSI Design</p> <p>Information: Unless otherwise specified, these are recommendations.</p>
4	<p><b>Contents:</b></p> <p><i>Contents of the course Advanced VLSI Design:</i></p> <p><b>Short Description</b></p> <p>The course provides basic knowledge about the modern application-oriented modeling, simulation, analysis, and synthesis of digital systems at different abstraction levels to chip layout.</p> <p><b>Contents</b></p> <p>In today's practice, chip design consists of the combined application of various languages, methods, and tools for the modeling, simulation, and synthesis of electronic circuits. Along the modern abstraction-based design flow of digital systems (electronic system level to chip layout), the course provides basic knowledge of the main description languages and their application in modeling, simulation, analysis and synthesis. This includes basic principles and application of the IEEE standard system/hardware description languages SystemVerilog, SystemC, Verilog, and VHDL, in conjunction with additional formats, e.g., SDF and UPF for time and power annotation. For their application, the fundamental principles of test environments for simulation, timing and power analysis, logic synthesis and physical design of digital circuits. Exercises will provide hands-on labs based on commercial tools from Mentor Graphics, Synopsys and, Cadence Design Systems.</p>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After the course students are able</p> <ul style="list-style-type: none"> <li>• to model, simulate, analyze and synthesize simple digital circuits at different abstraction levels and</li> <li>• to apply the most important commercial tools for simulation, analysis and synthesis of digital circuits.</li> </ul> <p><b>Key qualifications:</b></p> <p>After the course students are able</p> <ul style="list-style-type: none"> <li>• to assess, select and apply modern digital circuit description languages for their different applications,</li> <li>• apply the different methods and tools in the modern VLSI design.</li> </ul>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)			
12	<b>Module coordinator:</b> apl. Prof. Dr. Wolfgang Müller			
13	<b>Other Notes:</b> <i>Remarks of course Advanced VLSI Design:</i> <b>Course Homepage</b> <a href="http://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design">www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design</a> <b>Implementation</b> * Vorlesung mit Beamer und White-Board * Übungen mit Übungsblättern am Computer * Lecture with LCD projector and white board * Exercises with assignments and hands-on labs <b>Teaching Material, Literature</b> <ul style="list-style-type: none"> <li>• Lecture notes and exercise sheets will be provided via PAUL</li> <li>• IEEE standard reference manuals: IEEE Std 1800/1685/1666/1364/1076/1801/1497</li> <li>• Specific references for individual teaching units</li> </ul>			

### 3 Specialisation Area

Algorithms and Tools for Test and Diagnosis of Systems on a Chip							
Algorithms and Tools for Test and Diagnosis of Systems on a Chip							
<b>Module number:</b> M.048.92007	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92007 Algorithms and Tools for Test and Diagnosis of Systems on a Chip	2L 2Ex, WS+SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Recommended:</b> VLSI Testing, (Introduction to Algorithms)						
4	<b>Contents:</b>  <i>Contents of the course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Short Description</b> The course “Algorithms and Tools for Test and Diagnosis of Systems on Chip” deals with advanced topics in test and diagnosis of integrated systems. The focus is on algorithms and tools for computer-aided preparation and application of test and diagnosis procedures. <b>** Contents**</b> Topics include but are not restricted to: <ul style="list-style-type: none"> <li>• Advanced techniques for built-in self-test and embedded test</li> <li>• Built-in diagnosis</li> <li>• Test of robust and self-adaptive systems</li> <li>• Adaptive Testing</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe recent approaches in test and diagnosis,</li> <li>• to explain and apply the underlying models and algorithms,</li> <li>• to explain the specific challenges of nanoscale integration and evaluate test strategies accordingly.</li> </ul> <p><b>Key qualifications:</b> The students are able</p> <ul style="list-style-type: none"> <li>• to apply their basic knowledge for studying and understanding new approaches from the state of the art literature,</li> <li>• to present the new contents in a conference style presentation, and</li> <li>• to describe the new contents in a scientific manuscript.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Sybille Hellebrand</p>										

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i></p> <p><b>Module Homepage</b> <a href="http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Self-study on recent approaches based on recent conference and journal publications</li><li>• Oral presentation</li><li>• Manuscript</li></ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides</li><li>• Additional material can be found in panda</li><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Kluwer Academic Publishers,2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li><li>• Artikel aus Fachzeitschriften und Konferenzbänden / Articles from Journals and Conference Proceedings (e.g. IEEE Transactions on Computers, IEEE Transactions on CAD of Integrated Circuits and Systems, IEEE International Test Conference, etc.)</li></ul>
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### 3 Specialisation Area

<b>Approximate Computing</b>						
Approximate Computing						
<b>Module number:</b> M.079.4068	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.09758 Approximate Computing	L3 Ex2	75	105	C	100/30
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Approximate Computing:</i> <b>Recommended Proficiencies</b> Knowledge of the Bachelor-level courses Digital Design and Computer Architecture are beneficial.					
4	<b>Contents:</b>  <i>Contents of the course Approximate Computing:</i> Approximate Computing is an emerging paradigm that trades-off computational accuracy for a significant reduction in energy, execution time, or chip area. This research-oriented course introduces to the field of Approximate Computing and its most remarkable aspects, and explains the main methods used to implement efficient computing systems by reducing accuracy. The course discusses approximations at all levels of a computing system, from applications down to hardware technologies. In exercise/tutorial sessions the efficiency of these techniques in various domains are examined, including deep learning and digital signal processing.  <ul style="list-style-type: none"> <li>• Introduction and motivation for approximate computing</li> <li>• Approximation at the application level, e.g., in machine learning and digital signal processing</li> <li>• Programming languages/compilers for approximate computing</li> <li>• Approximate microarchitectures</li> <li>• Automated synthesis of approximate circuits</li> <li>• Inexact arithmetic components and performance optimization via accuracy trade-offs</li> <li>• Approximation techniques at the technology level</li> <li>• Exercises/tutorial: Approximating deep learning and digital signal processing algorithms</li> </ul>					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending this course, the students are able</p> <ul style="list-style-type: none"> <li>• to name and explain approximation techniques at all levels of a computing system,</li> <li>• to identify major engineering/research problems when building approximate computing systems,</li> <li>• to judge the suitability of approximation techniques for different application domains, and</li> <li>• to apply approximation techniques to realize efficient hardware accelerators, in particular for deep learning and digital signal processing</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Marco Platzner</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Approximate Computing:</i></p> <p><b>Implementation method</b></p> <ul style="list-style-type: none"><li>• Lecture with projector and black/white board</li><li>• Interactive exercises/discussions in the lecture room</li><li>• Computer-based tutorials</li></ul> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides, exercise sheets, and tutorial assignments</li><li>• Adrian Sampson, Luis Ceze, and Dan Grossman: Good-Enough Computing. <i>IEEE Spectrum</i>, 50(10):54-59, 2013</li><li>• Ravi Nair. Big Data Needs Approximate Computing: Technical Perspective. <i>Communications of the ACM</i>, 58(1): 104, 2015.</li><li>• Sparsh Mittal. A Survey of Techniques for Approximate Computing. <i>ACM Computing Surveys</i>, 48(4), 2016.</li><li>• Qiang Xu, Todd Mytkowitz, and Nam Sung Kim. Approximate Computing: A Survey. <i>IEEE Design &amp; Test</i>, 33(1):8-22, 2016.</li><li>• Weiqiang Liu and Fabrizio Lombardi (Editors), <i>Approximate Computing</i>. Springer, 2022.</li><li>• Additional resources and links to current research papers are provided in the lecture.</li></ul>
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### 3 Specialisation Area

Introduction to Quantum Computation							
Introduction to Quantum Computation							
<b>Module number:</b> M.079.4059	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05706 Introduction to Quantum Computation	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Introduction to Quantum Computation:</i> <b>Recommended Proficiencies</b> Linear Algebra, algorithms						
4	<b>Contents:</b>  <i>Contents of the course Introduction to Quantum Computation:</i> This lecture introduces the fundamental concepts of quantum computation and information from a computer science perspective. This includes an introduction to quantum mechanics, quantum entanglement, quantum algorithms, quantum error correction, and quantum information theory.  <ul style="list-style-type: none"> <li>• Quantum mechanics</li> <li>• Quantum entanglement</li> <li>• Quantum algorithms</li> <li>• Quantum error correction</li> <li>• Quantum information</li> </ul>						
5	<b>Learning outcomes and competences:</b> Students are able to: <ul style="list-style-type: none"> <li>• Describe and apply the postulates of quantum mechanics</li> <li>• Understand the use of entanglement as a resource</li> <li>• Design and analyze fundamental quantum algorithms</li> <li>• Apply the theory of error-correcting codes</li> <li>• Understand and apply basic quantum information theory concepts such as entropy</li> </ul>						

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>	
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%	
7	<b>Study Achievement:</b>			
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>	
a)	Assignments, course paper or progress reports		CA	
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr. Sevag Gharibian			
13	<b>Other Notes:</b> <i>Remarks of course Introduction to Quantum Computation:</i> <b>Implementation method</b> Slides and blackboard writing. All important concepts and techniques are further deepened with examples in exercises. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press</li> <li>• Lecture slides, exercises</li> </ul>			

### 3 Specialisation Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td style="text-align: center;">90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismail, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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### 3 Specialisation Area

<b>Reconfigurable Computing</b>							
Reconfigurable Computing							
<b>Module number:</b> M.079.4043	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	a)	L.079.05703 Reconfigurable Computing	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Reconfigurable Computing:</i> <b>Recommended Proficiencies</b> Knowledge of the Bachelor-level courses Digital Design, Programming, and Data Structures and Algorithms are beneficial.						
4	<b>Contents:</b>  <i>Contents of the course Reconfigurable Computing:</i> The course Reconfigurable Computing introduces into the field of computing with reprogrammable hardware structures. Computing systems built from reprogrammable hardware structures do not rely on a fixed hardware, but adapt their hardware architecture to the application under execution. The field was formed in the early 1990s when Field-programmable Gate Arrays (FPGAs) became commercially available that were powerful enough to be used for computing. Today, FPGA-based high-performance systems have outperformed state-of-the-art computers for many problems including database search, genomic sequence scanning, and cryptography. In embedded systems, FPGAs accelerate system functions, reduce system cost and energy consumption, and enable hardware-on-demand functionality. The course covers the following topics: <ul style="list-style-type: none"> <li>• Introduction to reconfigurable computing</li> <li>• Evolution of programmable hardware devices</li> <li>• FPGA architectures</li> <li>• Computer-aided design for FPGAs</li> <li>• High-level languages for programming FPGAs</li> <li>• Application domains for FPGAs</li> <li>• Comparison of devices, technologies, and reconfigurable systems</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• compare different reprogrammable hardware devices and describe their historical development,</li> <li>• name the design steps and problems when designing with FPGAs,</li> <li>• analyse algorithms for the design steps and apply them to examples,</li> <li>• compare and evaluate current approaches to programming FPGAs,</li> <li>• justify the suitability of different reprogrammable hardware components for different areas of application, and</li> <li>• implement functions of medium complexity with modern FPGA design tools.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Marco Platzner</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Reconfigurable Computing:</i></p> <p><b>Implementation Method</b></p> <p>The course consists of a lecture, and pencil&amp;paper as well as practical exercises. The lecture is held with a beamer and blackboard. In the pencil&amp;paper exercises, problems are handed out and their solutions are presented and discussed in a practice session. In addition, quizzes are offered for self-assessments. In the practical exercises, a tutorial on the design with FPGAs is carried out and then tasks are handed out, which are implemented as design or programming examples in groups of one to three participants.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides, assignment sheets for paper&amp;pencil exercises, quizzes</li><li>• Tutorial, assignment sheets for design and programming examples, technical documentation</li><li>• Selected scientific articles</li><li>• Additional literature will be announced in the course.</li></ul>
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### 3 Specialisation Area

<b>VLSI-Testing</b>							
VLSI-Testing							
<b>Module number:</b> M.048.92027	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	a)	L.048.92027 VLSI Testing	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course VLSI Testing:</i> <b>Recommended:</b> Digital Design						
4	<b>Contents:</b>  <i>Contents of the course VLSI Testing:</i> <b>Short Description</b> The course “VLSI Testing” focuses on techniques for detecting hardware defects in micro-electronic circuits. Algorithms for test data generation and test response evaluation as well as hardware structures for design for test (DFT) and on-chip test implementation (BIST) are presented. <b>Contents</b> In detail the following topics are covered: <ul style="list-style-type: none"><li>• Fault models</li><li>• Testability measures and design for test (DFT)</li><li>• Logic and fault simulation</li><li>• Automatic test pattern generation (ATPG)</li><li>• Built-in self-test (BIST), in particular test data compression and test response compaction</li><li>• Memory test</li></ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe fault models, DFT techniques, and test tools,</li> <li>• to explain and apply the underlying models and algorithms for fault simulation and test generation,</li> <li>• to analyze systems with respect to their testability and to derive appropriate test strategies.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the practiced strategies for problem solving across varying disciplines,</li> <li>• have experience in presenting their solutions to their fellow students, and</li> <li>• know how to improve their competences by private study.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sybille Hellebrand</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course VLSI Testing:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Exercises in small groups based on exercise sheets with students presenting their own solutions</li><li>• Hands-on exercises using various software tools</li></ul> <p><b>Teaching Material, Literature</b></p> <p>Additional material can be found in panda</p> <ul style="list-style-type: none"><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Boston, Dordrecht, London: Kluwer Academic Publishers, 2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li></ul>
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### 3.3 Specialisation Area “Control and Automation”

Specialisation Area	Control and Automation
Modules	<ul style="list-style-type: none"> <li>* Advanced Control</li> <li>* Advanced System Theory</li> <li>* Advanced Topics in Robotics</li> <li>* Coupled Fields</li> <li>* Controlled AC Drives</li> <li>* Control of Grid-Tied Converters</li> <li>* Machine Learning I</li> <li>* Nonlinear control of autonomous and robotic systems</li> <li>* Optimization-Based Control Methods</li> <li>* Reinforcement Learning</li> <li>* Robotics</li> <li>* Ultrasonic measurement technology</li> <li>* Environmental monitoring and measuring technologies</li> </ul>
Catalogue advisor	Hellebrand, Sybille, Prof. Dr.
Credits ECTS	6
Learning objectives	

The modules from this specialisation area enable specialisation in the field of control and automation technology.

<b>Advanced System Theory</b>			
Advanced System Theory			
<b>Module number:</b> M.048.92001	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term
<b>Language:</b> de / en	<b>Semester number:</b> 1. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P

### 3 Specialisation Area

1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92001 Advanced System Theory	2L 2Ex, WS/SS	60	120	C	60/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Advanced System Theory:</i> <b>Recommended:</b> Prerequisites are a basic understanding of differential equations, linear algebra, and Laplace transforms, as they are covered in a typical undergraduate course on system theory.						
4	<b>Contents:</b> <i>Contents of the course Advanced System Theory:</i> <b>Short Description</b> Building on an undergraduate system theory course, this course studies the dynamical behavior of linear systems with greater mathematical rigor. The course is primarily intended to serve students in engineering, but it can also be useful to students in physics and other natural sciences. <b>Contents</b> <ul style="list-style-type: none"> <li>• System models and differential equations</li> <li>• State-space and I/O descriptions</li> <li>• Relations between internal and external descriptions</li> <li>• Response of continuous- and discrete-time systems</li> <li>• Stability, controllability, observability</li> <li>• State-space realizations of external descriptions</li> <li>• Feedback systems</li> </ul>						
5	<b>Learning outcomes and competences:</b> After attending this course, students will be familiar with the most important concepts and results in linear system theory. Students will develop confidence in their ability to solve mathematical problems of analysis and design. Many of their timeless insights and intuitions about the dynamical behavior of systems will be drawn from this course. This course presents material broad enough so that students will have a clear understanding of the dynamical behavior of linear systems, including their power and limitations. This will allow students to apply the theory to other fields.						

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)			
12	<b>Module coordinator:</b> Prof. Dr. Erdal Kayacan			
13	<b>Other Notes:</b> <i>Remarks of course Advanced System Theory:</i> <b>Course Homepage</b> <a href="https://en.ei.uni-paderborn.de/rat">https://en.ei.uni-paderborn.de/rat</a> <b>Implementation</b> Lectures and exercises (including some computer simulations) Panda course for communication and material distribution <b>Teaching Material, Literature</b> Handouts and exercise / tutorial questions; literature references will be given in the first lecture			

### 3 Specialisation Area

<b>Advanced Topics in Robotics</b>							
Advanced Topics in Robotics							
<b>Module number:</b> M.048.92006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	a)	L.048.92006 Advanced Topics in Robotics	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Advanced Topics in Robotics:</i> None						
4	<b>Contents:</b>  <i>Contents of the course Advanced Topics in Robotics:</i> <b>Short Description</b> The course Advanced Topics in Robotics is based on the course Robotics. The students are introduced to current research topics in the field of autonomous and teleoperated mobile robots to solve interdisciplinary issues. The challenges encountered in developing intelligent mobile systems are analyzed and current solutions presented. <b>Contents</b> <ul style="list-style-type: none"><li>• Architectures of robot systems</li><li>• Middleware for hardware abstraction</li><li>• Device drivers and libraries</li><li>• Visualization</li><li>• Local navigation processes (collision avoidance)</li><li>• Global navigation processes (pathfinding)</li><li>• Navigation and self-localization methods (SLAM)</li><li>• Fundamentals of task planning</li></ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to name and analyze the basic robot architectures for mobile robots,</li> <li>• have a good command of the methods for the navigation and control of mobile robots and</li> <li>• are able to implement, test and apply them.</li> </ul> <p><b>Key qualifications:</b> The students have a good command of programming in the C language</p>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Bärbel Mertsching</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Topics in Robotics:</i></p> <hr/> <p><b>ATTENTION - IMPORTANT NOTICE</b> The course doesn't take place in winter term 2024/25. Please see the notice boards of the group.</p> <hr/> <p><b>Course Homepage</b> <a href="http://getwww.uni-paderborn.de/teaching/atir">http://getwww.uni-paderborn.de/teaching/atir</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• The theoretical and methodical fundamentals will be introduced during the lecture.</li><li>• The methods presented will be practiced during the subsequent exercise / lab part.</li><li>• Finally, the participants will implement, test, and apply simple algorithms.</li><li>• The necessary programming skills will be taught during the practical, this is explicitly not considered a programming course.</li></ul> <p><b>Teaching Material, Literature</b> Allocation of lecture notes; information on textbooks stocked in the textbook collection will be announced later.</p> <ul style="list-style-type: none"><li>• Mertsching, Bärbel: Robotics (lecture notes)</li><li>• McKerrow, Phillip J.: Introduction to Robotics. Addison-Wesley, 1991</li><li>• Siegwart, Roland; Nourbakhsh, Illah R. and Scaramuzza, David: Introduction to Autonomous Mobile Robots. The MIT Press, 2011, ISBN-13: 978-0262015356</li></ul>
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### 3 Specialisation Area

Gekoppelte Felder							
Coupled Fields							
<b>Module number:</b> M.048.27028	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.27028 Coupled Fields	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Gekoppelte Felder:</i> <b>Recommended:</b> Basic knowledge from the area of classical field theory, for example from the modules "Field Theory", "Electromagnetic Waves" and "Theoretical Electrical Engineering".						
4	<b>Contents:</b>  <i>Contents of the course Gekoppelte Felder:</i> The focus of the course Coupled Fields is the classical field theory of interacting electromagnetic, thermal and mechanical phenomena as well as their application in sensors and actuators. After an introduction to the mathematical description of the individual fields, the following topics are covered:  <ul style="list-style-type: none"> <li>• Electromechanical coupling based on examples in piezoelectricity, electrostriction and magnetostriction.</li> <li>• Thermomechanical coupling such as thermoelasticity and lossy acoustic waves.</li> <li>• Thermoelectric coupling, for example pyroelectricity.</li> <li>• Phenomena with electromagnetic-thermal-mechanical coupling such as the photoacoustic effect. In addition to the description of the effects, analogies as well as similarities and differences are considered and aspects of numerical simulation are discussed.</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending the course, students will be able to</p> <ul style="list-style-type: none"> <li>• describe the discussed physical effects phenomenologically and with differential equations.</li> <li>• interpret the results of numerical simulations of coupled fields and check them for plausibility.</li> <li>• select suitable components for sensor and actuator applications of coupled fields.</li> <li>• infer an acting physical effect from observations.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table> <p>Within the first three weeks of the lecture period each respective lecturer will specify the manner in which the examination will be conducted.</p>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Leander Claes</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> <a href="https://emt.upb.de">https://emt.upb.de</a></p> <p><b>Implementation</b> Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b> Lecture slides and exercises will be provided. Additional literature references will be given throughout the course.</p>
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### 3 Specialisation Area

Regelung elektrischer Antriebe						
Control of Electric Drives						
<b>Module number:</b> M.048.27013	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.27013 Control of Electric Drives	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Regelung elektrischer Antriebe:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• basics of control theory</li> <li>• basics of electrical machines</li> <li>• basics of power electronics</li> </ul>					
4	<b>Contents:</b>  <i>Contents of the course Regelung elektrischer Antriebe:</i> <b>Short Description</b> The course teaches the closed-loop control for electrical machines (DC machines, synchronous machines, and induction machines). The presented content is tested in simulations within the exercises. <b>Contents</b> <ul style="list-style-type: none"> <li>• machine models (dc machines, synchronous machines, and induction machines)</li> <li>• coordinate transformations</li> <li>• field-oriented control for particular machines</li> <li>• observers</li> <li>• advanced control techniques (direct torque control, model-predictive control)</li> </ul>					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After successfully completing the module, students will be able to</p> <ul style="list-style-type: none"> <li>• explain the functionality of electric drives (based on dc machines, synchronous machines, and induction machines)</li> <li>• set up a simulation model for the target application</li> <li>• design a suitable control system for the particular electric machines and implement it as a simulation model</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Jakub Kucka</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Regelung elektrischer Antriebe:</i></p> <p><b>Implementation</b> Parts of the course are organized as computer-based simulation exercises.</p> <p><b>Teaching Material</b> Lecture slides.</p>										

### 3 Specialisation Area

Geregelte Netzumrichter						
Control of Grid-Tied Converters						
<b>Module number:</b> M.048.22021	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.22021 Control of Grid-Tied Converters	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Geregelte Netzumrichter:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• Basics of Power Electronics (recommended)</li> <li>• Basics of Control Theory (recommended)</li> </ul>					
4	<b>Contents:</b>  <i>Contents of the course Geregelte Netzumrichter:</i> The goal of this module is to develop application-oriented skills regarding the operation of grid-tied converters. The focus is on the operational principles, requirements, and control. <ul style="list-style-type: none"> <li>• grid codes and other requirements</li> <li>• three-phase and single-phase voltage converter topologies for grid operation</li> <li>• modeling the inverter components for simulation purposes</li> <li>• voltage and current transformations utilized for control</li> <li>• the control algorithms for these topologies</li> <li>• different PLLs and their properties</li> </ul>					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After successfully completing the module, students will be able to</p> <ul style="list-style-type: none"> <li>• explain the functionality of grid-tied converters</li> <li>• set up a simulation model for the target application</li> <li>• design a suitable control system for the grid operation of the single-phase and three-phase inverters and implement it as a simulation model</li> <li>• explain essential grid code requirements</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <p>none</p>										
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Jakub Kucka</p>										
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="https://ei.uni-paderborn.de/en/lea/lehre/veranstaltungen/teaching">https://ei.uni-paderborn.de/en/lea/lehre/veranstaltungen/teaching</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>										

### 3 Specialisation Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td style="text-align: center;">90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 25%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismael, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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### 3 Specialisation Area

<b>Nonlinear control of autonomous and robotic systems</b>						
Nonlinear control of autonomous and robotic systems						
<b>Module number:</b> M.048.27032	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.27032 Nonlinear control of autonomous and robotic systems	2L 2Ex, SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Nonlinear control of autonomous and robotic systems:</i> None					
4	<b>Contents:</b>					
5	<b>Learning outcomes and competences:</b> -					
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)					
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>		
		a) Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%		
	The credit points are awarded after the module examination (MAP) was passed. The credit points are awarded after all module examinations (MTP) were passed.					
7	<b>Study Achievement:</b> none					

### 3 Specialisation Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p><b>Module Homepage</b>  <a href="http://sst.upb.de/teaching">http://sst.upb.de/teaching</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The credit points are awarded after all module examinations (MTP) were passed.</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Erdal Kayacan</p>
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="http://sst.upb.de/teaching">http://sst.upb.de/teaching</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>

### 3 Specialisation Area

Optimization-Based Control Methods							
Optimization-Based Control Methods							
<b>Module number:</b> M.048.27031	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.27031 Optimization-Based Control Methods	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optimization-Based Control Methods:</i> None						
4	<b>Contents:</b>						
5	<b>Learning outcomes and competences:</b> -						
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)						
	<b>zu</b>	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>			
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%			
7	<b>Study Achievement:</b> none						
8	<b>Prerequisites for participation in examinations:</b> None						

### 3 Specialisation Area

9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5)
12	<b>Module coordinator:</b> Dr. Adrian Redder
13	<b>Other Notes:</b> <b>Module Homepage</b> <a href="http://sst.upb.de/teaching">http://sst.upb.de/teaching</a> <b>Implementation</b> Lectures and exercises (including some computer simulations) <b>Teaching Material, Literature</b> Handouts and tutorial questions; literature references will be given in the first lecture

### 3 Specialisation Area

Reinforcement Learning						
Reinforcement Learning						
<b>Module number:</b> M.048.92045	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92045 Reinforcement Learning	2L 2Ex, SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Reinforcement Learning:</i> <b>Recommended:</b> It is recommended to have a sound basic knowledge in the field of system and control theory. Ideally, the students have knowledge in the field of un-/supervised machine learning and numerical optimization. In addition, at least some experience with Python will be advantageous for the exercise and tutorial tasks.					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Reinforcement Learning:</i></p> <p>The course covers the basics of reinforcement learning (RL) in an engineering context. RL stands for a series of methods of machine learning in which an agent independently learns a strategy (policy) to maximize the rewards received during interaction with an (unknown) system. This can be, for example, a control loop in which an adaptive controller tries to determine an optimal control law from previous observations of the control and measurement variables, which maximizes certain benchmark criteria with regard to controller performance. Well-known fields of application include the operation of autonomous vehicles and industrial robots or the identification of optimal strategies in the context of leisure games.</p> <p>The course has an application-oriented focus in the engineering sciences but is also designed for students of natural sciences (e.g. computer science, mathematics). In addition to teaching the methodological fundamentals within the lecture, great importance is attached to practical implementation and programming tasks during the exercise and tutorial hours.</p> <p>The course will cover the following content:</p> <ul style="list-style-type: none"><li>• Conceptual basics and historical overview</li><li>• Markov decision processes</li><li>• Dynamic programming</li><li>• Monte Carlo learning</li><li>• Temporal difference learning</li><li>• Bootstrapping</li><li>• Function approximation and deep learning</li><li>• On- and Off-policy strategies</li><li>• Policy gradient methods</li><li>• Trust region methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain-specific competences</b></p> <p>After attending the course, the students are able to</p> <ul style="list-style-type: none"><li>• differentiate, apply and analyze RL methods,</li><li>• name and explain differences as well as advantages and disadvantages of RL compared to neighboring approaches (e.g. model-predictive control),</li><li>• educate themselves independently in this branch of science on the basis of the methods learned for the analysis and synthesis of RL techniques.</li></ul> <p><b>Interdisciplinary competences</b></p> <p>The students</p> <ul style="list-style-type: none"><li>• can apply or transfer the acquired knowledge to interdisciplinary problems,</li><li>• have gained practical experience in programming which they can use across domains and</li><li>• are able to critically evaluate methods and results.</li></ul>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)			
12	<b>Module coordinator:</b> Dr Jarren Lange			
13	<b>Other Notes:</b> <i>Remarks of course Reinforcement Learning:</i> <b>Course homepage</b> <a href="https://en.ei.uni-paderborn.de/rat">https://en.ei.uni-paderborn.de/rat</a> <a href="https://github.com">https://github.com</a> (open-source course material) <b>Implementation</b> <ul style="list-style-type: none"> <li>• Slide-based lecture, which also serves as lecture notes.</li> <li>• Presence exercises with tutorial sheets (with many programming tasks)</li> </ul> <b>Main literature</b> <ul style="list-style-type: none"> <li>• Richard S. Sutton, Andrew G. Barto, „Reinforcement Learning“, 2. Ed., MIT Press, 2018</li> <li>• David Silver, „Reinforcement Learning“ (Skriptum), University College London, 2015</li> </ul>			

### 3 Specialisation Area

Robotics							
Robotics							
<b>Module number:</b> M.048.92012	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92012 Robotics	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Robotics:</i> None						
4	<b>Contents:</b> <i>Contents of the course Robotics:</i> <b>Short Description</b> The course “Robotics” is a fundamental module in the catalog “Cognitive Systems” of the Electrical Engineering Master’s program and related degree programs. It is the first of two courses that cover the relevant concepts and techniques in the field of robot manipulators and mobile robots. This course concentrates on modeling and controlling robot arms, while its successor in the winter semester (Advanced Topics in Robotics (L.048.23020 / L.048.92006) focuses on mobile robots. The challenges for the development of autonomous intelligent systems will be analyzed and the current solutions will be presented. <b>Contents</b> <ul style="list-style-type: none"> <li>• Sensors, effectors, actuators</li> <li>• Homogenous coordinates, general transformations, Denavit-Hartenberg parameters</li> <li>• Kinematics and dynamics of robot arms and mobile robots</li> </ul> After the presentation of methods in the lecture, the students will use Matlab and Octave to implement them.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"> <li>• know how to transfer basic methods from control and system theory to robotics and</li> <li>• are able to apply adequate methods to model as well as plan and control the movements of robot arms.</li> </ul> <p><b>Key qualifications:</b> The students are able to identify and evaluate the function and behavior of robots and their integration into the social and economic environment while also considering ethical aspects.</p>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bärbel Mertsching</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Robotics:</i></p> <p><b>Course Homepage</b> [<a href="http://getwww.uni-paderborn.de/teaching/robotik">http://getwww.uni-paderborn.de/teaching/robotik</a>]</p> <p><b>Course Documents</b> see PANDA ([<a href="https://panda.uni-paderborn.de">https://panda.uni-paderborn.de</a>])</p> <p><b>References (excerpt)</b></p> <ul style="list-style-type: none"><li>• Mertsching, Bärbel: Robotics (lecture notes)</li><li>• McKerrow, Phillip J.: Introduction to Robotics. Addison-Wesley, 1991</li><li>• Lynch, Kevin M. and Park, Frank C.: Modern Robotics: Mechanics, Planning, and Control. Cambridge University Press, 2017. ISBN-13 : 978-1107156302</li></ul>
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### 3 Specialisation Area

<b>Ultraschallmesstechnik</b>							
Ultrasonic measurement technology							
<b>Module number:</b> M.048.27015	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.27015 Ultrasonic Measurement Technology	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Ultraschallmesstechnik:</i> None						
4	<b>Contents:</b>  <i>Contents of the course Ultraschallmesstechnik:</i> <b>Short description</b> The course Ultrasonic Measurement Technology deals with the phenomena of propagation of mechanical waves in solids, liquids and gases. Based on this the most important acoustic measurement principles for the determination of acoustic material parameters, geometric and technical process parameters as well as their application in process and production engineering are described. The application of sound and ultrasound for non-destructive material diagnostics as well as for ultrasonic tomography are covered in detail. <b>Contents</b> The Ultrasonic Metrology lecture covers the following topics: <ul style="list-style-type: none"> <li>• Acoustic and sound field characteristics.</li> <li>• Fundamentals of wave propagation</li> <li>• Ultrasonic sensor design (experimental realization)</li> <li>• Methods for measurement and visualization of ultrasonic fields (needle and membrane hydrophone, schlieren measuring station, laser vibrometry. . .)</li> <li>• Metrological methods for acoustic material data determination (sound velocity, sound characteristic impedance. . .)</li> <li>• Application of ultrasound for non-destructive testing (NDT) and acoustic emission analysis</li> <li>• Application of ultrasound and in process measurement technology (distance, flow, level. . .)</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Specialized competence:</b> After attending the course, students will be able to,</p> <ul style="list-style-type: none"> <li>• use ultrasound to determine acoustic and non-acoustic quantities.</li> </ul> <p><b>Cross-disciplinary competencies:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills across disciplines and to complex problems,</li> <li>• are able to develop targeted solutions on the basis of systematic problem analysis,</li> <li>• are able to familiarize themselves with tangential fields of work due to the method-oriented knowledge transfer.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bernd Henning</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Ultraschallmesstechnik:</i></p> <p><b>Course Homepage</b> <a href="http://emt.upb.de">http://emt.upb.de</a></p> <p><b>Methodical implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with slide presentation of extensive correlations</li><li>• Practical work in groups using measurement techniques in the laboratory</li></ul> <p><b>Learning materials, references</b></p> <ul style="list-style-type: none"><li>• Provision of a script; references to textbooks from the textbook collection will be announced.</li></ul>
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### 3 Specialisation Area

<b>Umweltmesstechnik</b>							
Environmental monitoring and measuring technologies							
<b>Module number:</b> M.048.22010	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22010 Environmental Monitoring and Measuring Technologies	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Umweltmesstechnik:</i> None						
4	<b>Contents:</b> <i>Contents of the course Umweltmesstechnik:</i> <b>Short Description:</b> The ever more intensive use of natural resources is leading to increasing environmental pollution. This course deals with the problems of certain selected impact mechanisms in relation to the impact sites or habitats. The relevant quantities will be characterised and the measurement principles and methods suitable for determining them will be described. In particular, the explanations concentrate on the metrological determination of contamination and monitoring of air, water and soil. <b>Contents:</b> The lecture Environmental Monitoring and Measuring Technologies is structured as follows <ul style="list-style-type: none"> <li>• Legal framework of environmental protection</li> <li>• Significance and tasks of environmental monitoring and measuring technology</li> <li>• Explanation of the mechanisms of action in the increasingly intensive use of natural resources as well as the increasing hazard potential through the use of technologies</li> <li>• Chemosensor technology and sample preparation</li> <li>• Measurement principles and methods of environmental measurement technology</li> <li>• Optodes and optical measurement and analysis technology</li> <li>• Sensors for liquid analysis</li> <li>• Sensors for gas analysis</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, students are able to</p> <ul style="list-style-type: none"> <li>• analyse and understand the mechanisms of action in increasing environmental problems,</li> <li>• to select suitable measurement principles or measurement techniques for selected measurement tasks, considering the concrete measurement conditions,</li> <li>• characterise and interpret measurement results.</li> </ul> <p><b>Key qualifications:</b> The Students</p> <ul style="list-style-type: none"> <li>• can apply the acquired knowledge and skills in an interdisciplinary manner and with complex issues,</li> <li>• are able to develop targeted solutions based on systematic problem analysis,</li> <li>• are capable of familiarising themselves with relevant fields of work due to the method-oriented knowledge transfer.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination</td> <td>120-180 min or 30-45 min</td> <td>100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination	120-180 min or 30-45 min	100%
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a)	Written or Oral Examination	120-180 min or 30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRS-Ge Master v5</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bernd Henning</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Umweltmesstechnik:</i></p> <p><b>Module Homepage</b> <a href="http://emt.upb.de">http://emt.upb.de</a></p> <p><b>Methodical implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with slide presentation of extensive correlations</li><li>• Practical work in groups with measurement technology in the laboratory</li></ul> <p><b>Learning materials, references</b> Provision of a script; references to textbooks from the textbook collection will be announced.</p>
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### 3.4 Specialisation Area “Embedded Systems”

Specialisation Area	Embedded Systems
Modules	<ul style="list-style-type: none"> <li>* Advanced VLSI Design</li> <li>* Algorithms and Tools for Test and Diagnosis of Systems on a Chip</li> <li>* Approximate Computing</li> <li>* Integrated Circuits for Wireless Communications</li> <li>* Machine Learning I</li> <li>* Model-Based Systems Engineering</li> <li>* Reconfigurable Computing</li> <li>* Fast Integrated Circuits for Wireline Communications</li> <li>* Software Quality Assurance</li> <li>* VLSI Testing</li> </ul>
Catalogue advisor	Hellebrand, Sybille, Prof. Dr.
Credits ECTS	6
Learning objectives	

The modules from this specialisation area enable specialisation in the field of embedded systems.

Advanced VLSI Design						
Advanced VLSI Design						
Module number:	Workload (h):	Credits:	Regular Cycle:			
M.048.92043	180	6	summer term			
Language:	Semester number:	Duration (in sem.):	Module status (P=C/WP=CE)			
en	1.-3. Semester	1	WP			
1	<b>Module structure:</b>					
	Course	form of teaching	contact-time (h)	self-study (h)	status (C/CE)	group size (TN)
a)	L.048.92043 Advanced VLSI Design	2L 2Ex, SS	60	120	C	30/30

### 3 Specialisation Area

2	<p><b>Options within the module:</b></p> <p>None</p>
3	<p><b>Admission requirements:</b></p> <p>None</p> <p><i>Prerequisites of course Advanced VLSI Design:</i></p> <p><b>Recommended:</b> Fundamentals of Digital Circuits / Fundamentals of VLSI Design</p> <p>Information: Unless otherwise specified, these are recommendations.</p>
4	<p><b>Contents:</b></p> <p><i>Contents of the course Advanced VLSI Design:</i></p> <p><b>Short Description</b></p> <p>The course provides basic knowledge about the modern application-oriented modeling, simulation, analysis, and synthesis of digital systems at different abstraction levels to chip layout.</p> <p><b>Contents</b></p> <p>In today's practice, chip design consists of the combined application of various languages, methods, and tools for the modeling, simulation, and synthesis of electronic circuits. Along the modern abstraction-based design flow of digital systems (electronic system level to chip layout), the course provides basic knowledge of the main description languages and their application in modeling, simulation, analysis and synthesis. This includes basic principles and application of the IEEE standard system/hardware description languages SystemVerilog, SystemC, Verilog, and VHDL, in conjunction with additional formats, e.g., SDF and UPF for time and power annotation. For their application, the fundamental principles of test environments for simulation, timing and power analysis, logic synthesis and physical design of digital circuits. Exercises will provide hands-on labs based on commercial tools from Mentor Graphics, Synopsys and, Cadence Design Systems.</p>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After the course students are able</p> <ul style="list-style-type: none"> <li>• to model, simulate, analyze and synthesize simple digital circuits at different abstraction levels and</li> <li>• to apply the most important commercial tools for simulation, analysis and synthesis of digital circuits.</li> </ul> <p><b>Key qualifications:</b></p> <p>After the course students are able</p> <ul style="list-style-type: none"> <li>• to assess, select and apply modern digital circuit description languages for their different applications,</li> <li>• apply the different methods and tools in the modern VLSI design.</li> </ul>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> apl. Prof. Dr. Wolfgang Müller		
13	<b>Other Notes:</b> <i>Remarks of course Advanced VLSI Design:</i> <b>Course Homepage</b> <a href="http://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design">www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design</a> <b>Implementation</b> * Vorlesung mit Beamer und White-Board * Übungen mit Übungsblättern am Computer * Lecture with LCD projector and white board * Exercises with assignments and hands-on labs <b>Teaching Material, Literature</b> <ul style="list-style-type: none"> <li>• Lecture notes and exercise sheets will be provided via PAUL</li> <li>• IEEE standard reference manuals: IEEE Std 1800/1685/1666/1364/1076/1801/1497</li> <li>• Specific references for individual teaching units</li> </ul>		

### 3 Specialisation Area

<b>Algorithms and Tools for Test and Diagnosis of Systems on a Chip</b>						
Algorithms and Tools for Test and Diagnosis of Systems on a Chip						
<b>Module number:</b> M.048.92007	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
<b>1</b>	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92007 Algorithms and Tools for Test and Diagnosis of Systems on a Chip	2L 2Ex, WS+SS	60	120	C	30/30
<b>2</b>	<b>Options within the module:</b> None					
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Recommended:</b> VLSI Testing, (Introduction to Algorithms)					
<b>4</b>	<b>Contents:</b>  <i>Contents of the course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Short Description</b> The course “Algorithms and Tools for Test and Diagnosis of Systems on Chip” deals with advanced topics in test and diagnosis of integrated systems. The focus is on algorithms and tools for computer-aided preparation and application of test and diagnosis procedures. <b>** Contents**</b> Topics include but are not restricted to: <ul style="list-style-type: none"><li>● Advanced techniques for built-in self-test and embedded test</li><li>● Built-in diagnosis</li><li>● Test of robust and self-adaptive systems</li><li>● Adaptive Testing</li></ul>					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe recent approaches in test and diagnosis,</li> <li>• to explain and apply the underlying models and algorithms,</li> <li>• to explain the specific challenges of nanoscale integration and evaluate test strategies accordingly.</li> </ul> <p><b>Key qualifications:</b> The students are able</p> <ul style="list-style-type: none"> <li>• to apply their basic knowledge for studying and understanding new approaches from the state of the art literature,</li> <li>• to present the new contents in a conference style presentation, and</li> <li>• to describe the new contents in a scientific manuscript.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Sybille Hellebrand</p>										

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i></p> <p><b>Module Homepage</b> <a href="http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Self-study on recent approaches based on recent conference and journal publications</li><li>• Oral presentation</li><li>• Manuscript</li></ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides</li><li>• Additional material can be found in panda</li><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Kluwer Academic Publishers,2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li><li>• Artikel aus Fachzeitschriften und Konferenzbänden / Articles from Journals and Conference Proceedings (e.g. IEEE Transactions on Computers, IEEE Transactions on CAD of Integrated Circuits and Systems, IEEE International Test Conference, etc.)</li></ul>
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### 3 Specialisation Area

<b>Approximate Computing</b>						
Approximate Computing						
<b>Module number:</b> M.079.4068	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
<b>1</b>	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.09758 Approximate Computing	L3 Ex2	75	105	C	100/30
<b>2</b>	<b>Options within the module:</b> none					
<b>3</b>	<b>Admission requirements:</b> none  <i>Prerequisites of course Approximate Computing:</i> <b>Recommended Proficiencies</b> Knowledge of the Bachelor-level courses Digital Design and Computer Architecture are beneficial.					
<b>4</b>	<b>Contents:</b>  <i>Contents of the course Approximate Computing:</i> Approximate Computing is an emerging paradigm that trades-off computational accuracy for a significant reduction in energy, execution time, or chip area. This research-oriented course introduces to the field of Approximate Computing and its most remarkable aspects, and explains the main methods used to implement efficient computing systems by reducing accuracy. The course discusses approximations at all levels of a computing system, from applications down to hardware technologies. In exercise/tutorial sessions the efficiency of these techniques in various domains are examined, including deep learning and digital signal processing.  <ul style="list-style-type: none"> <li>• Introduction and motivation for approximate computing</li> <li>• Approximation at the application level, e.g., in machine learning and digital signal processing</li> <li>• Programming languages/compilers for approximate computing</li> <li>• Approximate microarchitectures</li> <li>• Automated synthesis of approximate circuits</li> <li>• Inexact arithmetic components and performance optimization via accuracy trade-offs</li> <li>• Approximation techniques at the technology level</li> <li>• Exercises/tutorial: Approximating deep learning and digital signal processing algorithms</li> </ul>					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending this course, the students are able</p> <ul style="list-style-type: none"> <li>• to name and explain approximation techniques at all levels of a computing system,</li> <li>• to identify major engineering/research problems when building approximate computing systems,</li> <li>• to judge the suitability of approximation techniques for different application domains, and</li> <li>• to apply approximation techniques to realize efficient hardware accelerators, in particular for deep learning and digital signal processing</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Marco Platzner</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Approximate Computing:</i></p> <p><b>Implementation method</b></p> <ul style="list-style-type: none"><li>• Lecture with projector and black/white board</li><li>• Interactive exercises/discussions in the lecture room</li><li>• Computer-based tutorials</li></ul> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides, exercise sheets, and tutorial assignments</li><li>• Adrian Sampson, Luis Ceze, and Dan Grossman: Good-Enough Computing. <i>IEEE Spectrum</i>, 50(10):54-59, 2013</li><li>• Ravi Nair. Big Data Needs Approximate Computing: Technical Perspective. <i>Communications of the ACM</i>, 58(1): 104, 2015.</li><li>• Sparsh Mittal. A Survey of Techniques for Approximate Computing. <i>ACM Computing Surveys</i>, 48(4), 2016.</li><li>• Qiang Xu, Todd Mytkowitz, and Nam Sung Kim. Approximate Computing: A Survey. <i>IEEE Design &amp; Test</i>, 33(1):8-22, 2016.</li><li>• Weiqiang Liu and Fabrizio Lombardi (Editors), <i>Approximate Computing</i>. Springer, 2022.</li><li>• Additional resources and links to current research papers are provided in the lecture.</li></ul>
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### 3 Specialisation Area

Integrierte Schaltungen für die drahtlose Kommunikation						
Integrated Circuits for Wireless Communications						
<b>Module number:</b> M.048.25017	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25017 Integrated Circuits for Wireless Communications	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Integrierte Schaltungen für die drahtlose Kommunikation:</i> <b>Recommended:</b> Lecture Schaltungstechnik resp. Circuit and System Design. Helpful supplement: Lecture "Wireless Communications" of Prof. Hab-Umbach.					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Mobile communications, wireless networks, and RFID technology are application examples of wireless communications. Wireless communications has found widespread use in everyday life and will become even more important in the future. The design of electronic circuits for radio frequencies requires a good system knowledge with respect to typical transmitter and receiver architectures in wireless communications, components, and radio signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to convey a methodical approach to the design of integrated circuits for wireless communications. A part of the exercises will pertain to calculation of circuit design problems another will be performed in small teams as a hands-on exercise using modern IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of radio frequency integrated circuits for wireless communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures “Schaltungstechnik” resp. “Circuit and System Design”. The following topics will be addressed:</p> <ul style="list-style-type: none"> <li>• Transmitter and receiver architectures for wireless communications</li> <li>• System Theory Basics             <ul style="list-style-type: none"> <li>– Signals and noise</li> <li>– Modulation and demodulation</li> <li>– Transmission properties of wireless communications systems</li> </ul> </li> <li>• Semiconductor technologies and integrated high-frequency devices</li> <li>• Amplifiers (low-noise and variable-gain amplifiers)</li> <li>• Mixers</li> <li>• Oscillators</li> <li>• Frequency synthesizer PLLs</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>The students will be able</p> <ul style="list-style-type: none"> <li>• to describe architectures and circuits of wireless communication systems</li> <li>• to describe and calculate fundamental signal transmission properties of wireless systems</li> <li>• to apply design methods to design components of radio frequency ICs</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                  <input type="checkbox"/> Module exam (MP)                  <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Oral Examination</td> <td style="text-align: center;">30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Oral Examination	30-45 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Oral Examination	30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								

### 3 Specialisation Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. J. Christoph Scheytt</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Course Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lecture with Powerpoint presentation and handwritten mathematical derivations using tablet and beamer</li> <li>• Exercises partly as handwritten calculation exercises using tablet and beamer and partly as practical IC design exercises using IC design software</li> </ul> <p><b>Teaching Material, Literature</b></p> <p>Lecture slides and videos as well as exercise slides will be made available.</p> <ul style="list-style-type: none"> <li>• Behzad Razavi “RF Microelectronics”, Prentice Hall, 2011</li> <li>• Thomas Lee “The Design of CMOS Radio-Frequency Integrated Circuits”, Cambridge University Press 2003</li> </ul>

### 3 Specialisation Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 20%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td style="text-align: center;">90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 20%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismael, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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### 3 Specialisation Area

Model-Based Systems Engineering							
Model-Based Systems Engineering							
<b>Module number:</b> M.079.4062	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05810 Model-Based Systems Engineering	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Model-Based Systems Engineering:</i> <b>Recommended Proficiencies</b> Basics of Systems Engineerings						
4	<b>Contents:</b>  <i>Contents of the course Model-Based Systems Engineering:</i> Due to the technical change from mechatronic to intelligent technical systems (ITS), companies and development teams are facing many challenges. A key factor is the increase in complexity and networking of systems (products). Existing approaches in product development cannot cover this efficiently and effectively. Model-based Systems Engineering (MBSE) presents itself as a promising approach to solve these challenges. MBSE sees itself as a further development of systems engineering and builds on its foundations. Systems engineering, which is primarily based on documents, is extended by the introduction of models. The course includes the following content: <ul style="list-style-type: none"> <li>• Intelligent Engineering Systems</li> <li>• Model-based Systems Engineering 101</li> <li>• Systems Modeling Fundamentals</li> <li>• Languages and Methods - CONSENS, SysML</li> <li>• Systems Architecting</li> <li>• IT Tools for MBSE</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• acquire a solid understanding of Model-Based System Engineering</li> <li>• know different methods, languages, and tools</li> <li>• are able to apply the knowledge they have gained</li> <li>• are able to work out solutions independently and communicate them to the lecturers.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Roman Dumitrescu</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Model-Based Systems Engineering:</i></p> <p><b>Implementation Method</b></p> <p>The module consists of two parts</p> <ol style="list-style-type: none"><li>1. lecture with slides: basics and concepts are explained in the lecture and illustrated with examples.</li><li>2. exercises (tutorial): In the exercises, knowledge is transferred and the concepts are applied. The exercises have to be prepared by the students themselves.</li></ol> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Gausemeier, J.; Dumitrescu, R.; Steffen, D.; Czaja, A.; Wiederkehr, O.; Tschirner, C.: Systems Engineering in industrial practice. Heinz Nixdorf Institute, University Paderborn, 2013, Under: <a href="https://www.hni.uni-paderborn.de/en/spe/systemsengineering/">https://www.hni.uni-paderborn.de/en/spe/systemsengineering/</a></li><li>• Dumitrescu, R.; Albers, A.; Riedel, O.; Stark, R.; Gausemeier, J. (Eds): Engineering in Germany – Status quo in Business and Science. Federal Ministry of Education and Research, 2021 Under: <a href="https://www.advanced-systems-engineering.de/#studie">https://www.advanced-systems-engineering.de/#studie</a></li><li>• Additional literature will be announced in the course.</li></ul>
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### 3 Specialisation Area

Reconfigurable Computing							
Reconfigurable Computing							
<b>Module number:</b> M.079.4043	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05703 Reconfigurable Computing	L2 Ex3	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Reconfigurable Computing:</i> <b>Recommended Proficiencies</b> Knowledge of the Bachelor-level courses Digital Design, Programming, and Data Structures and Algorithms are beneficial.						
4	<b>Contents:</b>  <i>Contents of the course Reconfigurable Computing:</i> The course Reconfigurable Computing introduces into the field of computing with reprogrammable hardware structures. Computing systems built from reprogrammable hardware structures do not rely on a fixed hardware, but adapt their hardware architecture to the application under execution. The field was formed in the early 1990s when Field-programmable Gate Arrays (FPGAs) became commercially available that were powerful enough to be used for computing. Today, FPGA-based high-performance systems have outperformed state-of-the-art computers for many problems including database search, genomic sequence scanning, and cryptography. In embedded systems, FPGAs accelerate system functions, reduce system cost and energy consumption, and enable hardware-on-demand functionality. The course covers the following topics:  <ul style="list-style-type: none"> <li>• Introduction to reconfigurable computing</li> <li>• Evolution of programmable hardware devices</li> <li>• FPGA architectures</li> <li>• Computer-aided design for FPGAs</li> <li>• High-level languages for programming FPGAs</li> <li>• Application domains for FPGAs</li> <li>• Comparison of devices, technologies, and reconfigurable systems</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• compare different reprogrammable hardware devices and describe their historical development,</li> <li>• name the design steps and problems when designing with FPGAs,</li> <li>• analyse algorithms for the design steps and apply them to examples,</li> <li>• compare and evaluate current approaches to programming FPGAs,</li> <li>• justify the suitability of different reprogrammable hardware components for different areas of application, and</li> <li>• implement functions of medium complexity with modern FPGA design tools.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Marco Platzner</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Reconfigurable Computing:</i></p> <p><b>Implementation Method</b></p> <p>The course consists of a lecture, and pencil&amp;paper as well as practical exercises. The lecture is held with a beamer and blackboard. In the pencil&amp;paper exercises, problems are handed out and their solutions are presented and discussed in a practice session. In addition, quizzes are offered for self-assessments. In the practical exercises, a tutorial on the design with FPGAs is carried out and then tasks are handed out, which are implemented as design or programming examples in groups of one to three participants.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides, assignment sheets for paper&amp;pencil exercises, quizzes</li><li>• Tutorial, assignment sheets for design and programming examples, technical documentation</li><li>• Selected scientific articles</li><li>• Additional literature will be announced in the course.</li></ul>
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### 3 Specialisation Area

Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation						
Fast Integrated Circuits for Wireline Communications						
<b>Module number:</b> M.048.25019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25019 Fast Integrated Circuits for Wireline Communications	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Recommended:</b> Module "Schaltungstechnik" of the Bachelor Electrical Engineering or module "Circuit and System Design" of the Master "Electrical Systems Engineering" or comparable modules / lectures					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Nowadays commercial fiber-optic communication systems reach very high data rates of 100 Gb/s per optical channel and several Tb/s in a single fiber. In a similar way very high data rates of more than 10 Gb/s occur at a single package pin of electronic chips. These signals are to be transmitted over printed circuit boards and inexpensive serial cables. In the future the progress of CMOS technology and communication technology will push speed of fiber-optic and wire-line communication continuously to ever higher data rates. The design of electronic circuits for high bandwidth resp. data rates requires a good system knowledge with respect to typical transmitter and receiver architectures, components, and signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to enable the student to utilize a methodological approach for the design of fast integrated electronic circuits for digital wired communications. A part of the exercises will be carried out using modern industry-standard IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of fast integrated electronic circuits for digital broadband communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures "Schaltungstechnik" resp. "Circuit and System Design". The lecture deals with:</p> <ul style="list-style-type: none"><li>• Transmitter and receiver architectures for fiber-optic communications</li><li>• Transmitter and receiver architectures for chip-to-chip communications</li><li>• System design</li><li>• Semiconductor technology and integrated high-frequency devices</li><li>• Broadband amplifiers</li><li>• Current-mode logic</li><li>• Transmitter and receiver circuits</li><li>• PLLs for frequency synthesis and clock recovery</li><li>• Measurement methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>The student will be able to:</p> <ul style="list-style-type: none"><li>• describe and analyze transmitter and receiver architectures for broadband communication links</li><li>• understand and describe semiconductor technologies and integrated high-frequency devices for broadband circuits</li><li>• to analyze circuit design techniques for transmitter and receiver circuits and describe ways to optimize them</li><li>• to describe circuits in PLL technique for frequency synthesis and clock recovery</li><li>• to describe measurement methods</li></ul> <p><b>Key qualifications:</b></p> <p>The students will learn how different interdisciplinary scientific domains and their methods - like mathematical signal and system analysis, non-linear and linear circuit analysis, semiconductor physics, semiconductor devices and high-frequency engineering - are applied together for the development of communications application.</p>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Oral Examination	30-45 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. J. Christoph Scheytt		
13	<b>Other Notes:</b> <i>Remarks of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Course Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/</a> <b>Implementation</b> Lecture with Exercises (including computer-aided design using electronic design software) <b>Teaching Material, Literature</b> Handouts and literature references will be given in the lecture. <ul style="list-style-type: none"> <li>• E. Säckinger, "Broadband Circuits for Optical Fiber Communication", Wiley, 2005</li> <li>• B. Razavi, "Design of Integrated Circuits for Optical Communications", McGraw-Hill, 2003</li> </ul> <b>Comments</b> As part of the lecture a 2-day excursion to IHP Leibnizinstitute for High-Performance Microelectronics in Frankfurt (Oder) is offered which includes the visit of a modern chip fabrication facility (participation in the excursion is voluntary).		

### 3 Specialisation Area

<b>VLSI-Testing</b>							
VLSI-Testing							
<b>Module number:</b> M.048.92027	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	a)	L.048.92027 VLSI Testing	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course VLSI Testing:</i> <b>Recommended:</b> Digital Design						
4	<b>Contents:</b>  <i>Contents of the course VLSI Testing:</i> <b>Short Description</b> The course “VLSI Testing” focuses on techniques for detecting hardware defects in micro-electronic circuits. Algorithms for test data generation and test response evaluation as well as hardware structures for design for test (DFT) and on-chip test implementation (BIST) are presented. <b>Contents</b> In detail the following topics are covered: <ul style="list-style-type: none"><li>• Fault models</li><li>• Testability measures and design for test (DFT)</li><li>• Logic and fault simulation</li><li>• Automatic test pattern generation (ATPG)</li><li>• Built-in self-test (BIST), in particular test data compression and test response compaction</li><li>• Memory test</li></ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe fault models, DFT techniques, and test tools,</li> <li>• to explain and apply the underlying models and algorithms for fault simulation and test generation,</li> <li>• to analyze systems with respect to their testability and to derive appropriate test strategies.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the practiced strategies for problem solving across varying disciplines,</li> <li>• have experience in presenting their solutions to their fellow students, and</li> <li>• know how to improve their competences by private study.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sybille Hellebrand</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course VLSI Testing:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Exercises in small groups based on exercise sheets with students presenting their own solutions</li><li>• Hands-on exercises using various software tools</li></ul> <p><b>Teaching Material, Literature</b></p> <p>Additional material can be found in panda</p> <ul style="list-style-type: none"><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Boston, Dordrecht, London: Kluwer Academic Publishers, 2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li></ul>
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### 3.5 Specialisation Area “Nano/Microelectronics”

Specialisation Area	Nano/Microelectronics
Modules	<ul style="list-style-type: none"> <li>* Advanced VLSI Design</li> <li>* Algorithms and Tools for Test and Diagnosis of Systems on a Chip</li> <li>* Introduction to High-Frequency Engineering</li> <li>* High Frequency Engineering</li> <li>* Integrated Circuits for Wireless Communications</li> <li>* Machine Learning I</li> <li>* Optoelectronics</li> <li>* Fast Integrated Circuits for Wireline Communications</li> <li>* Theory and Design of Phase-locked Loops</li> <li>* VLSI Testing</li> </ul>
Catalogue advisor	Hellebrand, Sybille, Prof. Dr.
Credits ECTS	6
Learning objectives	

The modules from this specialisation area enable specialisation in the field of nano- and microelectronics.

Advanced VLSI Design						
Advanced VLSI Design						
<b>Module number:</b>	<b>Workload (h):</b>	<b>Credits:</b>	<b>Regular Cycle:</b>			
M.048.92043	180	6	summer term			
<b>Language:</b>	<b>Semester number:</b>	<b>Duration (in sem.):</b>	<b>Module status (P=C/WP=CE)</b>			
en	1.-3. Semester	1	WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.92043 Advanced VLSI Design	2L 2Ex, SS	60	120	C	30/30

### 3 Specialisation Area

2	<p><b>Options within the module:</b></p> <p>None</p>
3	<p><b>Admission requirements:</b></p> <p>None</p> <p><i>Prerequisites of course Advanced VLSI Design:</i></p> <p><b>Recommended:</b> Fundamentals of Digital Circuits / Fundamentals of VLSI Design</p> <p>Information: Unless otherwise specified, these are recommendations.</p>
4	<p><b>Contents:</b></p> <p><i>Contents of the course Advanced VLSI Design:</i></p> <p><b>Short Description</b></p> <p>The course provides basic knowledge about the modern application-oriented modeling, simulation, analysis, and synthesis of digital systems at different abstraction levels to chip layout.</p> <p><b>Contents</b></p> <p>In today's practice, chip design consists of the combined application of various languages, methods, and tools for the modeling, simulation, and synthesis of electronic circuits. Along the modern abstraction-based design flow of digital systems (electronic system level to chip layout), the course provides basic knowledge of the main description languages and their application in modeling, simulation, analysis and synthesis. This includes basic principles and application of the IEEE standard system/hardware description languages SystemVerilog, SystemC, Verilog, and VHDL, in conjunction with additional formats, e.g., SDF and UPF for time and power annotation. For their application, the fundamental principles of test environments for simulation, timing and power analysis, logic synthesis and physical design of digital circuits. Exercises will provide hands-on labs based on commercial tools from Mentor Graphics, Synopsys and, Cadence Design Systems.</p>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After the course students are able</p> <ul style="list-style-type: none"> <li>• to model, simulate, analyze and synthesize simple digital circuits at different abstraction levels and</li> <li>• to apply the most important commercial tools for simulation, analysis and synthesis of digital circuits.</li> </ul> <p><b>Key qualifications:</b></p> <p>After the course students are able</p> <ul style="list-style-type: none"> <li>• to assess, select and apply modern digital circuit description languages for their different applications,</li> <li>• apply the different methods and tools in the modern VLSI design.</li> </ul>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> apl. Prof. Dr. Wolfgang Müller		
13	<b>Other Notes:</b> <i>Remarks of course Advanced VLSI Design:</i> <b>Course Homepage</b> <a href="http://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design">www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design</a> <b>Implementation</b> * Vorlesung mit Beamer und White-Board * Übungen mit Übungsblättern am Computer * Lecture with LCD projector and white board * Exercises with assignments and hands-on labs <b>Teaching Material, Literature</b> <ul style="list-style-type: none"> <li>• Lecture notes and exercise sheets will be provided via PAUL</li> <li>• IEEE standard reference manuals: IEEE Std 1800/1685/1666/1364/1076/1801/1497</li> <li>• Specific references for individual teaching units</li> </ul>		

### 3 Specialisation Area

<b>Algorithms and Tools for Test and Diagnosis of Systems on a Chip</b>						
Algorithms and Tools for Test and Diagnosis of Systems on a Chip						
<b>Module number:</b> M.048.92007	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
<b>1</b>	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92007 Algorithms and Tools for Test and Diagnosis of Systems on a Chip	2L 2Ex, WS+SS	60	120	C	30/30
<b>2</b>	<b>Options within the module:</b> None					
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Recommended:</b> VLSI Testing, (Introduction to Algorithms)					
<b>4</b>	<b>Contents:</b>  <i>Contents of the course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Short Description</b> The course “Algorithms and Tools for Test and Diagnosis of Systems on Chip” deals with advanced topics in test and diagnosis of integrated systems. The focus is on algorithms and tools for computer-aided preparation and application of test and diagnosis procedures. <b>** Contents**</b> Topics include but are not restricted to: <ul style="list-style-type: none"><li>• Advanced techniques for built-in self-test and embedded test</li><li>• Built-in diagnosis</li><li>• Test of robust and self-adaptive systems</li><li>• Adaptive Testing</li></ul>					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe recent approaches in test and diagnosis,</li> <li>• to explain and apply the underlying models and algorithms,</li> <li>• to explain the specific challenges of nanoscale integration and evaluate test strategies accordingly.</li> </ul> <p><b>Key qualifications:</b> The students are able</p> <ul style="list-style-type: none"> <li>• to apply their basic knowledge for studying and understanding new approaches from the state of the art literature,</li> <li>• to present the new contents in a conference style presentation, and</li> <li>• to describe the new contents in a scientific manuscript.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Sybille Hellebrand</p>										

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i></p> <p><b>Module Homepage</b> <a href="http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Self-study on recent approaches based on recent conference and journal publications</li><li>• Oral presentation</li><li>• Manuscript</li></ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides</li><li>• Additional material can be found in panda</li><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Kluwer Academic Publishers,2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li><li>• Artikel aus Fachzeitschriften und Konferenzbänden / Articles from Journals and Conference Proceedings (e.g. IEEE Transactions on Computers, IEEE Transactions on CAD of Integrated Circuits and Systems, IEEE International Test Conference, etc.)</li></ul>
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### 3 Specialisation Area

<b>Einführung in die Hochfrequenztechnik</b>							
Introduction to High-Frequency Engineering							
<b>Module number:</b> M.048.40003	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b>	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
<b>1</b>	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.11004 Introduction to High-Frequency Engineering	2L 2Ex, WS	60	120	CE	30/30	
<b>2</b>	<b>Options within the module:</b> None						
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Einführung in die Hochfrequenztechnik:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics and Foundations of Electrical Engineering.						
<b>4</b>	<b>Contents:</b>  <i>Contents of the course Einführung in die Hochfrequenztechnik:</i> <b>Short Description</b> The course Introduction to High-Frequency Engineering provides basic knowledge of high-frequency engineering in particular with respect to signal propagation along transmission lines on circuit boards and integrated circuits. This knowledge is prerequisite for the continuative courses High-Frequency Engineering, Optical Communication, and High-Frequency Electronics. <b>Contents</b> In the first part of the course Introduction to High-Frequency Engineering, an equivalent circuit together with primary transmission line parameter is introduced. The resulting telegraph equation is solved for various boundary conditions. In particular, stationary processes and lossless transmission lines are considered and the Smith diagram is introduced. The gained knowledge is used to dimension circuits comprising distributed and lumped components, in particular matching networks. In the second part, high-frequency aspects of circuit theory are covered. In particular, circuits comprising distributed and lumped elements are consistently described and classified by scattering parameters, and gain definitions are derived.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• describe circuits comprising distributed and lumped components,</li> <li>• to analyze,</li> <li>• and to design the latter.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can use of methodic knowledge for systematic problem analysis,</li> <li>• get familiar with the CAD system ADS, which is commonly used in industry</li> <li>• and gain foreign language competences related to the field.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Andreas Thiede</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Einführung in die Hochfrequenztechnik:</i></p> <p><b>Course Homepage</b> <a href="http://groups.uni-paderborn.de/hfe/teaching/hft.html">http://groups.uni-paderborn.de/hfe/teaching/hft.html</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with black board presentation, supported by animated graphics and transparencies,</li><li>• Presence exercises with task sheets to be solved by the students together, supported by the teacher, and partially using CAD software.</li></ul> <p><b>Teaching Material, Literature</b></p> <p>A. Thiede, Einführung in die Hochfrequenztechnik, Vorlesungsskript Universität Paderborn continuative and deepening literature A. Thiede, Integrierte Hochfrequenzschaltkreise, Springer Vieweg Verlag (YDA2058) P. Vielhauer, Lineare Netzwerke, Verlag Technik und Hüthig (65 YCF 1469) M. Hoffmann, Hochfrequenztechnik, Springer Verlag (51 YDA 1913) O. Zinke, H. Brunswig, Hochfrequenztechnik, Bd.1+2, Springer Verlag (51 YDA 1086) G. Gonzalez, Microwave Transistor Amplifiers, Prentice Hall (51 YEP 3142) P.C.L. Yip, High-Frequency Circuit Design and Measurements, Chapman&amp;Hall (51 YDA 1751) R.E. Collin, Foundations for Microwave Engineering, Mc Graw-Hill (51 YGA 1240)</p>
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### 3 Specialisation Area

High Frequency Engineering							
High Frequency Engineering							
<b>Module number:</b> M.048.92002	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92002 High Frequency Engineering	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course High Frequency Engineering:</i> None						
4	<b>Contents:</b> <i>Contents of the course High Frequency Engineering:</i> <b>Short Description</b> This lecture gives application-oriented knowledge in high frequency engineering. Furthermore, it gives knowledge in active and passive high-frequency circuits. <b>Contents</b> The lecture High-Frequency Engineering extends the content of the lecture Theoretische Elektrotechnik by further application-relevant knowledge. The aim is to qualify the students for development tasks for example in the radio frequency part of a mobile telephone. But considerations of high-frequency engineering are also needed in prevalent digital circuits. The emphases of the lecture are passive devices, high-frequency properties of fundamental transistor circuits, linear and nonlinear amplifiers, noisy multiports, mixers, oscillators, injection-locking and phase-locked loop.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught extent, to understand the function of components, circuits and systems of high-frequency engineering, to model and to apply them.</p> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course High Frequency Engineering:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Implementation</b> Lecture and exercise</p> <p><b>Teaching Material, Literature</b> Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• Thiede, A.: Skriptum Hochfrequenzelektronik/High-Frequency Electronics, Universität Paderborn</li><li>• Sze, S. M.: High Speed Semiconductor Devices, John Wiley &amp; Sons, 1990</li><li>• Herbst, L. J.: Integrated Circuit Engineering, Oxford University Press, 1996</li><li>• Yip, P. C. L.: High-Frequency Circuit Design and Measurement, Chapman &amp; Hall, 1996</li><li>• Gonzalez, G.: Microwave Transistor Amplifiers, Prentice Hall, 1997</li><li>• Hoffmann, M.: Hochfrequenztechnik, Springer, 1997</li></ul>
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### 3 Specialisation Area

<b>Integrierte Schaltungen für die drahtlose Kommunikation</b>							
Integrated Circuits for Wireless Communications							
<b>Module number:</b> M.048.25017	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
<b>1</b>	<b>Module structure:</b>						
	<b>a)</b>	L.048.25017 Integrated Circuits for Wire- less Communications	2L 2Ex, SS	60	120	C	40/40
<b>2</b>	<b>Options within the module:</b> None						
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Integrierte Schaltungen für die drahtlose Kommunikation:</i> <b>Recommended:</b> Lecture Schaltungstechnik resp. Circuit and System Design. Helpful supplement: Lecture "Wireless Communications" of Prof. Hab-Umbach.						

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Mobile communications, wireless networks, and RFID technology are application examples of wireless communications. Wireless communications has found widespread use in everyday life and will become even more important in the future. The design of electronic circuits for radio frequencies requires a good system knowledge with respect to typical transmitter and receiver architectures in wireless communications, components, and radio signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to convey a methodical approach to the design of integrated circuits for wireless communications. A part of the exercises will pertain to calculation of circuit design problems another will be performed in small teams as a hands-on exercise using modern IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of radio frequency integrated circuits for wireless communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures “Schaltungstechnik” resp. “Circuit and System Design”. The following topics will be addressed:</p> <ul style="list-style-type: none"> <li>• Transmitter and receiver architectures for wireless communications</li> <li>• System Theory Basics             <ul style="list-style-type: none"> <li>– Signals and noise</li> <li>– Modulation and demodulation</li> <li>– Transmission properties of wireless communications systems</li> </ul> </li> <li>• Semiconductor technologies and integrated high-frequency devices</li> <li>• Amplifiers (low-noise and variable-gain amplifiers)</li> <li>• Mixers</li> <li>• Oscillators</li> <li>• Frequency synthesizer PLLs</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>The students will be able</p> <ul style="list-style-type: none"> <li>• to describe architectures and circuits of wireless communication systems</li> <li>• to describe and calculate fundamental signal transmission properties of wireless systems</li> <li>• to apply design methods to design components of radio frequency ICs</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                  <input type="checkbox"/> Module exam (MP)                  <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Oral Examination</td> <td style="text-align: center;">30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Oral Examination	30-45 min	100%
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a)	Oral Examination	30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								

### 3 Specialisation Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. J. Christoph Scheytt</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Course Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lecture with Powerpoint presentation and handwritten mathematical derivations using tablet and beamer</li> <li>• Exercises partly as handwritten calculation exercises using tablet and beamer and partly as practical IC design exercises using IC design software</li> </ul> <p><b>Teaching Material, Literature</b></p> <p>Lecture slides and videos as well as exercise slides will be made available.</p> <ul style="list-style-type: none"> <li>• Behzad Razavi "RF Microelectronics", Prentice Hall, 2011</li> <li>• Thomas Lee "The Design of CMOS Radio-Frequency Integrated Circuits", Cambridge University Press 2003</li> </ul>

### 3 Specialisation Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td style="text-align: center;">90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 25%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismael, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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### 3 Specialisation Area

Optoelectronics							
Optoelectronics							
<b>Module number:</b> M.048.26011	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.26011 Optoelectronics	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optoelectronics:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optoelectronics:</i> <b>Short description</b> The lecture Optoelectronics covers the fundamental aspects of optoelectronic devices, starting with semiconductor materials and their interaction with light and photons, to the electronic aspects of the components, and finally to the use of quantum mechanical effects to optimise modern components for their respective areas of application, such as in lighting systems, renewable energy, broadband optical communication systems or in medical technology. <b>Contents</b> In the first part of the lecture, the basics of semiconductors (lattice structure, band structure, direct-indirect semiconductors, doping, degenerate and non-degenerate semiconductors, heterostructures, quantum effects in low-dimensional semiconductors) are recapitulated. The elementary interactions between light and semiconductors (absorption, stimulated emission, spontaneous emission) and the electronic aspects of the components (p-n junction, heterojunctions) are then covered. Finally, the most important devices such as solar cells, photodiodes, light-emitting diodes and semiconductor lasers are discussed in detail and their most important parameters and optimisation strategies are explained.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• explain the basic physical properties of optoelectronic semiconductor devices based on classical and fundamental quantum mechanical descriptions,</li> <li>• to describe the main concepts of optoelectronic semiconductor devices (photodiodes, solar cells, light emitting diodes, semiconductor lasers),</li> <li>• categorize different device designs according to their application requirements.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can use of methodic knowledge for systematic problem analysis for a wide range of disciplines,</li> <li>• will be in position to familiarise themselves independently with new generations of semiconductor devices, thanks to the comprehensive fundamental training received,</li> <li>• get familiar to rate-equation models to simulate steady-state and dynamic characteristics in coupled systems,</li> <li>• and gain foreign language competences related to the field.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								

### 3 Specialisation Area

12	<b>Module coordinator:</b> Prof. Dr.-Ing. Nils Christopher Gerhardt
13	<b>Other Notes:</b> <b>Module Homepage</b> to be announced at the start of the lecture <b>Implementation</b> Lectures and exercises (including some computer simulations) <b>Teaching Material, Literature</b> Lecture notes and handouts for the tutorial; literature references will be given in the first lecture

### 3 Specialisation Area

Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation						
Fast Integrated Circuits for Wireline Communications						
<b>Module number:</b> M.048.25019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25019 Fast Integrated Circuits for Wireline Communications	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Recommended:</b> Module "Schaltungstechnik" of the Bachelor Electrical Engineering or module "Circuit and System Design" of the Master "Electrical Systems Engineering" or comparable modules / lectures					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Nowadays commercial fiber-optic communication systems reach very high data rates of 100 Gb/s per optical channel and several Tb/s in a single fiber. In a similar way very high data rates of more than 10 Gb/s occur at a single package pin of electronic chips. These signals are to be transmitted over printed circuit boards and inexpensive serial cables. In the future the progress of CMOS technology and communication technology will push speed of fiber-optic and wire-line communication continuously to ever higher data rates. The design of electronic circuits for high bandwidth resp. data rates requires a good system knowledge with respect to typical transmitter and receiver architectures, components, and signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to enable the student to utilize a methodological approach for the design of fast integrated electronic circuits for digital wired communications. A part of the exercises will be carried out using modern industry-standard IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of fast integrated electronic circuits for digital broadband communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures "Schaltungstechnik" resp. "Circuit and System Design". The lecture deals with:</p> <ul style="list-style-type: none"><li>• Transmitter and receiver architectures for fiber-optic communications</li><li>• Transmitter and receiver architectures for chip-to-chip communications</li><li>• System design</li><li>• Semiconductor technology and integrated high-frequency devices</li><li>• Broadband amplifiers</li><li>• Current-mode logic</li><li>• Transmitter and receiver circuits</li><li>• PLLs for frequency synthesis and clock recovery</li><li>• Measurement methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>The student will be able to:</p> <ul style="list-style-type: none"><li>• describe and analyze transmitter and receiver architectures for broadband communication links</li><li>• understand and describe semiconductor technologies and integrated high-frequency devices for broadband circuits</li><li>• to analyze circuit design techniques for transmitter and receiver circuits and describe ways to optimize them</li><li>• to describe circuits in PLL technique for frequency synthesis and clock recovery</li><li>• to describe measurement methods</li></ul> <p><b>Key qualifications:</b></p> <p>The students will learn how different interdisciplinary scientific domains and their methods - like mathematical signal and system analysis, non-linear and linear circuit analysis, semiconductor physics, semiconductor devices and high-frequency engineering - are applied together for the development of communications application.</p>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Oral Examination	30-45 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. J. Christoph Scheytt		
13	<b>Other Notes:</b> <i>Remarks of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Course Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/</a> <b>Implementation</b> Lecture with Exercises (including computer-aided design using electronic design software) <b>Teaching Material, Literature</b> Handouts and literature references will be given in the lecture. <ul style="list-style-type: none"> <li>• E. Säckinger, "Broadband Circuits for Optical Fiber Communication", Wiley, 2005</li> <li>• B. Razavi, "Design of Integrated Circuits for Optical Communications", McGraw-Hill, 2003</li> </ul> <b>Comments</b> As part of the lecture a 2-day excursion to IHP Leibnizinstitute for High-Performance Microelectronics in Frankfurt (Oder) is offered which includes the visit of a modern chip fabrication facility (participation in the excursion is voluntary).		

### 3 Specialisation Area

<b>Theory and Design of Phase-locked Loops</b>							
Theory and Design of Phase-locked Loops							
<b>Module number:</b> M.048.24020	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.24020 Theory and Design of Phase-locked Loops	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Theory and Design of Phase-locked Loops:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• Advanced Signal Theory</li> <li>• Statistical Signal Processing (or another course with comparable syllabus in their bachelor)</li> <li>• Circuit and system design (or another course with comparable syllabus in their bachelor)</li> </ul>						

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Theory and Design of Phase-locked Loops:</i></p> <p>Chapter 1: Motivation</p> <ul style="list-style-type: none"> <li>• Time and frequency definition – definition in SI units – static frequency error</li> <li>• random fluctuations – Amplitude/Phase (AM/PM) noise</li> </ul> <p>Chapter 2: Mathematical formalism of signals</p> <ul style="list-style-type: none"> <li>• baseband and bandpass signals</li> <li>• time and frequency domain</li> </ul> <p>Chapter 3: introduction to random processes</p> <ul style="list-style-type: none"> <li>• baseband random processes and noise – correlation functions in time and frequency domain – some basedband random processes (thermal noise, shot noise, flicker noise . . .)</li> <li>• bandpass random processes – correlation functions – relation to baseband processes – phase noise and amplitude noise</li> </ul> <p>Chapter 4: PLL building blocks</p> <ul style="list-style-type: none"> <li>• Phase detector – Phase detector model – phase noise of phase detector</li> <li>• VCO – VCO model – phase noise of VCO</li> <li>• Frequency translators – frequency divider – frequency multiplier – phase noise of frequency translators</li> <li>• transistor level design of PLL blocks</li> </ul> <p>Chapter 5: Integer N PLLs: – Time domain – frequency domain – phase noise – spurious frequencies</p> <p>Chapter 6: Fractional PLLs – Time domain – frequency domain – phase noise – spurious frequencies</p>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Understanding of static/dynamic error in frequency standards. Mathematical modeling of amplitude/phase noise. Modeling random processes in time and frequency domain. Basic and modern PLL architecture and its building blocks. Systematic design of PLLs. Design and Modeling of Integer/Fractional N PLL.</p>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								

### 3 Specialisation Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr. Meysam Bahmanian</p>
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls">https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>

### 3 Specialisation Area

VLSI-Testing							
VLSI-Testing							
<b>Module number:</b> M.048.92027		<b>Workload (h):</b> 180		<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term	
<b>Language:</b> en		<b>Semester number:</b> 1.-3. Semester		<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P	
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92027 VLSI Testing	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course VLSI Testing:</i> <b>Recommended:</b> Digital Design						
4	<b>Contents:</b> <i>Contents of the course VLSI Testing:</i> <b>Short Description</b> The course “VLSI Testing” focuses on techniques for detecting hardware defects in micro-electronic circuits. Algorithms for test data generation and test response evaluation as well as hardware structures for design for test (DFT) and on-chip test implementation (BIST) are presented. <b>Contents</b> In detail the following topics are covered:						
	<ul style="list-style-type: none"> <li>• Fault models</li> <li>• Testability measures and design for test (DFT)</li> <li>• Logic and fault simulation</li> <li>• Automatic test pattern generation (ATPG)</li> <li>• Built-in self-test (BIST), in particular test data compression and test response compaction</li> <li>• Memory test</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe fault models, DFT techniques, and test tools,</li> <li>• to explain and apply the underlying models and algorithms for fault simulation and test generation,</li> <li>• to analyze systems with respect to their testability and to derive appropriate test strategies.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the practiced strategies for problem solving across varying disciplines,</li> <li>• have experience in presenting their solutions to their fellow students, and</li> <li>• know how to improve their competences by private study.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sybille Hellebrand</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course VLSI Testing:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Exercises in small groups based on exercise sheets with students presenting their own solutions</li><li>• Hands-on exercises using various software tools</li></ul> <p><b>Teaching Material, Literature</b></p> <p>Additional material can be found in panda</p> <ul style="list-style-type: none"><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Boston, Dordrecht, London: Kluwer Academic Publishers, 2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li></ul>
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### 3.6 Specialisation Area “Signal Processing”

Specialisation Area	Signal Processing
Modules	<ul style="list-style-type: none"> <li>* Advanced System Theory</li> <li>* Digital Image Processing I</li> <li>* Digital Image Processing II</li> <li>* Digital Speech Signal Processing</li> <li>* Integrated Circuits for Wireless Communications</li> <li>* Machine Learning I</li> <li>* Machine Learning for Biometrics</li> <li>* Optimal and Adaptive Filters</li> <li>* Fast Integrated Circuits for Wireline Communications</li> <li>* Statistical and Machine Learning</li> <li>* Cognitive Systems Engineering - Special Topics</li> <li>* Topics in Pattern Recognition and Machine Learning</li> <li>* Topics in Signal Processing</li> <li>* Wireless Communications</li> </ul>
Catalogue advisor	Hellebrand, Sybille, Prof. Dr.
Credits ECTS	6
Learning objectives	

The modules from this specialisation area enable specialisation in the field of signal, image and language processing.

Advanced System Theory			
Advanced System Theory			
<b>Module number:</b> M.048.92001	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term
<b>Language:</b> de / en	<b>Semester number:</b> 1. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P

### 3 Specialisation Area

1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92001 Advanced System Theory	2L 2Ex, WS/SS	60	120	C	60/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Advanced System Theory:</i> <b>Recommended:</b> Prerequisites are a basic understanding of differential equations, linear algebra, and Laplace transforms, as they are covered in a typical undergraduate course on system theory.						
4	<b>Contents:</b> <i>Contents of the course Advanced System Theory:</i> <b>Short Description</b> Building on an undergraduate system theory course, this course studies the dynamical behavior of linear systems with greater mathematical rigor. The course is primarily intended to serve students in engineering, but it can also be useful to students in physics and other natural sciences. <b>Contents</b>						
5	<b>Learning outcomes and competences:</b> After attending this course, students will be familiar with the most important concepts and results in linear system theory. Students will develop confidence in their ability to solve mathematical problems of analysis and design. Many of their timeless insights and intuitions about the dynamical behavior of systems will be drawn from this course. This course presents material broad enough so that students will have a clear understanding of the dynamical behavior of linear systems, including their power and limitations. This will allow students to apply the theory to other fields.						

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> Prof. Dr. Erdal Kayacan		
13	<b>Other Notes:</b> <i>Remarks of course Advanced System Theory:</i> <b>Course Homepage</b> <a href="https://en.ei.uni-paderborn.de/rat">https://en.ei.uni-paderborn.de/rat</a> <b>Implementation</b> Lectures and exercises (including some computer simulations) Panda course for communication and material distribution <b>Teaching Material, Literature</b> Handouts and exercise / tutorial questions; literature references will be given in the first lecture		

### 3 Specialisation Area

Digital Image Processing I							
Digital Image Processing I							
<b>Module number:</b> M.048.92008	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92008 Digital Image Processing I	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Digital Image Processing I:</i> None. Basic programming knowledge is an advantage.						

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Digital Image Processing I:</i></p> <p><b>Short Description</b></p> <p>This course provides a fundamental introduction to digital image processing. Upon successful completion, students will be able to thoroughly describe the basic concepts of image generation and representation. Additionally, they will acquire the skills to apply methods for enhancing and segmenting grayscale and color images in both the spatial and frequency domains, as well as techniques for image compression. Students will be capable of independently selecting, implementing, testing, and applying these techniques to complex image processing tasks. A typical application area is automation technology.</p> <p><b>Contents</b></p> <ol style="list-style-type: none"> <li>1. Introduction (Graphics File Formats, Application Examples, Human Vision) 2. Image Formation and Image Models (Camera Models, Image Formation, Image Sampling and Quantization)</li> <li>2. Image Enhancement in the Spatial Domain (Gray-Level Transformation Functions, Histogram Processing, Spatial Filtering)</li> <li>3. Image Enhancement in the Frequency Domain (2D Fourier Transform, Smoothing and Sharpening Filters, Implementation Details)</li> <li>4. Color Image Processing (Color Spaces, Color and Pseudo-Color Image Processing, Spatial Filtering)</li> <li>5. Image Compression and Reduction (Types of Redundancy, Compression Models, Lossless and Lossy Compression)</li> </ol>								
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• are able to describe the basics of image generation and image digitization and</li> <li>• are able to select, implement, test and apply methods for the enhancement of images in the spatial and frequency domain, image segmentation and data reduction independently for complex image processing tasks.</li> </ul> <p><b>Key qualifications</b></p> <p>The students have a good command of programming in Python.</p>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>								

### 3 Specialisation Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Markus Hennig</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Digital Image Processing I:</i></p> <p><b>Target group</b> Master's students in electrical engineering and related fields.</p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/get/teaching/dip-i">https://ei.uni-paderborn.de/get/teaching/dip-i</a></p> <p><b>Literature</b></p> <ul style="list-style-type: none"> <li>• Gonzalez, R., &amp; Woods, R. (2017). Digital Image Processing (4th Global Ed.). Pearson. Print ISBN: 978-1-292-22304-9, E-ISBN: 978-1-292-22307-0.</li> <li>• Mertsching, B. (2024). Digital Image Processing I (Lecture Notes).</li> <li>• Jähne, B. (2024). Digitale Bildverarbeitung (8th Edition, German Language). Springer. Print ISBN: 978-3-662-59509-1, E-ISBN: 978-3-662-59510-7.</li> </ul> <p><b>Comment</b> The material presented in the lecture is implemented in the exercises using Python. The first exercise provides an introduction to this, so that it is possible to get started with limited programming knowledge. Regular and active participation in lectures and exercises is expected.</p>

### 3 Specialisation Area

<b>Digital Image Processing II</b>							
Digital Image Processing II							
<b>Module number:</b> M.048.92010	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.92010 Digital Image Processing II	2L 2Ex, SS	60	120	C	30/30
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Digital Image Processing II:</i> <b>Recommended:</b> Basic knowledge of image processing, (e. g. from the course Digital Image Processing I (L.048.23002 / L.048.92008))						
4	<b>Contents:</b>  <i>Contents of the course Digital Image Processing II:</i> <b>Short Description</b> The course “Digital Image Processing II” is a module in the catalog “Cognitive Systems” for advanced students of the Electrical Engineering Master’s program and related degree programs. It follows the fundamental course “Digital Image Processing I” and covers methods for high-level image processing. <b>Contents</b> The following topics will be discussed during the semester: <ul style="list-style-type: none"><li>• Image segmentation (line and edge detection, segmentation by region, superpixels)</li><li>• Feature extraction (feature descriptors, principal components, Scale-Invariant-Feature-Transform (SIFT))</li><li>• Stereo image analysis (depth perception, stereo geometry, correspondence problem)</li><li>• Motion (motion detection, optical flow, motion models, motion segmentation)</li><li>• Object recognition and image pattern classification (patterns, classifiers, neural networks and deep learning, convolutional neural networks (CNN))</li></ul> After learning about the methods in the lecture, the students will implement them in Jupyter Notebooks.						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"> <li>• can apply methods for image segmentation, representation and description of features, stereo and motion image analysis, objection recognition and machine learning,</li> <li>• are able to transfer the acquired knowledge of image processing to the processing of other multi-dimensional signals,</li> <li>• are able to describe the state-of-the-art of the presented topics, and</li> <li>• are able to implement the presented methods.</li> </ul> <p><b>Key qualifications:</b> The students are able to identify and evaluate the function and the behavior of complex technical processes and their integration into the social environment while also considering ethical aspects.</p>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Bärbel Mertsching</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Digital Image Processing II:</i></p> <p><b>Course Homepage</b> [<a href="http://getwww.uni-paderborn.de/teaching/dip-II">http://getwww.uni-paderborn.de/teaching/dip-II</a>]</p> <p><b>Course Documents</b> see PANDA ([<a href="https://panda.uni-paderborn.de">https://panda.uni-paderborn.de</a>])</p> <p><b>References (excerpt)</b></p> <ul style="list-style-type: none"><li>• Mertsching, Bärbel: Digital Image Processing (lecture notes)</li><li>• Forsyth, David and Ponce, Jean: Computer Vision - A Modern Approach. Prentice-Hall, 2nd ed., 2011. ASIN: B006V372KG</li><li>• Gonzalez, Rafael C. and Woods, Richard E.: Digital Image Processing. Pearson Education Limited, 4th ed., 2018. ISBN-13: 978-1-292-22304-9</li><li>• Jähne, Bernd: Digitale Bildverarbeitung. Springer, 7. Aufl., 2012. ISBN-13: 978-3642049514</li></ul>
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### 3 Specialisation Area

<b>Digitale Sprachsignalverarbeitung</b>						
Digital Speech Signal Processing						
<b>Module number:</b> M.048.24001	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24001 Digital Speech Signal Processing	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Digitale Sprachsignalverarbeitung:</i> <b>Recommended:</b> Prior knowledge from the module Higher Mathematics.					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Digitale Sprachsignalverarbeitung:</i></p> <p><b>Short Description</b></p> <p>The course introduces the basic techniques and theories of digital speech signal processing. A focal point of the first part of the lecture is the topic “Listening and Speaking”, which is concerned with psychological effects of human sound perception and speech production. Subsequently, time discrete signals and systems, as well as computer based data processing are discussed. Further topics are non-parametric short-time analysis of speech signals, speech coding and IP-phones.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Listen and talk</li><li>• Generating voice: human vocal tract, source filter model, vocoder</li><li>• Acoustic waves</li><li>• Listen: human ear, psycho acoustics and physiology of listening, loudness, acoustic occlusion, frequency groups</li><li>• Time-discrete signals and systems</li><li>• Basics: Elementary signals, LTI systems</li><li>• Transformations: Fourier transformation of time-discrete signals, DFT, FFT</li><li>• Time-discrete filtering in frequency domain: Overlap-Add, overlap-Save</li><li>• Statistical speech signal analysis</li><li>• Basics in theory of probabilities</li><li>• Short-run analysis of speech signals: Spectrogram, cepstrum</li><li>• Estimation of speech signals</li><li>• Optimal filters</li><li>• LPC analysis</li><li>• Spectral filtering for noise suppression: spectral subtraction, Wiener filter</li><li>• Adaptive Filters: LMS adaptation algorithm, echo compensation</li><li>• Speech coding</li><li>• Time domain coding: signal shape coding, parametric coding, hybride coding techniques</li><li>• Frequency domain coding</li><li>• Amplitude quantization: uniform quantization, quantization with companders (ulaw, alaw)</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After attending the course, the students will be able to</p> <ul style="list-style-type: none"><li>• analyze digital signals, e.g., audio signals, in the time or frequency domain,</li><li>• represent audio signals efficiently and</li><li>• implement widely-used algorithms for speech analysis and speech processing in the frequency or time domain.</li></ul> <p><b>Key qualifications:</b></p> <p>The students</p> <ul style="list-style-type: none"><li>• are able to explain effects in real signals based on the theoretical knowledge,</li><li>• are able to investigate theoretical approaches by a systematic analysis and</li><li>• are, due to the precise treatment of the contents, in a position to continue their learning themselves</li></ul>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Dr.-Ing. Jörg Schmalenströer		
13	<b>Other Notes:</b> <i>Remarks of course Digitale Sprachsignalverarbeitung:</i> <b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/digital-speech-signal-processing">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/digital-speech-signal-processing</a> <b>Implementation</b> <ul style="list-style-type: none"> <li>• Lectures using the blackboard and presentations,</li> <li>• Alternating theoretical and practical exercise classes with exercise sheets and computer and</li> <li>• Demonstration of real technical systems in the lecture hall.</li> </ul> <b>Teaching Material, Literature</b> Allocation of a script; information on textbooks ; matlab scripts		

### 3 Specialisation Area

Integrierte Schaltungen für die drahtlose Kommunikation						
Integrated Circuits for Wireless Communications						
<b>Module number:</b> M.048.25017	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25017 Integrated Circuits for Wireless Communications	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Integrierte Schaltungen für die drahtlose Kommunikation:</i> <b>Recommended:</b> Lecture Schaltungstechnik resp. Circuit and System Design. Helpful supplement: Lecture "Wireless Communications" of Prof. Hab-Umbach.					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Mobile communications, wireless networks, and RFID technology are application examples of wireless communications. Wireless communications has found widespread use in everyday life and will become even more important in the future. The design of electronic circuits for radio frequencies requires a good system knowledge with respect to typical transmitter and receiver architectures in wireless communications, components, and radio signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to convey a methodical approach to the design of integrated circuits for wireless communications. A part of the exercises will pertain to calculation of circuit design problems another will be performed in small teams as a hands-on exercise using modern IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of radio frequency integrated circuits for wireless communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures “Schaltungstechnik” resp. “Circuit and System Design”. The following topics will be addressed:</p> <ul style="list-style-type: none"> <li>• Transmitter and receiver architectures for wireless communications</li> <li>• System Theory Basics             <ul style="list-style-type: none"> <li>– Signals and noise</li> <li>– Modulation and demodulation</li> <li>– Transmission properties of wireless communications systems</li> </ul> </li> <li>• Semiconductor technologies and integrated high-frequency devices</li> <li>• Amplifiers (low-noise and variable-gain amplifiers)</li> <li>• Mixers</li> <li>• Oscillators</li> <li>• Frequency synthesizer PLLs</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>The students will be able</p> <ul style="list-style-type: none"> <li>• to describe architectures and circuits of wireless communication systems</li> <li>• to describe and calculate fundamental signal transmission properties of wireless systems</li> <li>• to apply design methods to design components of radio frequency ICs</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                  <input type="checkbox"/> Module exam (MP)                  <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td style="text-align: center;">Oral Examination</td> <td style="text-align: center;">30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Oral Examination	30-45 min	100%
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a)	Oral Examination	30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								

### 3 Specialisation Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. J. Christoph Scheytt</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Course Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lecture with Powerpoint presentation and handwritten mathematical derivations using tablet and beamer</li> <li>• Exercises partly as handwritten calculation exercises using tablet and beamer and partly as practical IC design exercises using IC design software</li> </ul> <p><b>Teaching Material, Literature</b></p> <p>Lecture slides and videos as well as exercise slides will be made available.</p> <ul style="list-style-type: none"> <li>• Behzad Razavi "RF Microelectronics", Prentice Hall, 2011</li> <li>• Thomas Lee "The Design of CMOS Radio-Frequency Integrated Circuits", Cambridge University Press 2003</li> </ul>

### 3 Specialisation Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

### 3 Specialisation Area

5	<b>Learning outcomes and competences:</b> The students <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)										
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>							
	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%							
7	<b>Study Achievement:</b> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 5%; text-align: center;">zu</th> <th style="width: 55%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 20%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement										
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.										
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).										
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4										
12	<b>Module coordinator:</b> Prof. Dr. Heike Trautmann										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismail, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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### 3 Specialisation Area

<b>Machine Learning for Biometrics</b>							
Machine Learning for Biometrics							
<b>Module number:</b> M.079.4088	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	a)	L.079.05709 Machine Learning for Biometrics	form of teaching L2 Ex3	contact-time (h) 75	self-study (h) 105	status (C/CE) C	group size (TN) 70/35
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none						
4	<p><b>Contents:</b></p> <p><i>Contents of the course Machine Learning for Biometrics:</i></p> <p>Biometric verification is defined as the automated recognition of individuals based on their behavioral or biological characteristics. The course will give an overview of modern biometric systems and specifically address their functionality and challenges. For this purpose, various approaches of machine learning will be introduced, which aim at enabling reliable biometric recognition (e.g. by means of face recognition). At the same time, biometric applications place very specific requirements on the underlying algorithms. The course will specifically address these requirements and how they can be met algorithmically and in the algorithmic learning process. This includes the topics of privacy, fairness, explainability, uncertainties, efficiency, attacks and their automated detection.</p> <p>The course includes the following content:</p> <ul style="list-style-type: none"> <li>• Biometric systems, operation modes, and evaluation</li> <li>• Recap on traditional and deep learning</li> <li>• Face, Iris, and fingerprint recognition</li> <li>• Soft-biometrics and privacy</li> <li>• Fairness and bias in biometric systems</li> <li>• Explainability and confidence in biometric systems</li> <li>• Biometric sample quality</li> <li>• Efficient biometric systems</li> <li>• Presentation attacks and detection</li> <li>• Multi-biometric fusion</li> <li>• Biometric indexing</li> </ul>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• independently evaluate biometric systems,</li> <li>• train biometric recognition models for different modalities,</li> <li>• automatically detect biometric attacks and make systems robust against such attacks,</li> <li>• explain various challenges of biometric systems and name solution strategies to counter them,</li> <li>• name and explain open research questions in biometrics.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4)</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Philipp Terhörst</p>										

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning for Biometrics:</i></p> <p><b>Implementation Method</b></p> <p>First, students are given an overview of biometrics and its applications and basic functionalities. Then, required concepts of machine learning are introduced in a compact way. These will be applied and developed in context when dealing with specific biometric requirements. Parallel to the lecture, the theoretical concepts are practiced in the exercises using facial data. This is done in the form of short hand-written and implementation tasks.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Anil K. Jain, Patrick Flynn, and Arun A. Ross. 2010. Handbook of Biometrics (1st. ed.). Springer Publishing Company, Incorporated.</li><li>• Further literature will be announced in the lecture.</li></ul>
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### 3 Specialisation Area

Optimale und Adaptive Filter						
Optimal and Adaptive Filters						
<b>Module number:</b> M.048.24010	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24010 Optimal and Adaptive Filters	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Optimale und Adaptive Filter:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics and Digital Signal Processing.					

4	<p><b>Contents:</b></p> <p><i>Contents of the course Optimale und Adaptive Filter:</i></p> <p><b>Short Description</b></p> <p>The course “Optimal and adaptive filters” gives an introduction to the basic techniques and theories of adaptive filters. Based upon the basics of estimation theory optimal filters are discussed. Subsequently the topics Wiener filter theory, deterministic optimization under constraints and stochastic gradient methods are regarded. Concluding the Least Squares approach for solving filter tasks and the Kalman filter are introduced. The latter is regarded as a brief introduction to state based filters.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Classic parameter estimation</li><li>• Estimators</li><li>• MMSE-Estimation</li><li>• Linear estimators</li><li>• Orthogonality principle</li><li>• Evaluation of estimators</li><li>• Wiener filter</li><li>• Wiener-Hopf equation</li><li>• AR- and MA processes</li><li>• Linear prediction</li><li>• Iterative optimization methods</li><li>• Gradient ascent/descent</li><li>• Newton method</li><li>• Linear adaptive filters</li><li>• LMS algorithm</li><li>• Least-Squares method</li><li>• Blockwise and recursive adaptiv filters</li><li>• Realization aspects</li><li>• Statemodel based filters</li><li>• Kalman filter</li><li>• Applications</li><li>• System identification</li><li>• Channel estimation and equalization</li><li>• Multi-channel speech signal processing</li><li>• Noise and interference suppression</li></ul>
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### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• analyze task on the field of adaptive filters and to formulate requirements mathematically,</li> <li>• develop filter using cost functions and</li> <li>• implement selected adaptive filters in the frequency or time domain.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to check theoretical results using practical realizations,</li> <li>• are able to undertake theoretical approaches a systematic analysis using methodical procedures and</li> <li>• are, due to the precise treatment of the contents, in a position to continue their learning themselves.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)              <input type="checkbox"/>Module exam (MP)              <input type="checkbox"/>Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Jörg Schmalenströer</p>								

### 3 Specialisation Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optimale und Adaptive Filter:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/optimal-and-adaptive-filter">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/optimal-and-adaptive-filter</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures using the blackboard and presentations,</li><li>• Alternating theoretical and practical exercises classes with exercise sheets and computer and</li><li>• Demonstration of real technical systems in the lecture hall.</li></ul> <p><b>Teaching Material, Literature</b> Allocation of a script; information on textbooks; matlab scripts</p>
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### 3 Specialisation Area

Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation						
Fast Integrated Circuits for Wireline Communications						
<b>Module number:</b> M.048.25019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25019 Fast Integrated Circuits for Wireline Communications	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Recommended:</b> Module "Schaltungstechnik" of the Bachelor Electrical Engineering or module "Circuit and System Design" of the Master "Electrical Systems Engineering" or comparable modules / lectures					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Nowadays commercial fiber-optic communication systems reach very high data rates of 100 Gb/s per optical channel and several Tb/s in a single fiber. In a similar way very high data rates of more than 10 Gb/s occur at a single package pin of electronic chips. These signals are to be transmitted over printed circuit boards and inexpensive serial cables. In the future the progress of CMOS technology and communication technology will push speed of fiber-optic and wire-line communication continuously to ever higher data rates. The design of electronic circuits for high bandwidth resp. data rates requires a good system knowledge with respect to typical transmitter and receiver architectures, components, and signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to enable the student to utilize a methodological approach for the design of fast integrated electronic circuits for digital wired communications. A part of the exercises will be carried out using modern industry-standard IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of fast integrated electronic circuits for digital broadband communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures "Schaltungstechnik" resp. "Circuit and System Design". The lecture deals with:</p> <ul style="list-style-type: none"><li>• Transmitter and receiver architectures for fiber-optic communications</li><li>• Transmitter and receiver architectures for chip-to-chip communications</li><li>• System design</li><li>• Semiconductor technology and integrated high-frequency devices</li><li>• Broadband amplifiers</li><li>• Current-mode logic</li><li>• Transmitter and receiver circuits</li><li>• PLLs for frequency synthesis and clock recovery</li><li>• Measurement methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>The student will be able to:</p> <ul style="list-style-type: none"><li>• describe and analyze transmitter and receiver architectures for broadband communication links</li><li>• understand and describe semiconductor technologies and integrated high-frequency devices for broadband circuits</li><li>• to analyze circuit design techniques for transmitter and receiver circuits and describe ways to optimize them</li><li>• to describe circuits in PLL technique for frequency synthesis and clock recovery</li><li>• to describe measurement methods</li></ul> <p><b>Key qualifications:</b></p> <p>The students will learn how different interdisciplinary scientific domains and their methods - like mathematical signal and system analysis, non-linear and linear circuit analysis, semiconductor physics, semiconductor devices and high-frequency engineering - are applied together for the development of communications application.</p>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Oral Examination	30-45 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. J. Christoph Scheytt		
13	<b>Other Notes:</b> <i>Remarks of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Course Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/</a> <b>Implementation</b> Lecture with Exercises (including computer-aided design using electronic design software) <b>Teaching Material, Literature</b> Handouts and literature references will be given in the lecture. <ul style="list-style-type: none"> <li>• E. Säckinger, "Broadband Circuits for Optical Fiber Communication", Wiley, 2005</li> <li>• B. Razavi, "Design of Integrated Circuits for Optical Communications", McGraw-Hill, 2003</li> </ul> <b>Comments</b> As part of the lecture a 2-day excursion to IHP Leibnizinstitute for High-Performance Microelectronics in Frankfurt (Oder) is offered which includes the visit of a modern chip fabrication facility (participation in the excursion is voluntary).		

### 3 Specialisation Area

<b>Statistical and Machine Learning</b>							
Statistical and Machine Learning							
<b>Module number:</b> M.048.23012	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.23012 Statistical and Machine Learning	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Statistical and Machine Learning:</i> <b>Recommended:</b> Elementary knowledge in probability theory, as is taught in the course Statistical Signal Processing. Basic programming skills are desirable.						
4	<b>Contents:</b>  <i>Contents of the course Statistical and Machine Learning:</i> <b>Short Description</b> The course on Statistical and Machine Learning presents an introduction into the components and algorithms prevalent in statistical and machine learning. Modern techniques will be presented for gleaning information from data. Both supervised and unsupervised learning algorithms will be discussed. The presented techniques can be applied to a variety of classification and regression problems, both for one-dimensional input data (e.g., speech), two-dimensional (e.g., image) or symbolic input data (e.g., documents). <b>Contents</b> <i>Introduction to classification problems, Bayesian and other decision rules Optimization: gradient descent, algorithmic differentiation, optimization with constraints Linear classifiers, Support Vector Machines Deep neural networks (deep learning) Dimensionality reduction (PCA, LDA) Unsupervised learning (mixture densities, clustering techniques)</i>						

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the module students will be able to</p> <ul style="list-style-type: none"> <li>• Find an appropriate approach to solving a given classification or regression problem</li> <li>• Apply supervised or unsupervised learning techniques to data of various kinds and critically assess the outcome of the learning algorithms</li> <li>• Can appreciate the power and limitations of machine learning algorithms</li> <li>• Work with software for solving machine learning problems and write own software components, apply them to given data sets and optimize parameter settings</li> <li>• Find, for a given training set size, an appropriate choice of classifier complexity und feature vector dimensionality</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• Have gathered sufficient proficiency in Python, which is valuable well beyond this course</li> <li>• Can assess the importance of the principle of parsimony and are able to transfer it to other</li> <li>• Are able to analyse a given classification or regression problem, synthesize a solution, and evaluate the performance on test data</li> <li>• Are able to apply the knowledge and skills learnt in this course to a wide range of disciplines</li> <li>• Can work cooperatively in a team and subdivide an overall task into manageable subtasks and work packages</li> <li>• Acquired a general understanding of the power and limitations of machine learning algorithms</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								

### 3 Specialisation Area

11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Mohammad Soleymani</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Statistical and Machine Learning:</i></p> <p><b>Course Homepage</b>  <a href="https://ei.uni-paderborn.de/en/statistical-and-machine-learning">https://ei.uni-paderborn.de/en/statistical-and-machine-learning</a></p> <p><b>Implementation</b>  <i>Lectures predominantly using the blackboard or overhead projector, occasional presentations of (powerpoint) slides , Exercise classes with exercise sheets and demonstrations on computer *Implementation of learning and classification algorithms on a computer by the students themselves; use of algorithms on real-world data or data generated on the computer, evaluation of the simulation results</i></p> <p><b>Teaching Material, Literature</b>          Course script and summary slides are provided to the students. Exercises and solutions to exercises, as well as sample implementations of algorithms are provided to the students  <i>R.O. Duda, P.E. Hart, D.G.~ Stork, Pattern Classification, Wiley, 2001</i> I. Goodfellow, Y. Bengio, A. Courville: Deep Learning, MIT Press, 2016 S. Theodoridis: Machine Learning, Academic Press, 2015 K. Fukunaga, Introduction to Statistical Pattern Recognition, Academic Press, 1990</p>

### 3 Specialisation Area

Technische kognitive Systeme - Ausgewählte Kapitel						
Cognitive Systems Engineering - Special Topics						
<b>Module number:</b> M.048.23019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.23019 Cognitive Systems Engineering - Special Topics	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> two out of three topics, see point 4					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Technische kognitive Systeme - Ausgewählte Kapitel:</i> <b>Recommended:</b> Interest in the subject-matter and interdisciplinary work.					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p>This module is offered in three parts. Students have to choose <b>two out of three</b>. Each part lasts two hours per week.</p> <p><i>Contents of the course Technische kognitive Systeme - Ausgewählte Kapitel:</i></p> <p><b>Part A</b></p> <p>At any given time, the sensory receptors of living beings are exposed to a very large amount of information, of which only a small proportion can be consciously processed. Visual attention is understood as the pooling of available cognitive resources for optimal processing of visual stimuli. The seminar introduces the modeling and experimental investigation of visual attention and the transfer to intelligent technical systems. It will be shown how research can be conducted jointly across disciplinary boundaries. The current focus is on the topic of saliency. The course always takes place in the winter semester.</p> <p><b>Part B</b></p> <p>While “sensation” describes the signals from the physical world that reach our sensory receptors, “perception” refers to the processes by which our brain selects, organizes, and interprets the signals. This seminar provides students in technical courses with an overview of the fundamentals of biological sensory systems and perception. In addition to the exciting and (sometimes non-intuitive) background of these topics, there will be a critical discussion of the transferability of biological concepts and mechanisms to technical systems. This seminar is always in the summer semester.</p> <p><b>Part C</b></p> <p>In this seminar, current interim reports and results from ongoing bachelor’s and master’s theses, research projects, and third-party funded projects from the GETLab - Technical Cognitive Systems department will be presented. Furthermore, there will be presentations by guests of the research group. The seminar is offered in the summer and winter semester.</p>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"><li>• are able to name basic research topics related to the design and the implementation of technical cognitive systems,</li><li>• can apply and evaluate technical cognitive systems, and</li><li>• are able to understand, design, implement and evaluate basic psychophysical experiments.</li></ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"><li>• are able to research and evaluate technical literature,</li><li>• have developed an understanding of the discipline-related research approaches (computer science, electrical engineering, psychology) and</li><li>• are able to carefully consider the potential use of bio-inspired mechanisms in technical systems.</li></ul>

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4			
12	<b>Module coordinator:</b> Prof. Dr. Bärbel Mertsching			
13	<b>Other Notes:</b> <b>Module Homepage</b> <a href="http://getwww.uni-paderborn.de/teaching/cse">[http://getwww.uni-paderborn.de/teaching/cse]</a> <b>Teaching Material, Literature</b> Literature references will be given at the first dates of the seminar. <i>Remarks of course Technische kognitive Systeme - Ausgewählte Kapitel:</i> <hr style="width: 20%; margin-left: auto; margin-right: auto;"/> <b>ATTENTION - IMPORTANT NOTICE</b> The course doesn't take place in winter term 2024/25. Please see the notice boards of the group. <hr style="width: 20%; margin-left: auto; margin-right: auto;"/>			

### 3 Specialisation Area

<b>Topics in Pattern Recognition and Machine Learning</b>						
Topics in Pattern Recognition and Machine Learning						
<b>Module number:</b> M.048.92030	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92030 Topics in Pattern Recognition and Machine Learning	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Topics in Pattern Recognition and Machine Learning:</i> <b>Recommended:</b> Elementary knowledge in Probability Theory, as is taught in the module Statistical Signal Processing. Desirable, but not mandatory: knowledge in the field of statistical and machine learning; basic programming skills					

### 3 Specialisation Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Topics in Pattern Recognition and Machine Learning:</i></p> <p><b>Short Description</b></p> <p>The course on Topics in Pattern Recognition and Machine Learning first briefly summarizes the main concepts of statistical pattern recognition and machine learning. Next selected topics will be presented in detail. The choice of topics depends on current research activities and thus may change over time. Examples of such topics to be studied in detail include</p> <ul style="list-style-type: none"><li>• Deep Learning</li><li>• Model estimation in the presence of hidden variables, in order to reveal suspected latent structure buried in the data</li><li>• Bias-Variance dilemma and the tradeoff between degree of detail and generalizability of models</li><li>• Graphical models</li><li>• Sequential data and hidden Markov models</li><li>• Decision trees, model combination</li><li>• Specific classification tasks, such as automatic speech recognition</li></ul> <p>While the first part of the course will follow a regular lecture format, the second part will include active student participation. Students will be asked to read, analyze and present recently published papers from the pattern recognition and machine learning literature. This will often also include the implementation of proposed algorithms in Matlab.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Fundamentals of statistical pattern recognition: Bayes rule, learning of class-conditional densities, linear models for classification and regression</li><li>• Deep neural networks: MLP, CNN, RNN and others</li><li>• EM Algorithm and extensions thereof</li><li>• Models with discrete or continuous latent variables; GMM, NMF</li><li>• Bias-Variance dilemma and model selection</li><li>• Graphical models</li><li>• Hidden Markov models and their application in speech recognition</li><li>• Decision trees, model combination</li><li>• Recent publications in pattern recognition and machine learning</li></ul>
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### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to * Choose an appropriate classifier for a given classification problem and be able to learn the parameters of the classifier from training data</p> <ul style="list-style-type: none"> <li>• Choose an appropriate regression method for function approximation and learn its parameters from training data</li> <li>• Search for latent variables and structure in given data</li> <li>• Make an informative choice for the model order to find a good compromise between degree of detail and generalizability</li> <li>• Comprehend and analyze recent publications from the field of pattern recognition and machine learning</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• Have gathered an understanding of the importance of the chosen model order on the outcome of classification and regression tasks</li> <li>• Are aware of the impact of a priori assumptions on the result of latent variable and structure discovery in data</li> <li>• Are able to autonomously gain expertise in a certain field of pattern recognition by conducting a literature survey</li> <li>• Can gauge the importance of a given publication for the state of the art in a field</li> <li>• Are able to apply the knowledge and skills learnt in this course to a wide range of disciplines</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								

### 3 Specialisation Area

11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Häb-Umbach</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Topics in Pattern Recognition and Machine Learning:</i></p> <p><b>Course Homepage</b>  <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/topics-in-pattern-recognition-and-maschine-learning">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/topics-in-pattern-recognition-and-maschine-learning</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lectures predominantly using the blackboard or overhead projector, occasional presentations of (powerpoint) slides ,</li> <li>• Exercise classes with exercise sheets and demonstrations on computer</li> <li>• Instructions how to read and analyze scientific publications in this field Autonomous analysis of publications and presentation of results and gained insight</li> </ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"> <li>• R.O. Duda, P.E. Hart, D.G.~ Stork, Pattern Classification, Wiley, 2001</li> <li>• I. Goodfellow, Y. Bengio, A. Courville, Deep Learning, MIT Press, 2016</li> <li>• C. M. Bishop, Pattern Recognition and Machine Learning, Springer, 2006</li> </ul>

### 3 Specialisation Area

Topics in Signal Processing							
Topics in Signal Processing							
<b>Module number:</b> M.048.92014	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92014 Topics in Signal Processing	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Topics in Signal Processing:</i> <b>Recommended:</b> Signal and system theory, at least a basic understanding of probability and linear algebra						
4	<b>Contents:</b>  <i>Contents of the course Topics in Signal Processing:</i> <b>Short Description</b> This course covers a selection of current topics in signal processing. One part of this course will follow a regular lecture format, while the other part will require active student participation. <b>Contents</b> This course will first review relevant aspects of linear algebra and probability theory. Then students will learn how to read, analyze, and present recent papers from the signal processing literature.						
5	<b>Learning outcomes and competences:</b>  In this course, students will familiarize themselves with some current research topics in signal processing. They will learn to read and understand scientific publications and to critically evaluate results. Students will develop confidence in their ability to solve mathematical problems of analysis and design. They will be able to apply the principles they have learnt in this course to other areas.						

### 3 Specialisation Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> Prof. Dr. Peter Schreier		
13	<b>Other Notes:</b> <i>Remarks of course Topics in Signal Processing:</i> <b>Course Homepage</b> <a href="http://sst.uni-paderborn.de/teaching/courses/">http://sst.uni-paderborn.de/teaching/courses/</a> <b>Implementation</b> Lectures and tutorials with active student participation, student presentations <b>Teaching Material, Literature</b> References will be given in the first lecture.		

### 3 Specialisation Area

Wireless Communications						
Wireless Communications						
<b>Module number:</b> M.048.24022	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2.-4. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24022 Wireless Communications	2L 2Ex, SS	60	120	CE	30 / 30
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> <i>Prerequisites of course Wireless Communications:</i> <b>Recommended:</b> Prior knowledge from the modules Signal and Information Transmission, Signal Theory, and Probability for Engineers.					
4	<b>Contents:</b> <i>Contents of the course Wireless Communications:</i> The course provides students with important basic knowledge necessary for understanding the transmission concepts of modern mobile communication systems. Starting with a physical understanding of the mobile communications channel, in particular the phenomena of multipath propagation and the Doppler effect, the physical and statistical modeling of time-varying communication systems is presented. Subsequently, important diversity techniques for channel-adaptive transmission and multiple access techniques are introduced.					

### 3 Specialisation Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to</p> <ul style="list-style-type: none"> <li>• understand the fundamental propagation mechanisms in mobile radio channels;</li> <li>• describe and apply physical and statistical modeling principles to time-varying communication channels;</li> <li>• analyze fading processes and assess their impact on transmission reliability;</li> <li>• explain diversity techniques, apply them to design physical layer signal processing algorithms, and assess their impact on transmission reliability;</li> <li>• explain, compare, and evaluate common multiple access schemes;</li> <li>• assess transmission concepts employed in contemporary mobile communication systems;</li> <li>• connect theoretical channel models with practical system design considerations.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• have strong analytical and mathematical reasoning skills;</li> <li>• have the ability to abstract complex physical phenomena into engineering models;</li> <li>• can interpret analytical results at the system level;</li> <li>• have experience in clear and precise technical communication (oral and written)</li> <li>• can learn independently and engage critically with technical literature.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5)</p>								

### 3 Specialisation Area

12	<b>Module coordinator:</b> Dr.-Ing. Bho Matthiesen
13	<b>Other Notes:</b> <b>Module Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/nt">https://www.hni.uni-paderborn.de/en/nt</a> <b>Implementation</b> Lectures and exercises <b>Teaching Material, Literature</b> Lecture slides and reading assignments for each lecture; additional reading suggestions.

## 4 General Elective Area

### 4.1 EE Catalogue Energy and Environment

Bauelemente der Leistungselektronik						
Power Electronic Devices						
<b>Module number:</b> M.048.22003	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.22003 Power Electronic Devices		60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Bauelemente der Leistungselektronik:</i> <b>Recommended:</b> Knowledge from lecture Power Electronics is desirable.					

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Bauelemente der Leistungselektronik:</i></p> <p><b>Short Description</b> The course covers power electronic devices, snubber circuits, driving and cooling. Another topic is the design of magnetic components and fast current sensors.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"> <li>• power electronic devices: Diodes, BJT, GTO, MOSFET, IGBT</li> <li>• snubbers, driving and protection of semiconductor switches; cooling systems</li> <li>• magnetic materials, test circuit for core losses, winding patterns</li> <li>• concept of integrated magnetics</li> <li>• electromechanical design and modelling of uncoupled, linear coupled, nonlinear coils and SMPS transformers</li> <li>• capacitors</li> <li>• filters</li> <li>• dynamic current sensing</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to choose suitable power semiconductors, magnetic materials and core forms</li> <li>• to select and dimension snubber circuits, current sensors and drivers for power semiconductors</li> <li>• to design magnetic components and power filters</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• learn to describe real components with an equivalent circuit</li> <li>• improve their skills in computer aided circuit design</li> <li>• extend their competence by self study</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								

#### 4 General Elective Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5</p>
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Frank Schafmeister</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Bauelemente der Leistungselektronik:</i></p> <p><b>Course Homepage</b>  <a href="http://www.lea.upb.de">http://www.lea.upb.de</a></p> <p><b>Implementation</b>  <i>lecture exercise</i></p> <p><b>Teaching Material, Literature</b>  Lecture slides and notes, further literatur will be announced in lecture.</p>

#### 4 General Elective Area

Datengetriebenes Ressourcenmanagement						
Data-driven Resource Management						
<b>Module number:</b> M.104.7420	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3.	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.104.61231 Data-driven Resource Management	L2 S2	60	120	C	30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Datengetriebenes Ressourcenmanagement (ET):</i>            In this course, students work together with lecturers to develop interdisciplinary solutions for the interfaces between the topics of IIOT (Industrial Internet of Things) and resource management. The objectives are, for example, the use of industrial processes and energy storage systems to shift energy flows over time (supporting the energy transition) and increasing internal resource and energy efficiency. The thematic focus is on energy and material requirements in industry. There are already a number of technical approaches to this, which will be improved or combined within the event. Basic components of the course (varies depending on the semester assignment):</p> <ul style="list-style-type: none"> <li>• Fundamentals of resource management and efficiency</li> <li>• Fundamentals of energy management and efficiency, decentralized energy supply in industry, potentials of energy storage</li> <li>• Measuring technology</li> <li>• Fundamentals of data management</li> <li>• Management of product data and product life cycle</li> <li>• Finding creative solutions</li> <li>• Presentation of the issues</li> <li>• Carrying out project planning as a group</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Firstly, students acquire the necessary specialised knowledge (see contents). Subsequently, problems are formulated together or specified by the lecturer. The students develop solutions (preferably in groups) to increase sustainability in production or the factory. This can be, for example, a technical (possibly patentable) solution, a prototype for a product, a business idea for a spin-off or an in-depth study. The procedure promotes communication skills as well as teamwork and the ability to reflect.</p>										
6	<p><b>Assessments:</b></p> <p><input type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input checked="" type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td></td> <td></td> <td style="text-align: center;">40% / 60%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)			40% / 60%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)			40% / 60%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b></p> <p>Univ.-Prof. Dr.-Ing. Alexander Schlüter</p>										
13	<p><b>Other Notes:</b></p> <p>None</p> <p><i>Remarks of course Datengetriebenes Ressourcenmanagement (ET):</i> The assignments should be held in blocks by mutual agreement.</p>										

#### 4 General Elective Area

Circular Economy						
Circular Economy						
<b>Module number:</b> M.104.7422	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3.	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.104.61241 Circular Economy	L2 S2	60	120	C	12
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Circular Economy (ET):</i> none					
4	<b>Contents:</b> <i>Contents of the course Circular Economy (ET):</i> The contents of this module revolve around the interdisciplinary development of circular economy (CE) solutions in the field of energy. The subtopics of the semester will be announced on the department's homepage (go.upb.de/NIWI_1) no later than the start of registration. Examples could be: CE for batteries, CE for transformers, CE for Photovoltaic systems. The basic components are: <ul style="list-style-type: none"> <li>• Basics of Circular economy</li> <li>• Energy systems and its components</li> <li>• Assignments on circular economy in the selected subtopics of the energy branch</li> <li>• Semester-specific subtopic of CE in the energy field</li> <li>• Elaboration of the problem or need</li> <li>• Carrying out project planning</li> <li>• Feedback and optimization</li> </ul>					
5	<b>Learning outcomes and competences:</b> Students acquire specialist knowledge on the subtopic of the semester (see contents). For this purpose, problems are formulated together or given by the lecturer. The students develop solutions for a more sustainable coexistence or a more sustainable economy. This can e.g. be a technical (possibly patent-worthy) solution, a prototype for a product, a business idea for a spin-off or even an in-depth job. The approach promotes communicative skills as well as the ability to work in a team and reflect on solutions for a specific CE approach.					

#### 4 General Elective Area

6	<b>Assessments:</b> <input type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input checked="" type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)			40%/60%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examinations (MTP) were passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Univ.-Prof. Dr.-Ing. Alexander Schlüter		
13	<b>Other Notes:</b> None  <i>Remarks of course Circular Economy (ET):</i> The assignments should be held in blocks by mutual agreement.		

#### 4 General Elective Area

Design of Energy Transition Scenarios							
Design of Energy Transition Scenarios							
<b>Module number:</b> M.048.22020	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22020 Design of Energy Transition Scenarios	2L, 2EX, WS	60	120	P	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Design of Energy Transition Scenarios:</i> None						
4	<b>Contents:</b>  <i>Contents of the course Design of Energy Transition Scenarios:</i> Building on a fundamental understanding of energy systems, the course covers the basics of designing energy transition scenarios. To this end, integrated energy systems and, in particular, concepts of sector coupling are first repeated. In addition, the basics of scenario techniques in the context of the energy transition are introduced. On this basis, common concepts of modelling and simulation of energy systems are dealt with and common simulation software is introduced. The theory is deepened using practical examples and open source solutions for energy system planning. Energy transition scenarios are developed, calculated and evaluated from scratch for selected regions.						
5	<b>Learning outcomes and competences:</b>  By participating in the course, students will be able to design energy transition scenarios and calculate and evaluate them using common simulation software. Basic concepts of sector coupling as well as techniques for scenario building and energy system planning are learnt and can be applied.						

4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik			
12	<b>Module coordinator:</b> Prof. Dr. Henning Meschede			
13	<b>Other Notes:</b> none			

#### 4 General Elective Area

Energiesystemtechnik							
Energy System Technologies							
<b>Module number:</b> M.048.22018	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22018 Energy System Technologies	2L 2Ex, SS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Energiesystemtechnik:</i> None						
4	<b>Contents:</b>  <i>Contents of the course Energiesystemtechnik:</i> The field of energy system technologies includes the holistic consideration of thermal, electrical and chemical energy systems, consisting of the provision of useful energy, energy distribution and energy demand. In this course the basics of energy systems are taught. Based on the descriptions of the essential individual components, the interaction of these components is analyzed with regard to the coverage of the energy demand. Accordingly, aspects of sector coupling as well as storage technologies are introduced as components of energy systems. In addition to the technical description and design of energy systems, ecological and economic aspects for the holistic evaluation of energy systems are presented.						
5	<b>Learning outcomes and competences:</b>  Students can assess energy systems holistically, in particular they can analyze and design energy systems in terms of energy demand. They know the individual components and are able to design them both technically and to evaluate the interactions in the context of the overall system as well as in meaningful subsystems. Students are able to quantify energy systems using energy, ecological and economic indicators. They know aspects of renewable energies, energy storage and sector coupling and can apply these to questions of sustainable energy systems.						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5			
12	<b>Module coordinator:</b> Prof. Dr. Henning Meschede			
13	<b>Other Notes:</b> none			

#### 4 General Elective Area

Energy Transition						
Energy Transition						
<b>Module number:</b> M.048.22014	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.22014 Energy Transition	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Energy Transition:</i> None					

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Energy Transition:</i></p> <p><b>Short Description</b></p> <p>With dependency and the long-term depletion of fossil energy resources such as coal, oil, gas, the increasing climate crises, and the shut-down of the nuclear programs in many countries, the necessity to set-up an energy structure based on renewable energies with often fluctuating power output is a vast challenge for electrical engineering. This lecture faces that challenge explaining the functioning and performance parameters of all types of renewable energy conversion devices, their availability, interaction and adaptability to load structures. Vice versa, the adaptability of load curves to the availability of the energy sources shall be presented, including new concepts, e.g. decentralized generation, storage and energy management, in particular Demand-Side-Management, P2X.</p> <p><b>Contents</b></p> <ol style="list-style-type: none"> <li>1. Existing energy structures: History, development</li> <li>2. Present components &amp; systems: generation, transport, consumption</li> <li>3. Characteristics of variable renewable energy sources: solar thermal, photovoltaics, wind power</li> <li>4. Characteristics of renewable energy sources: hydro &amp; wind power</li> <li>5. Characteristics of steady renewable energy sources: biomass, geothermal energy</li> <li>6. Individual and combined availability and performance</li> <li>7. Energy management, transport (smart grid) and storage necessities</li> <li>8. Storage devices and concepts: types, performance, costs</li> <li>9. New concepts to minimize costs: decentralized, autonomous and semi-autonomous systems, swarm concepts, demand side management, (DSM), power to gas &amp; heat (P2X)</li> <li>10. Geographical differences: Local resources, potentials, load structures</li> <li>11. Legislative issues: access to grid &amp; electricity spot-market</li> <li>12. Excursion to practical project examples</li> </ol>								
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After completing the course the students should in a position to: understand the implications, necessities and properties of an energy supply system (energy system 2.0) based on the combination of different renewable energy sources, distribution, storage, demand side management and be familiarized with the components, its specific characteristics and parameters.</p> <p><b>Key qualifications:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• are enabled to apply the knowledge and skills across disciplines</li> <li>• are enabled to use method-oriented approaches for the implementation of sustainable energy supply</li> <li>• are enabled to educate themselves in the future</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written Examination</td> <td style="text-align: center;">120-180 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written Examination	120-180 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written Examination	120-180 min	100%						

#### 4 General Elective Area

7	<p><b>Study Achievement:</b></p> <p>none</p>
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Stefan Krauter</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Energy Transition:</i></p> <p><b>Course Homepage</b>  <a href="https://panda.uni-paderborn.de/course/view.php?id=40584">https://panda.uni-paderborn.de/course/view.php?id=40584</a>  <a href="http://www.nek.upb.de/lehre">http://www.nek.upb.de/lehre</a></p> <p><b>Implementation</b>  Lecture combined with practical examples &amp; simulations; Excursion to see applications in practice.</p> <p><b>Teaching Material, Literature</b>  All presentations and exercises plus additional resources are available on PANDA  Playlist for the videos of the lecture: <a href="https://youtube.com/playlist?list=PLpqi7D_IhqIrd37mBky0fSoKb9hvfutE9">https://youtube.com/playlist?list=PLpqi7D_IhqIrd37mBky0fSoKb9hvfutE9</a></p> <p>Literature  <i>Stephen W. Fardo, Dale R. Patrick: Electrical Power Systems Technology. The Fairmont Press, Inc., 2009. Michel Crappe: Electric Power Systems. John Wiley &amp; Sons, 2008. Magdi S. Mahmoud: Decentralized Systems with Design Constraints. Springer: Berlin Heidelberg, New York, 2011. Hermann Scheer, The Energy Imperative, 100 Percent Renewable Now. Routledge, 2011. Hermann Scheer: Energy Autonomy. Earthscan/James &amp; James, 2006. Geert Verbong, Derk Loorbach: Governing the Energy Transition - Reality, Illusion or Necessity?, Routledge, 2012 Fraunhofer ISE: Actual Facts for PV <a href="https://www.ise.fraunhofer.de/de/veroeffentlichungen/studien/aktuelle-fakten-zur-photovoltaik-in-deutschland.html">https://www.ise.fraunhofer.de/de/veroeffentlichungen/studien/aktuelle-fakten-zur-photovoltaik-in-deutschland.html</a> Solar Power Europe, PV Outlook 2022-26: <a href="https://www.solarpowereurope.org/insights/market-outlooks/global-market-outlook-for-solar-power-2022">https://www.solarpowereurope.org/insights/market-outlooks/global-market-outlook-for-solar-power-2022</a> *Journals: Renewable Energy, Elsevier; IEEE Transactions on Power Systems</i></p> <p><b>Comments</b>  Excursion to a practical project (e.g., pumped hydro storage (PHS))</p>

#### 4 General Elective Area

Geregelte Netzumrichter						
Control of Grid-Tied Converters						
<b>Module number:</b> M.048.22021	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.22021 Control of Grid-Tied Converters	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Geregelte Netzumrichter:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• Basics of Power Electronics (recommended)</li> <li>• Basics of Control Theory (recommended)</li> </ul>					
4	<b>Contents:</b>  <i>Contents of the course Geregelte Netzumrichter:</i> The goal of this module is to develop application-oriented skills regarding the operation of grid-tied converters. The focus is on the operational principles, requirements, and control. <ul style="list-style-type: none"> <li>• grid codes and other requirements</li> <li>• three-phase and single-phase voltage converter topologies for grid operation</li> <li>• modeling the inverter components for simulation purposes</li> <li>• voltage and current transformations utilized for control</li> <li>• the control algorithms for these topologies</li> <li>• different PLLs and their properties</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After successfully completing the module, students will be able to</p> <ul style="list-style-type: none"> <li>• explain the functionality of grid-tied converters</li> <li>• set up a simulation model for the target application</li> <li>• design a suitable control system for the grid operation of the single-phase and three-phase inverters and implement it as a simulation model</li> <li>• explain essential grid code requirements</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Jakub Kucka</p>										
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="https://ei.uni-paderborn.de/en/lea/lehre/veranstaltungen/teaching">https://ei.uni-paderborn.de/en/lea/lehre/veranstaltungen/teaching</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>										

4 General Elective Area

Intelligent Control of Electricity Grids						
Intelligent Control of Electricity Grids						
<b>Module number:</b> M.048.22002	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.22002 Intelligent Control of Electricity Grids	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Intelligent Control of Electricity Grids:</i> None					
4	<b>Contents:</b> <i>Contents of the course Intelligent Control of Electricity Grids:</i> <i>Dynamic properties of important energy converters also and especially in interaction with the grid. Classical control of island and interconnected grids as well as Future requirement profiles for automated grid control with decentralized feeders. Optimal economic load distribution Descriptions of networks for use in automated network control centers. Estimation of system states using linear and nonlinear methods (State Estimation). Estimation of system states based on measurements: Possibilities to detect and eliminate grossly incorrect measurement errors. *special questions in the environment of the topic</i>					
5	<b>Learning outcomes and competences:</b> <b>Specialized competence:</b> <ul style="list-style-type: none"> <li>• In this module, students learn about the problems of today's and the objectives and requirements of future automated power supply systems. For this purpose, special, representative questions are used as examples, with which important problems of future grids can also be discussed.</li> <li>• Current events in and around the "automation of electrical grids" will be discussed as a matter of course in order to assess the course content.</li> </ul>					

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5		
12	<b>Module coordinator:</b> Fette, Michael, Dr. –Ing. habil.		
13	<b>Other Notes:</b> none		

4 General Elective Area

Leistungselektronik							
Power Electronics							
<b>Module number:</b> M.048.22006	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22006 Power Electronics	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Leistungselektronik:</i> None						
4	<b>Contents:</b> <i>Contents of the course Leistungselektronik:</i> <b>Short Description</b> This course introduces the topic of power electronics. It explains different converter topologies, their building blocks, functionality, and modulation techniques to operate these converters. <b>Contents</b> <ul style="list-style-type: none"> <li>• ideal switches and non-ideal semiconductor switches</li> <li>• dc-dc converters</li> <li>• dc-ac converters</li> <li>• pulse-width modulation and other modulation techniques</li> <li>• rectifiers</li> </ul>						
5	<b>Learning outcomes and competences:</b> After successfully completing the module, students will be able to <ul style="list-style-type: none"> <li>• explain the base principles of power electronics</li> <li>• explain the functionality of different converter topologies</li> <li>• draw the typical waveforms of currents and voltages for the different converters, and do basic sizing calculations</li> <li>• create a simple simulation model to test the converter</li> </ul>						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5			
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Jakub Kucka			
13	<b>Other Notes:</b> <i>Remarks of course Leistungselektronik:</i> <b>Course Homepage</b> <a href="http://www.lea.upb.de">http://www.lea.upb.de</a> <b>Implementation</b> <ul style="list-style-type: none"> <li>• Lecture with presentation slides and blackboard notes</li> <li>• Exercises with the group</li> <li>• Exercises in the computer room</li> </ul>			

#### 4 General Elective Area

<b>Leistungselektronik für die Energiewende</b>						
Power Electronics for the Energy Transition						
<b>Module number:</b> M.048.22017	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
<b>1</b>	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.22017 Power Electronics for the Energy Transition	2L 2Ex, SS	60	120	C	40/40
<b>2</b>	<b>Options within the module:</b> None					
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Leistungselektronik für die Energiewende:</i> <b>Recommended:</b> It is recommended to take part also in the lecture power electronics, however, this is not mandatory					
<b>4</b>	<b>Contents:</b> <i>Contents of the course Leistungselektronik für die Energiewende:</i> <ul style="list-style-type: none"> <li>• Brief introduction to power electronics</li> <li>• Economic fundamentals of the energy industry, rules, EEG law, electricity stock exchange, etc.</li> <li>• Technical principles of the energy industry, average demand, daily and seasonal fluctuations, etc.</li> <li>• AC transmission and distribution grids, transformers, basics of control active and reactive power, minute reserve, primary, secondary and tertiary control</li> <li>• Flexible AC transmission systems (FACTS)</li> <li>• Static reactive power compensator (STATCOM), passive and active filters, electronic transformers</li> <li>• Sector coupling, Power to Gas, Vehicle to Grid, E-mobility</li> <li>• Photovoltaic inverter</li> <li>• Wind power converter</li> <li>• Uninterruptible power supplies</li> <li>• Battery storage and converters and energy management</li> <li>• Smart grids</li> <li>• DC grids</li> <li>• High-voltage DC transmission</li> </ul>					

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> <ul style="list-style-type: none"> <li>• Understanding of energy systems, interactions and necessary technologies</li> <li>• Ability to analyse and evaluate such systems</li> <li>• First competences for the selection and design of individual components</li> </ul>		
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
	zu	<b>Type of examination</b>	<b>Duration or scope</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Jakub Kucka		

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> <a href="https://ei.uni-paderborn.de/lea/lehre/veranstaltungen/lehrangebote/">https://ei.uni-paderborn.de/lea/lehre/veranstaltungen/lehrangebote/</a></p> <hr/> <p><b>ATTENTION - IMPORTANT NOTICE</b> The course doesn't take place in summer term 2024.</p> <hr/> <p><i>Remarks of course Leistungselektronik für die Energiewende:</i></p> <hr/> <p><b>ATTENTION - IMPORTANT NOTICE</b> The course doesn't take place from summer term 2024 until further notice.</p> <hr/>
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#### 4 General Elective Area

Leistungselektronische Stromversorgungen							
Switched mode power supplies							
<b>Module number:</b> M.048.22016	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22016 Switched Mode Power Supplies	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Leistungselektronische Stromversorgungen:</i> None						
4	<b>Contents:</b> <i>Contents of the course Leistungselektronische Stromversorgungen:</i> <b>Short Description</b> The course covers basic circuit topologies of electronic power supplies with electric isolation as well as their modeling and control. <b>Contents</b> <ul style="list-style-type: none"> <li>• Basic circuits of isolated DC-DC power converters</li> <li>• Transformers, coupled inductors, filters and resonant tanks</li> <li>• Resonant technique for low loss switching</li> <li>• Control design for switched mode power supplies</li> <li>• Rectifiers with sinusoidal current shape (PFC): power stage and control concepts</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to analyse power electronic circuits according to their mode of operation and component requirements</li> <li>• to compare technologies and switching techniques and to evaluate their ability for specific applications</li> <li>• to model circuit and control by special procedures</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• learn a circuit related view and the ability to define component requirements</li> <li>• improve their skills in computer-based circuit- and control modelling</li> <li>• extend their competence by self study; a one-day practical course will be offered therefore</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination</td> <td>120-180 min or 30-45 min</td> <td>100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination	120-180 min or 30-45 min	100%
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a)	Written or Oral Examination	120-180 min or 30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Frank Schafmeister</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Leistungselektronische Stromversorgungen:</i></p> <p><b>Course Homepage</b> <a href="http://www.lea.upb.de">http://www.lea.upb.de</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• lecture</li><li>• exercise (classic exercise and computer-based simulation)</li><li>• one-day practical course in the last week of lecture period (assembly and initial operation of a switched mode power supply)</li></ul> <p><b>Teaching Material, Literature</b> Lecture slides and skript, further literature will be announced within the lecture.</p>
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4 General Elective Area

<b>Mensch-Haus-Umwelt</b>							
Men-House-Environment							
<b>Module number:</b> M.048.22007	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22007 Men-House-Environment	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Mensch-Haus-Umwelt:</i> None						
4	<b>Contents:</b> <i>Contents of the course Mensch-Haus-Umwelt:</i> <b>Short description</b> The different levels of energy accounting and their respective significance. Calculation methods for the energy intensity of products, taking into account a holistic balancing of the product life cycles. Mechanisms and potentials of rational energy use using the example of the building and housing sector. <b>Contents</b> The course Man-House-Environment deals with the holistic consideration of energy demand elements during the construction and use up to the demolition of buildings (including the production of building materials). The mechanisms for energy balancing are fundamentally elaborated and their application is deepened in such a way that they are transferable to other life cycle considerations (products, manufacturing components, etc.).						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Expertise:</b></p> <ul style="list-style-type: none"> <li>• The complexity of energy supply, which is usually taken for granted, should be communicated. A central point here is the overall energy balancing approach, which often is neglected.</li> <li>• The interaction of ecological, economic, and sociological factors for the use of the environment as a living space is to be worked out.</li> </ul> <p><b>Cross-disciplinary competencies:</b></p> <ul style="list-style-type: none"> <li>• In addition to the subject-specific competencies, the course also enables students for general project-related work in their later professional career – by the intensive cooperation during the exercise phase. An important aspect is the mixture of skills that the students from the different disciplines “bring along” through their education.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination</td> <td>120-180 min or 30-45 min</td> <td>100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination	120-180 min or 30-45 min	100%
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a)	Written or Oral Examination	120-180 min or 30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Stefan Krauter</p>								

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Mensch-Haus-Umwelt:</i></p> <p><b>Course Homepage</b> <a href="http://www.nek.upb.de/lehre/projektarbeiten/mensch-haus-umwelt">http://www.nek.upb.de/lehre/projektarbeiten/mensch-haus-umwelt</a></p> <p><b>Implementation</b> In the course, which is offered in the form of a frontal lecture, the students are familiarized with the fundamentals and the calculation methods. In the context of the exercises, the knowledge is deepened and extended through their own examination of the topics.</p> <p><b>Teaching Material, Literature</b> A comprehensive script for the course is provided in which further literature sources are mentioned.</p>
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#### 4 General Elective Area

<b>Modellierung von Energiesystemen</b>							
Modelling of Energy Systems							
<b>Module number:</b> M.048.22019	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22019 Modelling of Energy Systems	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Modellierung von Energiesystemen:</i> None						
4	<b>Contents:</b>  <i>Contents of the course Modellierung von Energiesystemen:</i> Building on a holistic understanding of energy systems, the course covers the basics of modelling them. For this purpose, starting with simple modelling of stand-alone energy technology components, more extensive energy systems are dealt with step by step. The importance of input data and parameters as well as the effects of different spatial and temporal resolutions are discussed. In addition, various techniques for verification and validation, optimising simulations, for sensitivity analysis and for risk assessment in the context of regenerative energy systems are taught. The lectures are accompanied by practical exercises in which the students gradually deepen the learning content by building and simulating their own models.						
5	<b>Learning outcomes and competences:</b>  By participating in the course, students are enabled to comprehensively evaluate simulation models and studies as well as to independently model extensive energy systems. Basic techniques of modelling, optimisation and evaluation are learned and can be applied.						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5		
12	<b>Module coordinator:</b> Prof. Dr. Henning Meschede		
13	<b>Other Notes:</b> none		

4 General Elective Area

Solar Electric Energy Systems							
Solar Electric Energy Systems							
<b>Module number:</b> M.048.22013	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22013 Solar Electric Energy Systems	2L 2Ex, SS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Solarelektrische Energiesysteme:</i> None						

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Solarelektische Energiesysteme:</i></p> <p><b>Short Description</b> Conversion of solar energy into electricity for power supply: Basics, properties of devices and materials, performance issues, energy yield, durability, standards, testing, systems, modeling, simulation.</p> <p><b>Contents</b></p> <ol style="list-style-type: none"><li>1. Potentials, Irradiance, Shadowing</li><li>2. Concentration, Solar thermal systems</li><li>3. Principle of photovoltaic conversion, making of solar cells, characteristics of photovoltaic conversion devices</li><li>4. Manufacturing of solar modules, characteristics, performance</li><li>5. PV systems: wiring, inverters, grid-connected system configurations</li><li>6. PV systems: Mounting, BoS, Off- vs. On-grid grid Systems, Costs</li><li>7. Market development of PV: off-grid markets, markets triggered by feed-in tariffs (FiT), self-sustainable markets, cost and price development</li><li>8. Simulation of PV Systems and Microgrids via the HOMER software</li><li>9. Performance: optical, thermal and electrical modeling, simulation, measurement</li><li>10. Durability of PV modules and systems: Standards, tests, degradation effects</li><li>11. Energy Storage</li><li>12. Set-up methods for large scale PV power plants</li><li>13. PV for general electricity supply: Predictability, combination with other energy sources, Modification, Load Management</li><li>14. Excursion to a solar research unit or a solar project</li></ol>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completing the course the students should be Students in a position to:</p> <ul style="list-style-type: none"><li>• be familiarized with the basics of solar electric power engineering.</li><li>• understand the specific characteristics of a power supply via solar-thermal and photovoltaic energy conversion. understand, analyze and evaluate solar electric power plants and to be enabled to plan a layout of a PV power plant</li></ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"><li>• are enabled to apply the knowledge and skills across disciplines</li><li>• are enabled to use method-oriented approaches for the implementation of sustainable energy supply</li><li>• are enabled to educate themselves in the future.</li></ul>

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRSGe Master v5		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Stefan Krauter		
13	<b>Other Notes:</b> <i>Remarks of course Solarelektrische Energiesysteme:</i> <b>Course Homepage</b> <a href="http://www.nek.upb.de/lehre">http://www.nek.upb.de/lehre</a> <b>Implementation</b> Lecture combined with practical examples & simulations; Excursion to see applications in practice <b>Teaching Material, Literature</b> Martin A. Green: Solar Cells Solar Cells: Operating Principles, Technology, and System Applications, UNSW, Sydney, Publisher: Prentice Hall, 1981. Stuart R. Wenham, Martin A. Green, Muriel Watt, Richard Corkish, Alistair Sproul: Applied Photovoltaics, UNSW, Sydney, softcover version: Earthscan, 2012. Stefan Krauter: Solar Electric Power Generation. 1st Ed. Springer: Berlin, Heidelberg, New York, 2006. Stefan Krauter: Solar Electric Power Generation. 2nd Ed. Springer: Berlin, Heidelberg, New York, 2019 (under preparation, preprint available). Stefan Krauter, S.: Simple and effective methods to match photovoltaic power generation to the grid load profile for a PV based energy system. In: Solar Energy 159 (2018) S. 768–776. Stephen W. Fardo, Dale R. Patrick: Electrical Power Systems Technology. The Fairmont Press, Inc., 2009.		

#### 4 General Elective Area

<b>Umweltmesstechnik</b>							
Environmental monitoring and measuring technologies							
<b>Module number:</b> M.048.22010	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.22010 Environmental Monitoring and Measuring Technologies	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Umweltmesstechnik:</i> None						
4	<b>Contents:</b> <i>Contents of the course Umweltmesstechnik:</i> <b>Short Description:</b> The ever more intensive use of natural resources is leading to increasing environmental pollution. This course deals with the problems of certain selected impact mechanisms in relation to the impact sites or habitats. The relevant quantities will be characterised and the measurement principles and methods suitable for determining them will be described. In particular, the explanations concentrate on the metrological determination of contamination and monitoring of air, water and soil. <b>Contents:</b> The lecture Environmental Monitoring and Measuring Technologies is structured as follows <ul style="list-style-type: none"> <li>• Legal framework of environmental protection</li> <li>• Significance and tasks of environmental monitoring and measuring technology</li> <li>• Explanation of the mechanisms of action in the increasingly intensive use of natural resources as well as the increasing hazard potential through the use of technologies</li> <li>• Chemosensor technology and sample preparation</li> <li>• Measurement principles and methods of environmental measurement technology</li> <li>• Optodes and optical measurement and analysis technology</li> <li>• Sensors for liquid analysis</li> <li>• Sensors for gas analysis</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, students are able to</p> <ul style="list-style-type: none"> <li>• analyse and understand the mechanisms of action in increasing environmental problems,</li> <li>• to select suitable measurement principles or measurement techniques for selected measurement tasks, considering the concrete measurement conditions,</li> <li>• characterise and interpret measurement results.</li> </ul> <p><b>Key qualifications:</b> The Students</p> <ul style="list-style-type: none"> <li>• can apply the acquired knowledge and skills in an interdisciplinary manner and with complex issues,</li> <li>• are able to develop targeted solutions based on systematic problem analysis,</li> <li>• are capable of familiarising themselves with relevant fields of work due to the method-oriented knowledge transfer.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP) </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination</td> <td>120-180 min or 30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination	120-180 min or 30-45 min	100%
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a)	Written or Oral Examination	120-180 min or 30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4, UF Technik Lehramt GyGe Master v5, UF Technik Lehramt HRS-Ge Master v5</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bernd Henning</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Umweltmesstechnik:</i></p> <p><b>Module Homepage</b> <a href="http://emt.upb.de">http://emt.upb.de</a></p> <p><b>Methodical implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with slide presentation of extensive correlations</li><li>• Practical work in groups with measurement technology in the laboratory</li></ul> <p><b>Learning materials, references</b> Provision of a script; references to textbooks from the textbook collection will be announced.</p>
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## 4.2 EE Catalogue Cognitive Systems

Advanced Topics in Robotics						
Advanced Topics in Robotics						
<b>Module number:</b> M.048.92006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.92006 Advanced Topics in Robotics	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Advanced Topics in Robotics:</i> None					
4	<b>Contents:</b> <i>Contents of the course Advanced Topics in Robotics:</i> <b>Short Description</b> The course Advanced Topics in Robotics is based on the course Robotics. The students are introduced to current research topics in the field of autonomous and teleoperated mobile robots to solve interdisciplinary issues. The challenges encountered in developing intelligent mobile systems are analyzed and current solutions presented. <b>Contents</b> <ul style="list-style-type: none"> <li>• Architectures of robot systems</li> <li>• Middleware for hardware abstraction</li> <li>• Device drivers and libraries</li> <li>• Visualization</li> <li>• Local navigation processes (collision avoidance)</li> <li>• Global navigation processes (pathfinding)</li> <li>• Navigation and self-localization methods (SLAM)</li> <li>• Fundamentals of task planning</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to name and analyze the basic robot architectures for mobile robots,</li> <li>• have a good command of the methods for the navigation and control of mobile robots and</li> <li>• are able to implement, test and apply them.</li> </ul> <p><b>Key qualifications:</b> The students have a good command of programming in the C language</p>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Bärbel Mertsching</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Topics in Robotics:</i></p> <hr/> <p><b>ATTENTION - IMPORTANT NOTICE</b> The course doesn't take place in winter term 2024/25. Please see the notice boards of the group.</p> <hr/> <p><b>Course Homepage</b> <a href="http://getwww.uni-paderborn.de/teaching/atir">http://getwww.uni-paderborn.de/teaching/atir</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• The theoretical and methodical fundamentals will be introduced during the lecture.</li><li>• The methods presented will be practiced during the subsequent exercise / lab part.</li><li>• Finally, the participants will implement, test, and apply simple algorithms.</li><li>• The necessary programming skills will be taught during the practical, this is explicitly not considered a programming course.</li></ul> <p><b>Teaching Material, Literature</b> Allocation of lecture notes; information on textbooks stocked in the textbook collection will be announced later.</p> <ul style="list-style-type: none"><li>• Mertsching, Bärbel: Robotics (lecture notes)</li><li>• McKerrow, Phillip J.: Introduction to Robotics. Addison-Wesley, 1991</li><li>• Siegwart, Roland; Nourbakhsh, Illah R. and Scaramuzza, David: Introduction to Autonomous Mobile Robots. The MIT Press, 2011, ISBN-13: 978-0262015356</li></ul>
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#### 4 General Elective Area

Digital Image Processing I						
Digital Image Processing I						
<b>Module number:</b> M.048.92008	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92008 Digital Image Processing I	2L 2Ex, SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Digital Image Processing I:</i> None. Basic programming knowledge is an advantage.					

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Digital Image Processing I:</i></p> <p><b>Short Description</b></p> <p>This course provides a fundamental introduction to digital image processing. Upon successful completion, students will be able to thoroughly describe the basic concepts of image generation and representation. Additionally, they will acquire the skills to apply methods for enhancing and segmenting grayscale and color images in both the spatial and frequency domains, as well as techniques for image compression. Students will be capable of independently selecting, implementing, testing, and applying these techniques to complex image processing tasks. A typical application area is automation technology.</p> <p><b>Contents</b></p> <ol style="list-style-type: none"> <li>1. Introduction (Graphics File Formats, Application Examples, Human Vision) 2. Image Formation and Image Models (Camera Models, Image Formation, Image Sampling and Quantization)</li> <li>2. Image Enhancement in the Spatial Domain (Gray-Level Transformation Functions, Histogram Processing, Spatial Filtering)</li> <li>3. Image Enhancement in the Frequency Domain (2D Fourier Transform, Smoothing and Sharpening Filters, Implementation Details)</li> <li>4. Color Image Processing (Color Spaces, Color and Pseudo-Color Image Processing, Spatial Filtering)</li> <li>5. Image Compression and Reduction (Types of Redundancy, Compression Models, Lossless and Lossy Compression)</li> </ol>								
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• are able to describe the basics of image generation and image digitization and</li> <li>• are able to select, implement, test and apply methods for the enhancement of images in the spatial and frequency domain, image segmentation and data reduction independently for complex image processing tasks.</li> </ul> <p><b>Key qualifications</b></p> <p>The students have a good command of programming in Python.</p>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>								

#### 4 General Elective Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Markus Hennig</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Digital Image Processing I:</i></p> <p><b>Target group</b> Master's students in electrical engineering and related fields.</p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/get/teaching/dip-i">https://ei.uni-paderborn.de/get/teaching/dip-i</a></p> <p><b>Literature</b></p> <ul style="list-style-type: none"> <li>• Gonzalez, R., &amp; Woods, R. (2017). Digital Image Processing (4th Global Ed.). Pearson. Print ISBN: 978-1-292-22304-9, E-ISBN: 978-1-292-22307-0.</li> <li>• Mertsching, B. (2024). Digital Image Processing I (Lecture Notes).</li> <li>• Jähne, B. (2024). Digitale Bildverarbeitung (8th Edition, German Language). Springer. Print ISBN: 978-3-662-59509-1, E-ISBN: 978-3-662-59510-7.</li> </ul> <p><b>Comment</b> The material presented in the lecture is implemented in the exercises using Python. The first exercise provides an introduction to this, so that it is possible to get started with limited programming knowledge. Regular and active participation in lectures and exercises is expected.</p>

4 General Elective Area

<b>Digital Image Processing II</b>							
Digital Image Processing II							
<b>Module number:</b> M.048.92010	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.048.92010 Digital Image Processing II	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Digital Image Processing II:</i> <b>Recommended:</b> Basic knowledge of image processing, (e. g. from the course Digital Image Processing I (L.048.23002 / L.048.92008))						
4	<b>Contents:</b>  <i>Contents of the course Digital Image Processing II:</i> <b>Short Description</b> The course “Digital Image Processing II” is a module in the catalog “Cognitive Systems” for advanced students of the Electrical Engineering Master’s program and related degree programs. It follows the fundamental course “Digital Image Processing I” and covers methods for high-level image processing. <b>Contents</b> The following topics will be discussed during the semester: <ul style="list-style-type: none"><li>• Image segmentation (line and edge detection, segmentation by region, superpixels)</li><li>• Feature extraction (feature descriptors, principal components, Scale-Invariant-Feature-Transform (SIFT))</li><li>• Stereo image analysis (depth perception, stereo geometry, correspondence problem)</li><li>• Motion (motion detection, optical flow, motion models, motion segmentation)</li><li>• Object recognition and image pattern classification (patterns, classifiers, neural networks and deep learning, convolutional neural networks (CNN))</li></ul> After learning about the methods in the lecture, the students will implement them in Jupyter Notebooks.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"> <li>• can apply methods for image segmentation, representation and description of features, stereo and motion image analysis, objection recognition and machine learning,</li> <li>• are able to transfer the acquired knowledge of image processing to the processing of other multi-dimensional signals,</li> <li>• are able to describe the state-of-the-art of the presented topics, and</li> <li>• are able to implement the presented methods.</li> </ul> <p><b>Key qualifications:</b> The students are able to identify and evaluate the function and the behavior of complex technical processes and their integration into the social environment while also considering ethical aspects.</p>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bärbel Mertsching</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Digital Image Processing II:</i></p> <p><b>Course Homepage</b> [<a href="http://getwww.uni-paderborn.de/teaching/dip-II">http://getwww.uni-paderborn.de/teaching/dip-II</a>]</p> <p><b>Course Documents</b> see PANDA ([<a href="https://panda.uni-paderborn.de">https://panda.uni-paderborn.de</a>])</p> <p><b>References (excerpt)</b></p> <ul style="list-style-type: none"><li>• Mertsching, Bärbel: Digital Image Processing (lecture notes)</li><li>• Forsyth, David and Ponce, Jean: Computer Vision - A Modern Approach. Prentice-Hall, 2nd ed., 2011. ASIN: B006V372KG</li><li>• Gonzalez, Rafael C. and Woods, Richard E.: Digital Image Processing. Pearson Education Limited, 4th ed., 2018. ISBN-13: 978-1-292-22304-9</li><li>• Jähne, Bernd: Digitale Bildverarbeitung. Springer, 7. Aufl., 2012. ISBN-13: 978-3642049514</li></ul>
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4 General Elective Area

Reinforcement Learning						
Reinforcement Learning						
<b>Module number:</b> M.048.92045	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92045 Reinforcement Learning	2L 2Ex, SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Reinforcement Learning:</i> <b>Recommended:</b> It is recommended to have a sound basic knowledge in the field of system and control theory. Ideally, the students have knowledge in the field of un-/supervised machine learning and numerical optimization. In addition, at least some experience with Python will be advantageous for the exercise and tutorial tasks.					

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Reinforcement Learning:</i></p> <p>The course covers the basics of reinforcement learning (RL) in an engineering context. RL stands for a series of methods of machine learning in which an agent independently learns a strategy (policy) to maximize the rewards received during interaction with an (unknown) system. This can be, for example, a control loop in which an adaptive controller tries to determine an optimal control law from previous observations of the control and measurement variables, which maximizes certain benchmark criteria with regard to controller performance. Well-known fields of application include the operation of autonomous vehicles and industrial robots or the identification of optimal strategies in the context of leisure games.</p> <p>The course has an application-oriented focus in the engineering sciences but is also designed for students of natural sciences (e.g. computer science, mathematics). In addition to teaching the methodological fundamentals within the lecture, great importance is attached to practical implementation and programming tasks during the exercise and tutorial hours.</p> <p>The course will cover the following content:</p> <ul style="list-style-type: none"><li>• Conceptual basics and historical overview</li><li>• Markov decision processes</li><li>• Dynamic programming</li><li>• Monte Carlo learning</li><li>• Temporal difference learning</li><li>• Bootstrapping</li><li>• Function approximation and deep learning</li><li>• On- and Off-policy strategies</li><li>• Policy gradient methods</li><li>• Trust region methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain-specific competences</b></p> <p>After attending the course, the students are able to</p> <ul style="list-style-type: none"><li>• differentiate, apply and analyze RL methods,</li><li>• name and explain differences as well as advantages and disadvantages of RL compared to neighboring approaches (e.g. model-predictive control),</li><li>• educate themselves independently in this branch of science on the basis of the methods learned for the analysis and synthesis of RL techniques.</li></ul> <p><b>Interdisciplinary competences</b></p> <p>The students</p> <ul style="list-style-type: none"><li>• can apply or transfer the acquired knowledge to interdisciplinary problems,</li><li>• have gained practical experience in programming which they can use across domains and</li><li>• are able to critically evaluate methods and results.</li></ul>

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> Dr Jarren Lange		
13	<b>Other Notes:</b> <i>Remarks of course Reinforcement Learning:</i> <b>Course homepage</b> <a href="https://en.ei.uni-paderborn.de/rat">https://en.ei.uni-paderborn.de/rat</a> <a href="https://github.com">https://github.com</a> (open-source course material) <b>Implementation</b> <ul style="list-style-type: none"> <li>• Slide-based lecture, which also serves as lecture notes.</li> <li>• Presence exercises with tutorial sheets (with many programming tasks)</li> </ul> <b>Main literature</b> <ul style="list-style-type: none"> <li>• Richard S. Sutton, Andrew G. Barto, „Reinforcement Learning“, 2. Ed., MIT Press, 2018</li> <li>• David Silver, „Reinforcement Learning“ (Skriptum), University College London, 2015</li> </ul>		

4 General Elective Area

Robotics							
Robotics							
<b>Module number:</b> M.048.92012	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92012 Robotics	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Robotics:</i> None						
4	<b>Contents:</b> <i>Contents of the course Robotics:</i> <b>Short Description</b> The course “Robotics” is a fundamental module in the catalog “Cognitive Systems” of the Electrical Engineering Master’s program and related degree programs. It is the first of two courses that cover the relevant concepts and techniques in the field of robot manipulators and mobile robots. This course concentrates on modeling and controlling robot arms, while its successor in the winter semester (Advanced Topics in Robotics (L.048.23020 / L.048.92006) focuses on mobile robots. The challenges for the development of autonomous intelligent systems will be analyzed and the current solutions will be presented. <b>Contents</b> <ul style="list-style-type: none"> <li>• Sensors, effectors, actuators</li> <li>• Homogenous coordinates, general transformations, Denavit-Hartenberg parameters</li> <li>• Kinematics and dynamics of robot arms and mobile robots</li> </ul> After the presentation of methods in the lecture, the students will use Matlab and Octave to implement them.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"> <li>• know how to transfer basic methods from control and system theory to robotics and</li> <li>• are able to apply adequate methods to model as well as plan and control the movements of robot arms.</li> </ul> <p><b>Key qualifications:</b> The students are able to identify and evaluate the function and behavior of robots and their integration into the social and economic environment while also considering ethical aspects.</p>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bärbel Mertsching</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Robotics:</i></p> <p><b>Course Homepage</b> [<a href="http://getwww.uni-paderborn.de/teaching/robotik">http://getwww.uni-paderborn.de/teaching/robotik</a>]</p> <p><b>Course Documents</b> see PANDA ([<a href="https://panda.uni-paderborn.de">https://panda.uni-paderborn.de</a>])</p> <p><b>References (excerpt)</b></p> <ul style="list-style-type: none"><li>• Mertsching, Bärbel: Robotics (lecture notes)</li><li>• McKerrow, Phillip J.: Introduction to Robotics. Addison-Wesley, 1991</li><li>• Lynch, Kevin M. and Park, Frank C.: Modern Robotics: Mechanics, Planning, and Control. Cambridge University Press, 2017. ISBN-13 : 978-1107156302</li></ul>
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4 General Elective Area

Statistical and Machine Learning							
Statistical and Machine Learning							
<b>Module number:</b> M.048.23012	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.23012 Statistical and Machine Learning	2L 2Ex, SS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Statistical and Machine Learning:</i> <b>Recommended:</b> Elementary knowledge in probability theory, as is taught in the course Statistical Signal Processing. Basic programming skills are desirable.						
4	<b>Contents:</b>  <i>Contents of the course Statistical and Machine Learning:</i> <b>Short Description</b> The course on Statistical and Machine Learning presents an introduction into the components and algorithms prevalent in statistical and machine learning. Modern techniques will be presented for gleaning information from data. Both supervised and unsupervised learning algorithms will be discussed. The presented techniques can be applied to a variety of classification and regression problems, both for one-dimensional input data (e.g., speech), two-dimensional (e.g., image) or symbolic input data (e.g., documents). <b>Contents</b> <i>Introduction to classification problems, Bayesian and other decision rules Optimization: gradient descent, algorithmic differentiation, optimization with constraints Linear classifiers, Support Vector Machines Deep neural networks (deep learning) Dimensionality reduction (PCA, LDA) Unsupervised learning (mixture densities, clustering techniques)</i>						

4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the module students will be able to</p> <ul style="list-style-type: none"> <li>• Find an appropriate approach to solving a given classification or regression problem</li> <li>• Apply supervised or unsupervised learning techniques to data of various kinds and critically assess the outcome of the learning algorithms</li> <li>• Can appreciate the power and limitations of machine learning algorithms</li> <li>• Work with software for solving machine learning problems and write own software components, apply them to given data sets and optimize parameter settings</li> <li>• Find, for a given training set size, an appropriate choice of classifier complexity und feature vector dimensionality</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• Have gathered sufficient proficiency in Python, which is valuable well beyond this course</li> <li>• Can assess the importance of the principle of parsimony and are able to transfer it to other</li> <li>• Are able to analyse a given classification or regression problem, synthesize a solution, and evaluate the performance on test data</li> <li>• Are able to apply the knowledge and skills learnt in this course to a wide range of disciplines</li> <li>• Can work cooperatively in a team and subdivide an overall task into manageable subtasks and work packages</li> <li>• Acquired a general understanding of the power and limitations of machine learning algorithms</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" data-bbox="277 1211 1422 1420"> <thead> <tr> <th data-bbox="277 1211 363 1308">zu</th> <th data-bbox="363 1211 900 1308">Type of examination</th> <th data-bbox="900 1211 1123 1308">Duration or scope</th> <th data-bbox="1123 1211 1422 1308">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td data-bbox="277 1308 363 1420">a)</td> <td data-bbox="363 1308 900 1420">Written or Oral Examination or Presentation</td> <td data-bbox="900 1308 1123 1420">120-180 min or 30-45 min or 30 min</td> <td data-bbox="1123 1308 1422 1420">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b> none</p>								
8	<p><b>Prerequisites for participation in examinations:</b> None</p>								
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>								

#### 4 General Elective Area

11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Mohammad Soleymani</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Statistical and Machine Learning:</i></p> <p><b>Course Homepage</b>  <a href="https://ei.uni-paderborn.de/en/statistical-and-machine-learning">https://ei.uni-paderborn.de/en/statistical-and-machine-learning</a></p> <p><b>Implementation</b>  <i>Lectures predominantly using the blackboard or overhead projector, occasional presentations of (powerpoint) slides , Exercise classes with exercise sheets and demonstrations on computer *Implementation of learning and classification algorithms on a computer by the students themselves; use of algorithms on real-world data or data generated on the computer, evaluation of the simulation results</i></p> <p><b>Teaching Material, Literature</b>          Course script and summary slides are provided to the students. Exercises and solutions to exercises, as well as sample implementations of algorithms are provided to the students  <i>R.O. Duda, P.E. Hart, D.G.~ Stork, Pattern Classification, Wiley, 2001 I. Goodfellow, Y. Bengio, A. Courville: Deep Learning, MIT Press, 2016 S. Theodoridis: Machine Learning, Academic Press, 2015 K. Fukunaga, Introduction to Statistical Pattern Recognition, Academic Press, 1990</i></p>

4 General Elective Area

Technische kognitive Systeme - Ausgewählte Kapitel						
Cognitive Systems Engineering - Special Topics						
<b>Module number:</b> M.048.23019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.23019 Cognitive Systems Engineering - Special Topics	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> two out of three topics, see point 4					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Technische kognitive Systeme - Ausgewählte Kapitel:</i> <b>Recommended:</b> Interest in the subject-matter and interdisciplinary work.					

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p>This module is offered in three parts. Students have to choose <b>two out of three</b>. Each part lasts two hours per week.</p> <p><i>Contents of the course Technische kognitive Systeme - Ausgewählte Kapitel:</i></p> <p><b>Part A</b></p> <p>At any given time, the sensory receptors of living beings are exposed to a very large amount of information, of which only a small proportion can be consciously processed. Visual attention is understood as the pooling of available cognitive resources for optimal processing of visual stimuli. The seminar introduces the modeling and experimental investigation of visual attention and the transfer to intelligent technical systems. It will be shown how research can be conducted jointly across disciplinary boundaries. The current focus is on the topic of saliency. The course always takes place in the winter semester.</p> <p><b>Part B</b></p> <p>While “sensation” describes the signals from the physical world that reach our sensory receptors, “perception” refers to the processes by which our brain selects, organizes, and interprets the signals. This seminar provides students in technical courses with an overview of the fundamentals of biological sensory systems and perception. In addition to the exciting and (sometimes non-intuitive) background of these topics, there will be a critical discussion of the transferability of biological concepts and mechanisms to technical systems. This seminar is always in the summer semester.</p> <p><b>Part C</b></p> <p>In this seminar, current interim reports and results from ongoing bachelor’s and master’s theses, research projects, and third-party funded projects from the GETLab - Technical Cognitive Systems department will be presented. Furthermore, there will be presentations by guests of the research group. The seminar is offered in the summer and winter semester.</p>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> The students</p> <ul style="list-style-type: none"><li>• are able to name basic research topics related to the design and the implementation of technical cognitive systems,</li><li>• can apply and evaluate technical cognitive systems, and</li><li>• are able to understand, design, implement and evaluate basic psychophysical experiments.</li></ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"><li>• are able to research and evaluate technical literature,</li><li>• have developed an understanding of the discipline-related research approaches (computer science, electrical engineering, psychology) and</li><li>• are able to carefully consider the potential use of bio-inspired mechanisms in technical systems.</li></ul>

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> None			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4			
12	<b>Module coordinator:</b> Prof. Dr. Bärbel Mertsching			
13	<b>Other Notes:</b> <b>Module Homepage</b> <a href="http://getwww.uni-paderborn.de/teaching/cse">[http://getwww.uni-paderborn.de/teaching/cse]</a> <b>Teaching Material, Literature</b> Literature references will be given at the first dates of the seminar. <i>Remarks of course Technische kognitive Systeme - Ausgewählte Kapitel:</i> <hr style="width: 20%; margin-left: auto; margin-right: auto;"/> <b>ATTENTION - IMPORTANT NOTICE</b> The course doesn't take place in winter term 2024/25. Please see the notice boards of the group. <hr style="width: 20%; margin-left: auto; margin-right: auto;"/>			

4 General Elective Area

Topics in Pattern Recognition and Machine Learning							
Topics in Pattern Recognition and Machine Learning							
<b>Module number:</b> M.048.92030	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92030 Topics in Pattern Recognition and Machine Learning	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Topics in Pattern Recognition and Machine Learning:</i> <b>Recommended:</b> Elementary knowledge in Probability Theory, as is taught in the module Statistical Signal Processing. Desirable, but not mandatory: knowledge in the field of statistical and machine learning; basic programming skills						

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Topics in Pattern Recognition and Machine Learning:</i></p> <p><b>Short Description</b></p> <p>The course on Topics in Pattern Recognition and Machine Learning first briefly summarizes the main concepts of statistical pattern recognition and machine learning. Next selected topics will be presented in detail. The choice of topics depends on current research activities and thus may change over time. Examples of such topics to be studied in detail include</p> <ul style="list-style-type: none"><li>• Deep Learning</li><li>• Model estimation in the presence of hidden variables, in order to reveal suspected latent structure buried in the data</li><li>• Bias-Variance dilemma and the tradeoff between degree of detail and generalizability of models</li><li>• Graphical models</li><li>• Sequential data and hidden Markov models</li><li>• Decision trees, model combination</li><li>• Specific classification tasks, such as automatic speech recognition</li></ul> <p>While the first part of the course will follow a regular lecture format, the second part will include active student participation. Students will be asked to read, analyze and present recently published papers from the pattern recognition and machine learning literature. This will often also include the implementation of proposed algorithms in Matlab.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Fundamentals of statistical pattern recognition: Bayes rule, learning of class-conditional densities, linear models for classification and regression</li><li>• Deep neural networks: MLP, CNN, RNN and others</li><li>• EM Algorithm and extensions thereof</li><li>• Models with discrete or continuous latent variables; GMM, NMF</li><li>• Bias-Variance dilemma and model selection</li><li>• Graphical models</li><li>• Hidden Markov models and their application in speech recognition</li><li>• Decision trees, model combination</li><li>• Recent publications in pattern recognition and machine learning</li></ul>
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4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to * Choose an appropriate classifier for a given classification problem and be able to learn the parameters of the classifier from training data</p> <ul style="list-style-type: none"> <li>• Choose an appropriate regression method for function approximation and learn its parameters from training data</li> <li>• Search for latent variables and structure in given data</li> <li>• Make an informative choice for the model order to find a good compromise between degree of detail and generalizability</li> <li>• Comprehend and analyze recent publications from the field of pattern recognition and machine learning</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• Have gathered an understanding of the importance of the chosen model order on the outcome of classification and regression tasks</li> <li>• Are aware of the impact of a priori assumptions on the result of latent variable and structure discovery in data</li> <li>• Are able to autonomously gain expertise in a certain field of pattern recognition by conducting a literature survey</li> <li>• Can gauge the importance of a given publication for the state of the art in a field</li> <li>• Are able to apply the knowledge and skills learnt in this course to a wide range of disciplines</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" data-bbox="277 1211 1422 1420"> <thead> <tr> <th data-bbox="277 1211 363 1308">zu</th> <th data-bbox="363 1211 900 1308">Type of examination</th> <th data-bbox="900 1211 1123 1308">Duration or scope</th> <th data-bbox="1123 1211 1422 1308">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td data-bbox="277 1308 363 1420">a)</td> <td data-bbox="363 1308 900 1420">Written or Oral Examination or Presentation</td> <td data-bbox="900 1308 1123 1420">120-180 min or 30-45 min or 30 min</td> <td data-bbox="1123 1308 1422 1420">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b> none</p>								
8	<p><b>Prerequisites for participation in examinations:</b> None</p>								
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>								

#### 4 General Elective Area

11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Häb-Umbach</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Topics in Pattern Recognition and Machine Learning:</i></p> <p><b>Course Homepage</b>  <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/topics-in-pattern-recognition-and-maschine-learning">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/topics-in-pattern-recognition-and-maschine-learning</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lectures predominantly using the blackboard or overhead projector, occasional presentations of (powerpoint) slides ,</li> <li>• Exercise classes with exercise sheets and demonstrations on computer</li> <li>• Instructions how to read and analyze scientific publications in this field Autonomous analysis of publications and presentation of results and gained insight</li> </ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"> <li>• R.O. Duda, P.E. Hart, D.G.~ Stork, Pattern Classification, Wiley, 2001</li> <li>• I. Goodfellow, Y. Bengio, A. Courville, Deep Learning, MIT Press, 2016</li> <li>• C. M. Bishop, Pattern Recognition and Machine Learning, Springer, 2006</li> </ul>

## 4.3 EE Catalogue Communications

Ausgewählte Kapitel der theoretischen Elektrotechnik						
Selected Topics in Theoretical Electrical Engineering						
<b>Module number:</b> M.048.24023	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.24023 Selected Topics of Theoretical Electrical Engineering	2L 2Ex, WS+SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Ausgewählte Kapitel der theoretischen Elektrotechnik:</i> <b>Recommended:</b> Basic knowledge from the compulsory module Theoretical Electrical Engineering					

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Ausgewählte Kapitel der theoretischen Elektrotechnik:</i></p> <p><b>Short Description</b></p> <p>The course Selected Topics of Theoretical Electrical Engineering extends and deepens the knowledge of electromagnetic wave propagation in free space and on waveguides acquired in the compulsory course Theoretical Electrical Engineering by selected topics. Based on the theory of longitudinally homogeneous waveguides, the system description by means of scattering parameters as well as the mode-matching method are practically motivated and treated in terms of wave theory. Another thematic focus is the Green's method for the mathematical solution of boundary value problems, which is derived in detail and applied to electromagnetic field problems.</p> <p><b>Contents</b></p> <p>The lecture Selected Topics of Theoretical Electrical Engineering is organized as follows:</p> <ul style="list-style-type: none"> <li>• Theory of eigenwaves and its application in scattering parameter theory.</li> <li>• Ez-Hz field approach for longitudinal homogeneous waveguide structures</li> <li>• System description by means of scattering matrices</li> <li>• Basics of the mode-matching method</li> <li>• Green's method in electromagnetic field theory</li> <li>• Green's functions and their determination</li> <li>• The aperture field method in antenna theory</li> <li>• Solution of physical field problems by means of Green's functions</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After attending the course, the student will be able to</p> <ul style="list-style-type: none"> <li>• mathematically model simple physical field problems</li> <li>• transfer, apply, validate numerical methods on physical problems</li> <li>• to physically interpret and visualise the obtained results</li> </ul> <p><b>Key qualifications:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• learn to transfer the acquired skills also to other disciplines</li> <li>• extend their cooperation and team capabilities as well as the presentation skills in the context of solving the exercises</li> <li>• learn strategies to acquire knowledge from literature and internet</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								

#### 4 General Elective Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Denis Sievers</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Ausgewählte Kapitel der theoretischen Elektrotechnik:</i></p> <p><b>Course Homepage</b>  <a href="http://www.tet.upb.de">http://www.tet.upb.de</a></p> <p><b>Implementation</b> The theoretical concepts are presented in the form of a lecture, which also includes a large proportion of field visualizations. In the exercises, the theory is deepened by means of simple questions and calculation examples, which are solved independently during the presence exercises.</p> <p><b>Teaching Material, Literature</b>  Lecture slides and blackboard notes, additional recommended reading will be announced in lecture.</p>

4 General Elective Area

<b>Digitale Sprachsignalverarbeitung</b>						
Digital Speech Signal Processing						
<b>Module number:</b> M.048.24001	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24001 Digital Speech Signal Processing	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Digitale Sprachsignalverarbeitung:</i> <b>Recommended:</b> Prior knowledge from the module Higher Mathematics.					

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Digitale Sprachsignalverarbeitung:</i></p> <p><b>Short Description</b></p> <p>The course introduces the basic techniques and theories of digital speech signal processing. A focal point of the first part of the lecture is the topic “Listening and Speaking”, which is concerned with psychological effects of human sound perception and speech production. Subsequently, time discrete signals and systems, as well as computer based data processing are discussed. Further topics are non-parametric short-time analysis of speech signals, speech coding and IP-phones.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Listen and talk</li><li>• Generating voice: human vocal tract, source filter model, vocoder</li><li>• Acoustic waves</li><li>• Listen: human ear, psycho acoustics and physiology of listening, loudness, acoustic occlusion, frequency groups</li><li>• Time-discrete signals and systems</li><li>• Basics: Elementary signals, LTI systems</li><li>• Transformations: Fourier transformation of time-discrete signals, DFT, FFT</li><li>• Time-discrete filtering in frequency domain: Overlap-Add, overlap-Save</li><li>• Statistical speech signal analysis</li><li>• Basics in theory of probabilities</li><li>• Short-run analysis of speech signals: Spectrogram, cepstrum</li><li>• Estimation of speech signals</li><li>• Optimal filters</li><li>• LPC analysis</li><li>• Spectral filtering for noise suppression: spectral subtraction, Wiener filter</li><li>• Adaptive Filters: LMS adaptation algorithm, echo compensation</li><li>• Speech coding</li><li>• Time domain coding: signal shape coding, parametric coding, hybride coding techniques</li><li>• Frequency domain coding</li><li>• Amplitude quantization: uniform quantization, quantization with companders (ulaw, alaw)</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>After attending the course, the students will be able to</p> <ul style="list-style-type: none"><li>• analyze digital signals, e.g., audio signals, in the time or frequency domain,</li><li>• represent audio signals efficiently and</li><li>• implement widely-used algorithms for speech analysis and speech processing in the frequency or time domain.</li></ul> <p><b>Key qualifications:</b></p> <p>The students</p> <ul style="list-style-type: none"><li>• are able to explain effects in real signals based on the theoretical knowledge,</li><li>• are able to investigate theoretical approaches by a systematic analysis and</li><li>• are, due to the precise treatment of the contents, in a position to continue their learning themselves</li></ul>

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Dr.-Ing. Jörg Schmalenströer		
13	<b>Other Notes:</b> <i>Remarks of course Digitale Sprachsignalverarbeitung:</i> <b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/digital-speech-signal-processing">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/digital-speech-signal-processing</a> <b>Implementation</b> <ul style="list-style-type: none"> <li>• Lectures using the blackboard and presentations,</li> <li>• Alternating theoretical and practical exercise classes with exercise sheets and computer and</li> <li>• Demonstration of real technical systems in the lecture hall.</li> </ul> <b>Teaching Material, Literature</b> Allocation of a script; information on textbooks ; matlab scripts		

4 General Elective Area

Elektromagnetische Feldsimulation						
Simulation of Electromagnetic Fields						
<b>Module number:</b> M.048.24006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.24006 Simulation of Electromagnetic Fields	2L 2Ex, WS+SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Elektromagnetische Feldsimulation:</i> <b>Recommended:</b> In-depth knowledge of electromagnetic field theory taught in the modules "Field Theory", "Electromagnetic Waves" and "Theoretical Electrical Engineering".					

4

**Contents:**

*Contents of the course Elektromagnetische Feldsimulation:*

**Short description**

The course Electromagnetic Field Simulation offers an introduction to modern simulation methods for electromagnetic field problems. The focus is on the method Finite Integration (FIT), a modern, very efficient and successful approach from the class of grid-based methods. Field problems of statics, quasistatics and fast-varying fields (electromagnetic waves) with almost arbitrary material distribution can be treated. The modeling with FIT leads to algebraic matrix equations, whose solution is also discussed in an introductory way. In addition, some related methods such as finite differences and finite elements will be discussed. One of the aims of the course is to get to know and to be able to assess the possibilities and limitations of the discussed methods in practical use. In addition, the foundation is laid for further development of the algorithms in the context of scientific projects.

**Contents**

as follows

- Introduction
- Motivation
- Classification of solution methods
- Numerical approaches
- Fundamentals of the finite integration method
- Lattice Maxwell equations
- Properties of discretization matrices
- Boundary conditions
- Solution of electromagnetic field problems
- Static fields
- Time-varying fields
- Time-harmonic fields (frequency domain)
- Transient fields (time domain)

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Specialized competence:</b> After attending the course, students will be able to,</p> <ul style="list-style-type: none"> <li>• formulate complex electromagnetic field problems mathematically (modeling, analyzing)</li> <li>• to apply the Finite integration method to physical problems (apply, synthesize, evaluate)</li> <li>• visualize numerically obtained results and interpret them physically (Evaluate).</li> </ul> <p><b>Cross-disciplinary competencies:</b> The students</p> <ul style="list-style-type: none"> <li>• learn to apply the acquired knowledge and skills across disciplines,</li> <li>• expand their cooperation and teamwork skills as well as presentation skills when working on exercises</li> <li>• learn strategies for acquiring knowledge by studying literature and using the Internet,</li> <li>• acquire a subject-related foreign language competence</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Denis Sievers</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Elektromagnetische Feldsimulation:</i></p> <p><b>Course Homepage</b> <a href="http://www.tet.upb.de">http://www.tet.upb.de</a></p> <p><b>Implementation</b> The theoretical concepts are presented in the form of a lecture, and in the associated practical programming exercise, small Matlab programs are created for simple problems in simulation technology.</p> <p><b>Teaching Material, Literature</b> Lecture slides and blackboard notes</p>
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#### 4 General Elective Area

<b>Feldberechnung mit der Randelementmethode</b>							
Field Computation Using Boundary Element Method							
<b>Module number:</b> M.048.24013	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term				
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
<b>1</b>	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.24013 Field Computation Using Boundary Element Method	2L 2Ex, WS+SS	60	120	C	40/40	
<b>2</b>	<b>Options within the module:</b> None						
<b>3</b>	<b>Admission requirements:</b> None  <i>Prerequisites of course Feldberechnung mit der Randelementmethode:</i> <b>Recommended:</b> In-depth knowledge of electromagnetic field theory taught in the modules "Field Theory", "Electromagnetic Waves" and "Theoretical Electrical Engineering".						
<b>4</b>	<b>Contents:</b>  <i>Contents of the course Feldberechnung mit der Randelementmethode:</i> <b>Short description</b> The course Field Calculation with the Boundary Element Method focuses on a discretization method which is preferably used in antenna technology for the solution of radiation problems as well as in radar technology for the analysis of scattering objects. From the numerically determined results, important parameters such as the directivity of antennas or the backscatter cross section of radar targets can be derived. The aim of the lecture is to impart the theoretical basic knowledge about the boundary element method under consideration of application-related aspects, with the main focus on the use in engineering practice. <b>**Content</b> The lecture Field Calculation with the Boundary Element Method is structured as follows: 1. introduction (motivation, mathematical basics) 2. integral equation method (representation formulas for electromagnetic fields, surface integral equations) 3. mathematical modeling (formulation of antenna, scattering and eigenvalue problems, excitation modes, calculation of backscattering cross sections and antenna parameters) 4. discretization by means of the method of moments (principle of projection methods, basis functions) 5. calculation of matrix contributions (numerical integration, treatment of singular integrals) 6. aspects of solving the discrete model problem (solution strategies, matrix compression methods)						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Specialized competence:</b> After attending the course, students will be able to,</p> <ul style="list-style-type: none"> <li>• formulate complex electromagnetic field problems mathematically (modeling, analyzing)</li> <li>• transfer, apply and test the boundary element method to physical problems (apply, synthesize, evaluate)</li> <li>• visualize numerically obtained results and interpret them physically (Evaluate).</li> </ul> <p><b>Cross-disciplinary competencies:</b> The students</p> <ul style="list-style-type: none"> <li>• learn to apply the acquired knowledge and skills across disciplines,</li> <li>• expand their cooperation and teamwork skills as well as presentation skills when working on exercises</li> <li>• learn strategies for acquiring knowledge by studying literature and using the Internet,</li> <li>• acquire a subject-related foreign language competence</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Denis Sievers</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Feldberechnung mit der Randelementmethode:</i></p> <p><b>Course Homepage</b> <a href="http://tet.upb.de/">http://tet.upb.de/</a></p> <p><b>Implementation</b> The theoretical concepts will be presented in the form of a lecture accompanied by a practical programming exercise in which the algorithms presented will be implemented on a computer and tested using simple practical examples.</p> <p><b>Teaching Material, Literature</b> Lecture slides and blackboard notes, additional recommended reading will be announced in lecture.</p>
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#### 4 General Elective Area

Hochfrequenztechnik							
High Frequency Engineering							
<b>Module number:</b> M.048.24007	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.24007 High-Frequency Engineering	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Hochfrequenztechnik:</i> None						
4	<b>Contents:</b> <i>Contents of the course Hochfrequenztechnik:</i> <b>Short Description</b> This lecture gives application-oriented knowledge in high frequency engineering. Furthermore, it gives knowledge in active and passive high-frequency circuits. <b>Contents</b> The lecture High-Frequency Engineering (4 SWS, 6 ECTS credit points) extends the content of the lecture Theoretische Elektrotechnik by further application-relevant knowledge. The aim is to qualify the students for development tasks for example in the radio frequency part of a mobile telephone. But considerations of high-frequency engineering are also needed in prevalent digital circuits. The emphases of the lecture are passive devices, high-frequency properties of fundamental transistor circuits, linear and nonlinear amplifiers, noisy multiports, mixers, oscillators, injection-locking and phase-locked loop						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>**Professional Competence</p> <p>After attending the course, the students will be able, in the taught extent, to understand the function of components, circuits and systems of high-frequency engineering, to model and to apply them.</p> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Hochfrequenztechnik:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Implementation</b> Lecture and exercise</p> <p><b>Teaching Material, Literature</b> Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• Thiede, A.: Skriptum Hochfrequenzelektronik/High-Frequency Electronics, Universität Paderborn</li><li>• Sze, S. M.: High Speed Semiconductor Devices, John Wiley &amp; Sons, 1990</li><li>• Herbst, L. J.: Integrated Circuit Engineering, Oxford University Press, 1996</li><li>• Yip, P. C. L.: High-Frequency Circuit Design and Measurement, Chapman &amp; Hall, 1996</li><li>• Gonzalez, G.: Microwave Transistor Amplifiers, Prentice Hall, 1997</li><li>• Hoffmann, M.: Hochfrequenztechnik, Springer, 1997</li></ul>
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4 General Elective Area

Information Theory							
Information Theory							
<b>Module number:</b> M.048.24021	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2.-4. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.048.24021 Information Theory	2L 2Ex, SS	60	120	CE	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Information Theory:</i> **Recommended:* Prior knowledge from the modules Signal and Information Transmission, and Probability for Engineers.						
4	<b>Contents:</b>  <i>Contents of the course Information Theory:</i> Information theory is the mathematical foundation of modern communication, data compression, and statistical inference. Originally developed by Claude Shannon, information theory quantifies information, uncertainty, and the fundamental limits of data transmission and storage. This course provides a first rigorous introduction to information theory. Students will learn the core concepts of entropy, mutual information, relative entropy, and typicality, and understand their operational meaning in compression and communication systems. The course covers lossless and lossy source coding, channel capacity, and the fundamental limits of reliable communication over noisy channels. Classical results such as Shannon's source coding theorem and channel coding theorem are derived from first principles. Strong emphasis will be placed on an intuitive understanding of mathematical concepts and the practical interpretation of these results in the context of real-world systems. Beyond communication systems, the course highlights connections to machine learning, statistics, signal processing, and networked systems.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to</p> <ul style="list-style-type: none"> <li>• explain fundamental questions, models, tools, and results of Shannon's information theory;</li> <li>• apply information-theoretic measures to assess the quality processing blocks of communication systems and explain the operational meaning;</li> <li>• explain the relationship between abstract communication-theoretic models and realistic physical systems and apply them purposefully in system design;</li> <li>• understand scientific documents in information theory.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can abstract complex problems and decompose them into solvable subproblems;</li> <li>• are able to understand and apply mathematics as a universal language for describing the physical world;</li> <li>• can transfer information-theoretic concepts and results to problems beyond the field of communications engineering.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>										
12	<p><b>Module coordinator:</b> Dr.-Ing. Bho Matthiesen</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/nt">https://www.hni.uni-paderborn.de/en/nt</a></p> <p><b>Implementation</b> Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b> Handouts and tutorial questions; literature references will be given in the first lecture</p>
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4 General Elective Area

Optical Waveguide Theory						
Optical Waveguide Theory						
<b>Module number:</b> M.048.24019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.24019 Optical Waveguide Theory	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Optical Waveguide Theory:</i> <b>Recommended:</b> Bachelor-level knowledge in electrodynamics and mathematics as taught in the course Fields&Waves.					
4	<b>Contents:</b>  <i>Contents of the course Optical Waveguide Theory:</i> <b>Short Description</b> Dielectric optical waveguides constitute key-elements of present-day integrated optical / photonic circuits. This course provides an introduction to their theoretical background, and, as such, a sound basis for further, more specific, modelling, simulation, and design work, as well as for experimental activities in the field. <b>Contents</b> <ul style="list-style-type: none"><li>• Photonics / integrated optics, dielectric waveguides: introductory examples, motivation.</li><li>• Brush up on mathematical tools.</li><li>• Maxwell equations, survey of different formulations; classes of simulation tasks.</li><li>• Normal modes of dielectric optical waveguides, orthogonality, completeness, scattering matrices, reciprocal circuits.</li><li>• Examples for dielectric optical waveguides (multilayer slabs, integrated optical channels, fibers), bent waveguides, whispering gallery resonances.</li><li>• Coupled mode theory, conventional codirectional, and hybrid analytical / numerical variant, perturbations of optical waveguides.</li><li>• Optional, brief remarks on: boundary conditions, initial value problems (beam propagation method), waveguide discontinuities (BEP/QUEP simulations), photonic crystal waveguides &amp; fibers, plasmonic waveguides.</li></ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the student will be able to</p> <ul style="list-style-type: none"> <li>• to mathematically model electromagnetic field problems of systems in integrated optics and photonics</li> <li>• to identify, apply and verify appropriate analytical methods and approximation techniques</li> <li>• to physically interpret and visualise the obtained results</li> <li>• to extend, develop and validate theoretical models for integrated optics and photonics</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• learn to transfer the acquired skills also to other disciplines</li> <li>• extend their cooperation and team capabilities as well as the presentation skills in the context of solving the exercises</li> <li>• learn strategies to acquire knowledge from literature and internet</li> <li>• acquire a specialised foreign language competence</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr. Manfred Hammer</p>								

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Waveguide Theory:</i></p> <p><b>CourseHomepage</b> <a href="http://ei.uni-paderborn.de/tet/">http://ei.uni-paderborn.de/tet/</a></p> <p><b>Implementation</b> The theoretical concepts will be presented as a lecture. The methods presented will be practiced in exercises classes and by means of homework assignments.</p>
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4 General Elective Area

Optimale und Adaptive Filter						
Optimal and Adaptive Filters						
<b>Module number:</b> M.048.24010	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24010 Optimal and Adaptive Filters	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optimale und Adaptive Filter:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics and Digital Signal Processing.					

4	<p><b>Contents:</b></p> <p><i>Contents of the course Optimale und Adaptive Filter:</i></p> <p><b>Short Description</b></p> <p>The course “Optimal and adaptive filters” gives an introduction to the basic techniques and theories of adaptive filters. Based upon the basics of estimation theory optimal filters are discussed. Subsequently the topics Wiener filter theory, deterministic optimization under constraints and stochastic gradient methods are regarded. Concluding the Least Squares approach for solving filter tasks and the Kalman filter are introduced. The latter is regarded as a brief introduction to state based filters.</p> <p><b>Contents</b></p> <ul style="list-style-type: none"><li>• Classic parameter estimation</li><li>• Estimators</li><li>• MMSE-Estimation</li><li>• Linear estimators</li><li>• Orthogonality principle</li><li>• Evaluation of estimators</li><li>• Wiener filter</li><li>• Wiener-Hopf equation</li><li>• AR- and MA processes</li><li>• Linear prediction</li><li>• Iterative optimization methods</li><li>• Gradient ascent/descent</li><li>• Newton method</li><li>• Linear adaptive filters</li><li>• LMS algorithm</li><li>• Least-Squares method</li><li>• Blockwise and recursive adaptiv filters</li><li>• Realization aspects</li><li>• Statemodel based filters</li><li>• Kalman filter</li><li>• Applications</li><li>• System identification</li><li>• Channel estimation and equalization</li><li>• Multi-channel speech signal processing</li><li>• Noise and interference suppression</li></ul>
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#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• analyze task on the field of adaptive filters and to formulate requirements mathematically,</li> <li>• develop filter using cost functions and</li> <li>• implement selected adaptive filters in the frequency or time domain.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to check theoretical results using practical realizations,</li> <li>• are able to undertake theoretical approaches a systematic analysis using methodical procedures and</li> <li>• are, due to the precise treatment of the contents, in a position to continue their learning themselves.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Jörg Schmalenströer</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optimale und Adaptive Filter:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/optimal-and-adaptive-filter">https://ei.uni-paderborn.de/en/nt/teaching/veranstaltungen/optimal-and-adaptive-filter</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures using the blackboard and presentations,</li><li>• Alternating theoretical and practical exercises classes with exercise sheets and computer and</li><li>• Demonstration of real technical systems in the lecture hall.</li></ul> <p><b>Teaching Material, Literature</b> Allocation of a script; information on textbooks; matlab scripts</p>
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4 General Elective Area

Theory and Design of Phase-locked Loops						
Theory and Design of Phase-locked Loops						
<b>Module number:</b> M.048.24020	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.24020 Theory and Design of Phase-locked Loops	2L 2Ex, WS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Theory and Design of Phase-locked Loops:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• Advanced Signal Theory</li> <li>• Statistical Signal Processing (or another course with comparable syllabus in their bachelor)</li> <li>• Circuit and system design (or another course with comparable syllabus in their bachelor)</li> </ul>					

## 4 General Elective Area

<b>4</b>	<p><b>Contents:</b></p> <p><i>Contents of the course Theory and Design of Phase-locked Loops:</i></p> <p>Chapter 1: Motivation</p> <ul style="list-style-type: none"> <li>• Time and frequency definition – definition in SI units – static frequency error</li> <li>• random fluctuations – Amplitude/Phase (AM/PM) noise</li> </ul> <p>Chapter 2: Mathematical formalism of signals</p> <ul style="list-style-type: none"> <li>• baseband and bandpass signals</li> <li>• time and frequency domain</li> </ul> <p>Chapter 3: introduction to random processes</p> <ul style="list-style-type: none"> <li>• baseband random processes and noise – correlation functions in time and frequency domain – some basedband random processes (thermal noise, shot noise, flicker noise . . .)</li> <li>• bandpass random processes – correlation functions – relation to baseband processes – phase noise and amplitude noise</li> </ul> <p>Chapter 4: PLL building blocks</p> <ul style="list-style-type: none"> <li>• Phase detector – Phase detector model – phase noise of phase detector</li> <li>• VCO – VCO model – phase noise of VCO</li> <li>• Frequency translators – frequency divider – frequency multiplier – phase noise of frequency translators</li> <li>• transistor level design of PLL blocks</li> </ul> <p>Chapter 5: Integer N PLLs: – Time domain – frequency domain – phase noise – spurious frequencies</p> <p>Chapter 6: Fractional PLLs – Time domain – frequency domain – phase noise – spurious frequencies</p>								
<b>5</b>	<p><b>Learning outcomes and competences:</b></p> <p>Understanding of static/dynamic error in frequency standards. Mathematical modeling of amplitude/phase noise. Modeling random processes in time and frequency domain. Basic and modern PLL architecture and its building blocks. Systematic design of PLLs. Design and Modeling of Integer/Fractional N PLL.</p>								
<b>6</b>	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)              <input type="checkbox"/>Module exam (MP)              <input type="checkbox"/>Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
<b>7</b>	<p><b>Study Achievement:</b></p> <p>none</p>								
<b>8</b>	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								

#### 4 General Elective Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr. Meysam Bahmanian</p>
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls">https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>

#### 4 General Elective Area

Topics in Signal Processing							
Topics in Signal Processing							
<b>Module number:</b> M.048.92014	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92014 Topics in Signal Processing	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Topics in Signal Processing:</i> <b>Recommended:</b> Signal and system theory, at least a basic understanding of probability and linear algebra						
4	<b>Contents:</b>  <i>Contents of the course Topics in Signal Processing:</i> <b>Short Description</b> This course covers a selection of current topics in signal processing. One part of this course will follow a regular lecture format, while the other part will require active student participation. <b>Contents</b> This course will first review relevant aspects of linear algebra and probability theory. Then students will learn how to read, analyze, and present recent papers from the signal processing literature.						
5	<b>Learning outcomes and competences:</b>  In this course, students will familiarize themselves with some current research topics in signal processing. They will learn to read and understand scientific publications and to critically evaluate results. Students will develop confidence in their ability to solve mathematical problems of analysis and design. They will be able to apply the principles they have learnt in this course to other areas.						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)		
12	<b>Module coordinator:</b> Prof. Dr. Peter Schreier		
13	<b>Other Notes:</b> <i>Remarks of course Topics in Signal Processing:</i> <b>Course Homepage</b> <a href="http://sst.uni-paderborn.de/teaching/courses/">http://sst.uni-paderborn.de/teaching/courses/</a> <b>Implementation</b> Lectures and tutorials with active student participation, student presentations <b>Teaching Material, Literature</b> References will be given in the first lecture.		

4 General Elective Area

Wireless Communications						
Wireless Communications						
<b>Module number:</b> M.048.24022	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2.-4. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.24022 Wireless Communications	2L 2Ex, SS	60	120	CE	30 / 30
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> <i>Prerequisites of course Wireless Communications:</i> <b>Recommended:</b> Prior knowledge from the modules Signal and Information Transmission, Signal Theory, and Probability for Engineers.					
4	<b>Contents:</b> <i>Contents of the course Wireless Communications:</i> The course provides students with important basic knowledge necessary for understanding the transmission concepts of modern mobile communication systems. Starting with a physical understanding of the mobile communications channel, in particular the phenomena of multipath propagation and the Doppler effect, the physical and statistical modeling of time-varying communication systems is presented. Subsequently, important diversity techniques for channel-adaptive transmission and multiple access techniques are introduced.					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After completion of the course students will be able to</p> <ul style="list-style-type: none"> <li>• understand the fundamental propagation mechanisms in mobile radio channels;</li> <li>• describe and apply physical and statistical modeling principles to time-varying communication channels;</li> <li>• analyze fading processes and assess their impact on transmission reliability;</li> <li>• explain diversity techniques, apply them to design physical layer signal processing algorithms, and assess their impact on transmission reliability;</li> <li>• explain, compare, and evaluate common multiple access schemes;</li> <li>• assess transmission concepts employed in contemporary mobile communication systems;</li> <li>• connect theoretical channel models with practical system design considerations.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• have strong analytical and mathematical reasoning skills;</li> <li>• have the ability to abstract complex physical phenomena into engineering models;</li> <li>• can interpret analytical results at the system level;</li> <li>• have experience in clear and precise technical communication (oral and written)</li> <li>• can learn independently and engage critically with technical literature.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP) </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5)</p>								

#### 4 General Elective Area

12	<b>Module coordinator:</b> Dr.-Ing. Bho Matthiesen
13	<b>Other Notes:</b> <b>Module Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/nt">https://www.hni.uni-paderborn.de/en/nt</a> <b>Implementation</b> Lectures and exercises <b>Teaching Material, Literature</b> Lecture slides and reading assignments for each lecture; additional reading suggestions.

## 4.4 EE Catalogue Microelectronics

Advanced VLSI Design						
Advanced VLSI Design						
<b>Module number:</b> M.048.92043	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.92043 Advanced VLSI Design	2L 2Ex, SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Advanced VLSI Design:</i> <b>Recommended:</b> Fundamentals of Digital Circuits / Fundamentals of VLSI Design Information: Unless otherwise specified, these are recommendations.					
4	<b>Contents:</b> <i>Contents of the course Advanced VLSI Design:</i> <b>Short Description</b> The course provides basic knowledge about the modern application-oriented modeling, simulation, analysis, and synthesis of digital systems at different abstraction levels to chip layout. <b>Contents</b> In today's practice, chip design consists of the combined application of various languages, methods, and tools for the modeling, simulation, and synthesis of electronic circuits. Along the modern abstraction-based design flow of digital systems (electronic system level to chip layout), the course provides basic knowledge of the main description languages and their application in modeling, simulation, analysis and synthesis. This includes basic principles and application of the IEEE standard system/hardware description languages SystemVerilog, SystemC, Verilog, and VHDL, in conjunction with additional formats, e.g., SDF and UPF for time and power annotation. For their application, the fundamental principles of test environments for simulation, timing and power analysis, logic synthesis and physical design of digital circuits. Exercises will provide hands-on labs based on commercial tools from Mentor Graphics, Synopsys and, Cadence Design Systems.					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After the course students are able</p> <ul style="list-style-type: none"> <li>• to model, simulate, analyze and synthesize simple digital circuits at different abstraction levels and</li> <li>• to apply the most important commercial tools for simulation, analysis and synthesis of digital circuits.</li> </ul> <p><b>Key qualifications:</b> After the course students are able</p> <ul style="list-style-type: none"> <li>• to assess, select and apply modern digital circuit description languages for their different applications,</li> <li>• apply the different methods and tools in the modern VLSI design.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>apl. Prof. Dr. Wolfgang Müller</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced VLSI Design:</i></p> <p><b>Course Homepage</b> <a href="http://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design">www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/advanced-vlsi-design</a></p> <p><b>Implementation</b> * Vorlesung mit Beamer und White-Board * Übungen mit Übungsblättern am Computer * Lecture with LCD projector and white board * Exercises with assignments and hands-on labs</p> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture notes and exercise sheets will be provided via PAUL</li><li>• IEEE standard reference manuals: IEEE Std 1800/1685/1666/1364/1076/1801/1497</li><li>• Specific references for individual teaching units</li></ul>
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4 General Elective Area

<b>Algorithms and Tools for Test and Diagnosis of Systems on a Chip</b>						
Algorithms and Tools for Test and Diagnosis of Systems on a Chip						
<b>Module number:</b> M.048.92007	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.92007 Algorithms and Tools for Test and Diagnosis of Systems on a Chip	2L 2Ex, WS+SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Recommended:</b> VLSI Testing, (Introduction to Algorithms)					
4	<b>Contents:</b>  <i>Contents of the course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i> <b>Short Description</b> The course “Algorithms and Tools for Test and Diagnosis of Systems on Chip” deals with advanced topics in test and diagnosis of integrated systems. The focus is on algorithms and tools for computer-aided preparation and application of test and diagnosis procedures. <b>** Contents**</b> Topics include but are not restricted to: <ul style="list-style-type: none"><li>• Advanced techniques for built-in self-test and embedded test</li><li>• Built-in diagnosis</li><li>• Test of robust and self-adaptive systems</li><li>• Adaptive Testing</li></ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe recent approaches in test and diagnosis,</li> <li>• to explain and apply the underlying models and algorithms,</li> <li>• to explain the specific challenges of nanoscale integration and evaluate test strategies accordingly.</li> </ul> <p><b>Key qualifications:</b> The students are able</p> <ul style="list-style-type: none"> <li>• to apply their basic knowledge for studying and understanding new approaches from the state of the art literature,</li> <li>• to present the new contents in a conference style presentation, and</li> <li>• to describe the new contents in a scientific manuscript.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sybille Hellebrand</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Algorithms and Tools for Test and Diagnosis of Systems on a Chip:</i></p> <p><b>Module Homepage</b> <a href="http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">http://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Self-study on recent approaches based on recent conference and journal publications</li><li>• Oral presentation</li><li>• Manuscript</li></ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides</li><li>• Additional material can be found in panda</li><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Kluwer Academic Publishers,2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li><li>• Artikel aus Fachzeitschriften und Konferenzbänden / Articles from Journals and Conference Proceedings (e.g. IEEE Transactions on Computers, IEEE Transactions on CAD of Integrated Circuits and Systems, IEEE International Test Conference, etc.)</li></ul>
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#### 4 General Elective Area

Analoge CMOS-Schaltkreise							
Analog CMOS ICs							
<b>Module number:</b> M.048.25008	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.25008 Analog CMOS ICs	2L 2Ex, SS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Analoge CMOS-Schaltkreise:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics, Physics, and the Foundations of Electrical Engineering, Materials of Electrical Engineering, Semiconductor Devices, Signal Theory, System Theory.						
4	<b>Contents:</b>  <i>Contents of the course Analoge CMOS-Schaltkreise:</i> <b>Short Description</b> The course provides basic knowledge on analogue circuit technology with particular regard to complementary MOS transistors. <b>Contents</b> Based on simplified as well as advanced current-voltage characteristics of MOS transistors, analogue amplifier circuits are introduced and analyzed with respect of its DC behavior. Next, frequency performance, noise, effects of feed-backs, stability, non-linearity, and impacts of fabrication related asymmetries are considered. Further circuits such as oscillators, reference voltage sources, and switched capacitors are discussed. The course concludes with remarks on modeling and layout issues of basic devices.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• analyse the characteristics of analogue circuits using scientific methods</li> <li>• and can make creative use of the acquired knowledge in the circuit design process.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• make use of methodic knowledge for systematic problem analysis,</li> <li>• consolidate their basic knowledge by practical training,</li> <li>• enhance their creative abilities,</li> <li>• and gain foreign language competences related to the field.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b> none</p>										
8	<p><b>Prerequisites for participation in examinations:</b> None</p>										
9	<p><b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b> Prof. Dr. Andreas Thiede</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Analoge CMOS-Schaltkreise:</i></p> <p><b>Course Homepage</b> <a href="http://groups.upb.de/hfe/teaching/acc.html">http://groups.upb.de/hfe/teaching/acc.html</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with black board presentation, supported by animated graphics and transparencies,</li><li>• Presence exercises with task sheets to be solved by the students together, supported by the teacher.</li></ul> <p><b>Teaching Material, Literature</b></p> <p>A. Thiede, Analog CMOS Integrated Circuits, Vorlesungsskript Universität Paderborn A. Thiede, Analog CMOS Integrated Circuits, Lecture Script University Paderborn</p> <ul style="list-style-type: none"><li>• Razavi, B.: Design of Analog CMOS Integrated Circuits. McGraw Hill. 2001</li></ul>
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#### 4 General Elective Area

Hochfrequenzleistungsverstärker							
Radio Frequency Power Amplifiers							
<b>Module number:</b> M.048.25015	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.25015 Radio Frequency Power Amplifiers	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Hochfrequenzleistungsverstärker:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics, Physics, and the Foundations of Electrical Engineering, Materials of Electrical Engineering, Semiconductor Devices, Signal Theory, System Theory, High-Frequency Electronics.						
4	<b>Contents:</b>  <i>Contents of the course Hochfrequenzleistungsverstärker:</i> <b>Short Description</b> The course provides basic knowledge on the design of integrated RF power amplifiers, in particular for mobile communication and sensor applications. <b>Contents</b> The course starts with an overview on analysis and simulation techniques for non-linear circuits. After that, first the conventional amplifier classes A, AB, B, and C are analysed and in particular overdrive effects are investigated. Second, the specific amplifier classes D, E,F, and S are introduced. Next, dedicated measures for the efficiency enhancement and linearization are described and particular amplifier architectures are presented. The course ends with an overview on semiconductor fabrication technologies for power amplifiers.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• describe and analyse the performance of non-linear amplifiers,</li> <li>• distinguish, make dedicated use, and dimension power amplifiers of different classes,</li> <li>• take effective measures for efficiency enhancement and linearization,</li> <li>• and to select appropriate semiconductor fabricated technologies for given problems.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can make use of methodic knowledge for systematic problem analysis,</li> <li>• include aspects of fabrication technology and economy into complex optimization problems,</li> <li>• get familiar with the CAD system ADS, which is commonly used in industry</li> <li>• and gain foreign language competences related to the field.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Andreas Thiede</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Hochfrequenzleistungsverstärker:</i></p> <p><b>Course Homepage</b> <a href="http://groups.uni-paderborn.de/hfe/teaching/acc.html">http://groups.uni-paderborn.de/hfe/teaching/acc.html</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with black board presentation, supported by animated graphics and transparencies</li><li>• Presence exercises with task sheets to be solved by the students together, supported by the teacher, and partially using CAD software.</li></ul> <p><b>Teaching Material, Literature</b></p> <p>A. Thiede, RF Power Amplifiers, Vorlesungsskript Universität Paderborn A. Thiede, RF Power Amplifiers, Lecture Script University Paderborn Steve C. Cripps, RF Power Amplifiers for Wireless Communications, Artech House, 1999 Stephen A. Maas, Nonlinear Microwave and RF Circuits, Artech House, 1997</p>
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4 General Elective Area

Integrierte Schaltungen für die drahtlose Kommunikation						
Integrated Circuits for Wireless Communications						
<b>Module number:</b> M.048.25017	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25017 Integrated Circuits for Wireless Communications	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Integrierte Schaltungen für die drahtlose Kommunikation:</i> <b>Recommended:</b> Lecture Schaltungstechnik resp. Circuit and System Design. Helpful supplement: Lecture "Wireless Communications" of Prof. Hab-Umbach.					

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Mobile communications, wireless networks, and RFID technology are application examples of wireless communications. Wireless communications has found widespread use in everyday life and will become even more important in the future. The design of electronic circuits for radio frequencies requires a good system knowledge with respect to typical transmitter and receiver architectures in wireless communications, components, and radio signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to convey a methodical approach to the design of integrated circuits for wireless communications. A part of the exercises will pertain to calculation of circuit design problems another will be performed in small teams as a hands-on exercise using modern IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of radio frequency integrated circuits for wireless communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures “Schaltungstechnik” resp. “Circuit and System Design”. The following topics will be addressed:</p> <ul style="list-style-type: none"> <li>• Transmitter and receiver architectures for wireless communications</li> <li>• System Theory Basics             <ul style="list-style-type: none"> <li>– Signals and noise</li> <li>– Modulation and demodulation</li> <li>– Transmission properties of wireless communications systems</li> </ul> </li> <li>• Semiconductor technologies and integrated high-frequency devices</li> <li>• Amplifiers (low-noise and variable-gain amplifiers)</li> <li>• Mixers</li> <li>• Oscillators</li> <li>• Frequency synthesizer PLLs</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>The students will be able</p> <ul style="list-style-type: none"> <li>• to describe architectures and circuits of wireless communication systems</li> <li>• to describe and calculate fundamental signal transmission properties of wireless systems</li> <li>• to apply design methods to design components of radio frequency ICs</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                  <input type="checkbox"/> Module exam (MP)                  <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Oral Examination</td> <td style="text-align: center;">30-45 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Oral Examination	30-45 min	100%
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a)	Oral Examination	30-45 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								

#### 4 General Elective Area

8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. J. Christoph Scheytt</p>
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Integrierte Schaltungen für die drahtlose Kommunikation:</i></p> <p><b>Course Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/integrierte-schaltungen-fuer-die-drahtlose-kommunikation/</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"> <li>• Lecture with Powerpoint presentation and handwritten mathematical derivations using tablet and beamer</li> <li>• Exercises partly as handwritten calculation exercises using tablet and beamer and partly as practical IC design exercises using IC design software</li> </ul> <p><b>Teaching Material, Literature</b></p> <p>Lecture slides and videos as well as exercise slides will be made available.</p> <ul style="list-style-type: none"> <li>• Behzad Razavi "RF Microelectronics", Prentice Hall, 2011</li> <li>• Thomas Lee "The Design of CMOS Radio-Frequency Integrated Circuits", Cambridge University Press 2003</li> </ul>

4 General Elective Area

<b>Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation</b>						
Fast Integrated Circuits for Wireline Communications						
<b>Module number:</b> M.048.25019	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.048.25019 Fast Integrated Circuits for Wireline Communications	2L 2Ex, WS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Recommended:</b> Module "Schaltungstechnik" of the Bachelor Electrical Engineering or module "Circuit and System Design" of the Master "Electrical Systems Engineering" or comparable modules / lectures					

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i></p> <p><b>Short Description</b></p> <p>Nowadays commercial fiber-optic communication systems reach very high data rates of 100 Gb/s per optical channel and several Tb/s in a single fiber. In a similar way very high data rates of more than 10 Gb/s occur at a single package pin of electronic chips. These signals are to be transmitted over printed circuit boards and inexpensive serial cables. In the future the progress of CMOS technology and communication technology will push speed of fiber-optic and wire-line communication continuously to ever higher data rates. The design of electronic circuits for high bandwidth resp. data rates requires a good system knowledge with respect to typical transmitter and receiver architectures, components, and signal properties. Furthermore a thorough understanding of integrated circuit design as well as precise high-frequency modeling of passive and active devices are required. Goal of the lecture is to enable the student to utilize a methodological approach for the design of fast integrated electronic circuits for digital wired communications. A part of the exercises will be carried out using modern industry-standard IC design software.</p> <p><b>Contents</b></p> <p>The lecture deals with analysis and design of fast integrated electronic circuits for digital broadband communication systems. A part of the exercises will be performed using modern chip design CAD tools. The lecture is based on the compulsory lectures "Schaltungstechnik" resp. "Circuit and System Design". The lecture deals with:</p> <ul style="list-style-type: none"><li>• Transmitter and receiver architectures for fiber-optic communications</li><li>• Transmitter and receiver architectures for chip-to-chip communications</li><li>• System design</li><li>• Semiconductor technology and integrated high-frequency devices</li><li>• Broadband amplifiers</li><li>• Current-mode logic</li><li>• Transmitter and receiver circuits</li><li>• PLLs for frequency synthesis and clock recovery</li><li>• Measurement methods</li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b></p> <p>The student will be able to:</p> <ul style="list-style-type: none"><li>• describe and analyze transmitter and receiver architectures for broadband communication links</li><li>• understand and describe semiconductor technologies and integrated high-frequency devices for broadband circuits</li><li>• to analyze circuit design techniques for transmitter and receiver circuits and describe ways to optimize them</li><li>• to describe circuits in PLL technique for frequency synthesis and clock recovery</li><li>• to describe measurement methods</li></ul> <p><b>Key qualifications:</b></p> <p>The students will learn how different interdisciplinary scientific domains and their methods - like mathematical signal and system analysis, non-linear and linear circuit analysis, semiconductor physics, semiconductor devices and high-frequency engineering - are applied together for the development of communications application.</p>

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Oral Examination	30-45 min	100%
7	<b>Study Achievement:</b> none		
8	<b>Prerequisites for participation in examinations:</b> None		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination (MAP) was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4		
12	<b>Module coordinator:</b> Prof. Dr.-Ing. J. Christoph Scheytt		
13	<b>Other Notes:</b> <i>Remarks of course Schnelle integrierte Schaltungen für die leitungsgebundene Kommunikation:</i> <b>Course Homepage</b> <a href="https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/">https://www.hni.uni-paderborn.de/en/system-and-circuit-technology/teaching/fast-integrated-circuits-for-wireline-communications/</a> <b>Implementation</b> Lecture with Exercises (including computer-aided design using electronic design software) <b>Teaching Material, Literature</b> Handouts and literature references will be given in the lecture. <ul style="list-style-type: none"> <li>• E. Säckinger, "Broadband Circuits for Optical Fiber Communication", Wiley, 2005</li> <li>• B. Razavi, "Design of Integrated Circuits for Optical Communications", McGraw-Hill, 2003</li> </ul> <b>Comments</b> As part of the lecture a 2-day excursion to IHP Leibnizinstitute for High-Performance Microelectronics in Frankfurt (Oder) is offered which includes the visit of a modern chip fabrication facility (participation in the excursion is voluntary).		

4 General Elective Area

Theory and Design of Phase-locked Loops							
Theory and Design of Phase-locked Loops							
<b>Module number:</b> M.048.24020	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.24020 Theory and Design of Phase-locked Loops	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Theory and Design of Phase-locked Loops:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• Advanced Signal Theory</li> <li>• Statistical Signal Processing (or another course with comparable syllabus in their bachelor)</li> <li>• Circuit and system design (or another course with comparable syllabus in their bachelor)</li> </ul>						

## 4 General Elective Area

<b>4</b>	<p><b>Contents:</b></p> <p><i>Contents of the course Theory and Design of Phase-locked Loops:</i></p> <p>Chapter 1: Motivation</p> <ul style="list-style-type: none"> <li>• Time and frequency definition – definition in SI units – static frequency error</li> <li>• random fluctuations – Amplitude/Phase (AM/PM) noise</li> </ul> <p>Chapter 2: Mathematical formalism of signals</p> <ul style="list-style-type: none"> <li>• baseband and bandpass signals</li> <li>• time and frequency domain</li> </ul> <p>Chapter 3: introduction to random processes</p> <ul style="list-style-type: none"> <li>• baseband random processes and noise – correlation functions in time and frequency domain – some basedband random processes (thermal noise, shot noise, flicker noise . . .)</li> <li>• bandpass random processes – correlation functions – relation to baseband processes – phase noise and amplitude noise</li> </ul> <p>Chapter 4: PLL building blocks</p> <ul style="list-style-type: none"> <li>• Phase detector – Phase detector model – phase noise of phase detector</li> <li>• VCO – VCO model – phase noise of VCO</li> <li>• Frequency translators – frequency divider – frequency multiplier – phase noise of frequency translators</li> <li>• transistor level design of PLL blocks</li> </ul> <p>Chapter 5: Integer N PLLs: – Time domain – frequency domain – phase noise – spurious frequencies</p> <p>Chapter 6: Fractional PLLs – Time domain – frequency domain – phase noise – spurious frequencies</p>								
<b>5</b>	<p><b>Learning outcomes and competences:</b></p> <p>Understanding of static/dynamic error in frequency standards. Mathematical modeling of amplitude/phase noise. Modeling random processes in time and frequency domain. Basic and modern PLL architecture and its building blocks. Systematic design of PLLs. Design and Modeling of Integer/Fractional N PLL.</p>								
<b>6</b>	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
<b>7</b>	<p><b>Study Achievement:</b></p> <p>none</p>								
<b>8</b>	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								

#### 4 General Elective Area

9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>
12	<p><b>Module coordinator:</b></p> <p>Dr. Meysam Bahmanian</p>
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b>  <a href="https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls">https://www.hni.uni-paderborn.de/en/sct/teaching/theory-and-design-of-plls</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b>  Handouts and tutorial questions; literature references will be given in the first lecture</p>

4 General Elective Area

VLSI-Testing							
VLSI-Testing							
<b>Module number:</b> M.048.92027	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92027 VLSI Testing	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course VLSI Testing:</i> <b>Recommended:</b> Digital Design						
4	<b>Contents:</b> <i>Contents of the course VLSI Testing:</i> <b>Short Description</b> The course "VLSI Testing" focuses on techniques for detecting hardware defects in micro-electronic circuits. Algorithms for test data generation and test response evaluation as well as hardware structures for design for test (DFT) and on-chip test implementation (BIST) are presented. <b>Contents</b> In detail the following topics are covered: <ul style="list-style-type: none"> <li>• Fault models</li> <li>• Testability measures and design for test (DFT)</li> <li>• Logic and fault simulation</li> <li>• Automatic test pattern generation (ATPG)</li> <li>• Built-in self-test (BIST), in particular test data compression and test response compaction</li> <li>• Memory test</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able</p> <ul style="list-style-type: none"> <li>• to describe fault models, DFT techniques, and test tools,</li> <li>• to explain and apply the underlying models and algorithms for fault simulation and test generation,</li> <li>• to analyze systems with respect to their testability and to derive appropriate test strategies.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the practiced strategies for problem solving across varying disciplines,</li> <li>• have experience in presenting their solutions to their fellow students, and</li> <li>• know how to improve their competences by private study.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sybille Hellebrand</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course VLSI Testing:</i></p> <p><b>Course Homepage</b> <a href="https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview">https://ei.uni-paderborn.de/en/electrical-engineering/date/teaching/electrical-engineering/overview</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lecture based on slide presentation, extensions on blackboard</li><li>• Exercises in small groups based on exercise sheets with students presenting their own solutions</li><li>• Hands-on exercises using various software tools</li></ul> <p><b>Teaching Material, Literature</b></p> <p>Additional material can be found in panda</p> <ul style="list-style-type: none"><li>• Michael L. Bushnell, Vishwani D. Agrawal, „Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits,“ Boston, Dordrecht, London: Kluwer Academic Publishers, 2000</li><li>• Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, „VLSI Test Principles and Architectures: Design for Testability,“ Morgan Kaufmann Series in Systems on Silicon, ISBN: 0123705975</li></ul>
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## 4.5 EE Catalogue Optoelectronics

Fundamentals of Optics						
Fundamentals of Optics						
<b>Module number:</b> M.048.26007	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.26007 Fundamentals of Optics	2L 2Ex, WS/SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Fundamentals of Optics:</i> <b>Recommended:</b> It is strongly advised that the participants should have already passed a more introductory lecture such as "Fields and Waves", "Electromagnetic Waves and Waveguides", or any similar lectures on basic concepts in electromagnetic field propagation. Hence, participants should already be generally familiar with concepts such as Maxwell's equations, constitutive relations, wave equation and plane-wave solutions to it, and Poynting vector.					
4	<b>Contents:</b> <i>Contents of the course Fundamentals of Optics:</i> <ul style="list-style-type: none"> <li>• Diffraction theory (describing beam propagation in homogenous media, considering different approximation regimes, e.g. Fresnel regime and Fraunhofer regime).</li> <li>• Gaussian beams (including propagation of Gaussian beams and Gaussian beams in a resonator).</li> <li>• Pulse propagation in dispersive homogenous media (considering effects like chirp and also specific case of a Gaussian pulse).</li> <li>• Fourier optics (studying the transfer function of a thin lens, 2f setups and 4f setups for optical filtering).</li> <li>• Optics in anisotropic media (studying the index ellipsoid and the normal modes of anisotropic crystals).</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending the lectures and the associated exercise sessions, the students will:</p> <ul style="list-style-type: none"> <li>• Be able to mathematically model problems in spatial and temporal propagation of electromagnetic/optical field in homogenous media</li> <li>• Gain physical intuition about the electromagnetic/optical beam and pulse propagation</li> <li>• Be able to identify the appropriate methods and approximations to solve field propagation problems</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade								
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sina Saravi</p>										
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> Not yet available</p> <p><b>Implementation</b> The theoretical concepts will be taught in a lecture format. The exercises consist of theoretical problems related to the concepts from the lecture, to be solved mathematically by the students, and later on the solutions will be presented in the exercise sessions.</p> <p><b>Teaching Material, Literature</b> Lecture notes will be handed out; literature recommendations will be given in the first lecture.</p>										

4 General Elective Area

Advanced Quantum Optics						
Advanced Quantum Optics						
<b>Module number:</b> M.048.26010	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.26010 Advanced Quantum Optics	2L 2Ex, WS/SS	60	120	C	30/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Advanced Quantum Optics:</i> <b>Recommended:</b> It is strongly advised that the participants should have already passed a more introductory lecture such as “Theoretical Quantum Optics”, “Quantum Optics”, or any similar lectures on basic concepts in quantum optics. Hence, participants should already be generally familiar with concepts such as field quantization, properties of nonclassical states of light such as Fock states and coherent states, quantum theory of light-matter interaction and Jaynes-Cummings model.					
4	<b>Contents:</b> <i>Contents of the course Advanced Quantum Optics:</i> <ul style="list-style-type: none"> <li>• Introducing density operators and quantum operations (Krauss maps)</li> <li>• Derivation of Lindblad master equation from both a phenomenological approach and a derivation from first principles. Introducing Jump operators.</li> <li>• Addressing various examples of an open quantum system involving a 2-level system and/or a lossy optical cavity.</li> <li>• Introducing the physics and the mathematical methods for phase-space treatments in quantum optics, such as operator spaces and expansions in operator basis.</li> <li>• Introducing various quasi-probability distributions (including the Wigner function) and investigating the fundamental relations between them, including specific examples for some basic states.</li> <li>• Introducing general measurements.</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending the lectures and the associated exercise sessions, the students will:</p> <ul style="list-style-type: none"> <li>• Be able to mathematically model quantum optics problems in open quantum systems</li> <li>• Gain a basic yet fundamental understanding of phase-space methods in quantum optics</li> <li>• Be able to identify the appropriate methods and approximations to solve quantum optics problems in open quantum systems</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Sina Saravi</p>										
13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> Not yet available</p> <p><b>Implementation</b> The theoretical concepts will be taught in a lecture format. The exercises consist of theoretical problems related to the concepts from the lecture, to be solved mathematically by the students, and later on the solutions will be presented in the exercise sessions.</p> <p><b>Teaching Material, Literature</b> Lecture notes will be handed out; literature recommendations will be given in the first lecture.</p>										

4 General Elective Area

<b>Hochfrequenzelektronik</b>							
High-Frequency Electronics							
<b>Module number:</b> M.048.26001	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.26001 High-Frequency Electronics	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Hochfrequenzelektronik:</i> <b>Recommended:</b> Prior knowledge from the modules Higher Mathematics, Physics, and the Foundations of Electrical Engineering, Materials of Electrical Engineering, Semiconductor Devices, Signal Theory, System Theory, Introduction to High-Frequency Engineering.						
4	<b>Contents:</b>  <i>Contents of the course Hochfrequenzelektronik:</i> <b>Short Description</b> The course High-Frequency Electronics provides necessary knowledge for the design of integrated high-frequency circuits ranging from device physics, semiconductor technology, high-frequency engineering, and packaging technology. Besides conveying new specialized knowledge, skills developed by various other courses are integrated, and thus students are directly prepared for a professional life in the field. <b>Contents</b> Starting from physically founded properties of different semiconductor systems, knowledge about the function, modeling, and fabrication of special high-frequency transistors is conveyed. Subsequently, all necessary steps of a high-frequency amplifier design are explained with respect to theoretical concepts and practical implementation. After that, further circuits such as broad-band amplifiers, oscillators, mixers and digital gates are presented. As currently most interesting applications, optoelectronic data transmission systems, mixed-signal systems such as ADC, DAC, digital synthesizers and PLL's, as well as millimeter wave transceivers are discussed. The course closes with an overview of high-frequency assembling and packaging technologies.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• select the most suitable semiconductor technology for a given problem,</li> <li>• run the complete design process of a high-frequency integrated circuit,</li> <li>• and to characterize fabricated samples.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can use of methodic knowledge for systematic problem analysis,</li> <li>• include aspects of fabrication technology and economy into complex optimization problems,</li> <li>• get familiar with the CAD system ADS, which is commonly used in industry</li> <li>• and gain foreign language competences related to the field.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)              <input type="checkbox"/> Module exam (MP)              <input type="checkbox"/> Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Andreas Thiede</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Hochfrequenzelektronik:</i></p> <p><b>Course Homepage</b> <a href="http://groups.upb.de/hfe/teaching/hfe.html">http://groups.upb.de/hfe/teaching/hfe.html</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Vorlesungen mit überwiegendem Tafelinsatz, unterstützt durch Animationen und Folien,</li><li>• Präsenzübungen mit Aufgabenblättern, deren Lösungen die Studierenden in der Übung gemeinsam und mit Unterstützung des Übungsleiters, teilweise unter Einsatz von CAD-Software erarbeiten.</li></ul> <p><b>Teaching Material, Literature</b></p> <p>A. Thiede, High-Frequency Electronics, Vorlesungsskript Universität Paderborn A. Thiede, High-Frequency Electronics, Lecture Script University Paderborn References to continuative and deepening literature can be found in the respective sections of the script.</p>
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4 General Elective Area

Optical Communication A							
Optical Communication A							
<b>Module number:</b> M.048.92019	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92019 Optical Communication A	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication A:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication A:</i> <b>Short Description</b> The lecture Optical Communication A gives basic knowledge in Optical Communication and the components used in this field. <b>Contents</b> Maxwell's equations, wave propagation, polarization, dielectric slab and cylindrical waveguides, dispersion, laser, photodiodes, optical amplifiers, modulation, signal formats, optical receivers, noise, regenerators, wavelength division multiplex. Here the most important knowledge is taught.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication A:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• R. Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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4 General Elective Area

Optical Communication B							
Optical Communication B							
<b>Module number:</b> M.048.92020	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92020 Optical Communication B	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication B:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication B:</i> <b>Short Description</b> The lecture Optical Communication B gives some knowledge about mode coupling in Optical Communication and explains the function of many optical components. <b>Contents</b> Mode Coupling: Polarization mode dispersion, moden orthogonality, constant and periodic, co- and counterdirectional mode coupling, profiles of differential group delay, electrooptic effect. The function of many passive and active optical elements is thereby explained, among others amplitude and phase modulators, broadband and wavelength-selective couplers, Bragg gratings, polarization-maintaining fibers, polarization transformers, equalizers for polarization mode dispersion and chromatic dispersion.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication B:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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4 General Elective Area

Optical Communication C							
Optical Communication C							
<b>Module number:</b> M.048.92021	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92021 Optical Communication C	2L 2Ex, WS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication C:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication C:</i> <b>Short Description</b> The lecture Optical Communication C gives knowledge in various optical modulation and demodulation techniques. <b>Contents</b> Modulation Formats: Data transmission by differential binary and quaternary phase shift keying in the presence of optical amplifiers, polarization division multiplex, coherent optical data transmission, synchronous and asynchronous demodulation, coherent baseband receivers, polarization diversity, electronic compensators of optical distortions like electronic polarization control and electronic compensation of polarization mode dispersion and chromatic dispersion, phase noise, other modulation formats. Advanced modulation formats are an important possibility for the upgrading of high-performance optical information transmission systems.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication C:</i></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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4 General Elective Area

Optical Communication D							
Optical Communication D							
<b>Module number:</b> M.048.92022	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.92022 Optical Communication D	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optical Communication D:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optical Communication D:</i> <b>Short Description</b> The lecture Optical Communication D gives knowledge about nonlinear optical effects in waveguides, their electronic detection, furthermore polarization scrambling. <b>Contents</b> Selected Topics in Optical Communication: Nonlinear distortions in glass fibers and their polarization dependence, electronic detection of linear optical distortions, polarization scrambling, ... . Nonlinear distortions are important in practice and difficult to handle. The students should also prepare topics of their choice and present them to the others.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Professional Competence</b> After attending the course, the students will be able, in the taught subjects, to</p> <ul style="list-style-type: none"> <li>• describe, model and apply the function of components, systems and effects of optical communications and</li> <li>• apply knowledge of optoelectronics</li> </ul> <p><b>(Soft) Skills</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills to a wide range of disciplines,</li> <li>• are able to make use of a methodical procedure when undertaking systematic analysis and</li> <li>• are, due to the abstract and precise treatment of the contents, in a position to continue and develop their learning themselves</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Reinhold Noé</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Optical Communication D:</i></p> <p><b>Course Homepage</b> <a href="http://ont.upb.de">http://ont.upb.de</a></p> <p><b>Teaching Material, Literature</b></p> <p>Scripts, exercise sheets and advanced literature (excerpt):</p> <ul style="list-style-type: none"><li>• R. Noe, Essentials of Modern Optical Fiber Communication, Springer, 2. Auflage / 2nd Edition, 2016, ISBN 978-3-662-49621-3, ISBN ISBN 978-3-662-49623-7</li><li>• Petermann/Voges, Optische Kommunikationstechnik, Springer-Verlag (modernes Nachschlagewerk) 2002</li><li>• D. As, Univ. Paderborn, Vorlesung Optoelektronik</li><li>• W. Sohler, Univ. Paderborn, Vorlesung Integrierte Optik</li><li>• G. Grau, W. Freude, Optische Nachrichtentechnik, Springer-Verlag, Heidelberg, 1991, (umfassend, viele Zwischenschritte fehlen)</li><li>• K.J. Ebeling, Integrierte Optoelektronik, Springer-Verlag, Heidelberg, 1992</li><li>• H.-G. Unger, Optische Nachrichtentechnik, Teile I und II, Hüthig-Verlag Heidelberg, 1984 und 1985, (Schwerpunkt optische Wellenleiter)</li><li>• Yariv, Optical Electronics, Holt, 1984 (und weitere Werke, sehr physikalisch, kaum Nachrichtentechnik)</li><li>• R. Th. Kersten, Einführung in die Optische Nachrichtentechnik, Springer-Verlag</li></ul>
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#### 4 General Elective Area

Optoelectronics							
Optoelectronics							
<b>Module number:</b> M.048.26011	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.26011 Optoelectronics	2L 2Ex, SS	60	120	C	30/30	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Optoelectronics:</i> None						
4	<b>Contents:</b> <i>Contents of the course Optoelectronics:</i> <b>Short description</b> The lecture Optoelectronics covers the fundamental aspects of optoelectronic devices, starting with semiconductor materials and their interaction with light and photons, to the electronic aspects of the components, and finally to the use of quantum mechanical effects to optimise modern components for their respective areas of application, such as in lighting systems, renewable energy, broadband optical communication systems or in medical technology. <b>Contents</b> In the first part of the lecture, the basics of semiconductors (lattice structure, band structure, direct-indirect semiconductors, doping, degenerate and non-degenerate semiconductors, heterostructures, quantum effects in low-dimensional semiconductors) are recapitulated. The elementary interactions between light and semiconductors (absorption, stimulated emission, spontaneous emission) and the electronic aspects of the components (p-n junction, heterojunctions) are then covered. Finally, the most important devices such as solar cells, photodiodes, light-emitting diodes and semiconductor lasers are discussed in detail and their most important parameters and optimisation strategies are explained.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, the students will be able to</p> <ul style="list-style-type: none"> <li>• explain the basic physical properties of optoelectronic semiconductor devices based on classical and fundamental quantum mechanical descriptions,</li> <li>• to describe the main concepts of optoelectronic semiconductor devices (photodiodes, solar cells, light emitting diodes, semiconductor lasers),</li> <li>• categorize different device designs according to their application requirements.</li> </ul> <p><b>Key qualifications:</b> The students</p> <ul style="list-style-type: none"> <li>• can use of methodic knowledge for systematic problem analysis for a wide range of disciplines,</li> <li>• will be in position to familiarise themselves independently with new generations of semiconductor devices, thanks to the comprehensive fundamental training received,</li> <li>• get familiar to rate-equation models to simulate steady-state and dynamic characteristics in coupled systems,</li> <li>• and gain foreign language competences related to the field.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)              <input type="checkbox"/>Module exam (MP)              <input type="checkbox"/>Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td style="text-align: center;">120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Informationstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								

#### 4 General Elective Area

12	<b>Module coordinator:</b> Prof. Dr.-Ing. Nils Christopher Gerhardt
13	<b>Other Notes:</b> <b>Module Homepage</b> to be announced at the start of the lecture <b>Implementation</b> Lectures and exercises (including some computer simulations) <b>Teaching Material, Literature</b> Lecture notes and handouts for the tutorial; literature references will be given in the first lecture

## 4.6 EE Catalogue Process Dynamics

Advanced System Theory						
Advanced System Theory						
<b>Module number:</b> M.048.92001	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 1. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.92001 Advanced System Theory	2L 2Ex, WS/SS	60	120	C	60/30
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None <i>Prerequisites of course Advanced System Theory:</i> <b>Recommended:</b> Prerequisites are a basic understanding of differential equations, linear algebra, and Laplace transforms, as they are covered in a typical undergraduate course on system theory.					
4	<b>Contents:</b> <i>Contents of the course Advanced System Theory:</i> <b>Short Description</b> Building on an undergraduate system theory course, this course studies the dynamical behavior of linear systems with greater mathematical rigor. The course is primarily intended to serve students in engineering, but it can also be useful to students in physics and other natural sciences. <b>Contents</b> <ul style="list-style-type: none"> <li>• System models and differential equations</li> <li>• State-space and I/O descriptions</li> <li>• Relations between internal and external descriptions</li> <li>• Response of continuous- and discrete-time systems</li> <li>• Stability, controllability, observability</li> <li>• State-space realizations of external descriptions</li> <li>• Feedback systems</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending this course, students will be familiar with the most important concepts and results in linear system theory. Students will develop confidence in their ability to solve mathematical problems of analysis and design. Many of their timeless insights and intuitions about the dynamical behavior of systems will be drawn from this course. This course presents material broad enough so that students will have a clear understanding of the dynamical behavior of linear systems, including their power and limitations. This will allow students to apply the theory to other fields.</p>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Erdal Kayacan</p>								
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced System Theory:</i></p> <p><b>Course Homepage</b>  <a href="https://en.ei.uni-paderborn.de/rat">https://en.ei.uni-paderborn.de/rat</a></p> <p><b>Implementation</b>  Lectures and exercises (including some computer simulations) Panda course for communication and material distribution</p> <p><b>Teaching Material, Literature</b>  Handouts and exercise / tutorial questions; literature references will be given in the first lecture</p>								

#### 4 General Elective Area

Gekoppelte Felder							
Coupled Fields							
<b>Module number:</b> M.048.27028	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.048.27028 Coupled Fields	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Gekoppelte Felder:</i> <b>Recommended:</b> Basic knowledge from the area of classical field theory, for example from the modules "Field Theory", "Electromagnetic Waves" and "Theoretical Electrical Engineering".						
4	<b>Contents:</b>  <i>Contents of the course Gekoppelte Felder:</i> The focus of the course Coupled Fields is the classical field theory of interacting electromagnetic, thermal and mechanical phenomena as well as their application in sensors and actuators. After an introduction to the mathematical description of the individual fields, the following topics are covered: <ul style="list-style-type: none"> <li>• Electromechanical coupling based on examples in piezoelectricity, electrostriction and magnetostriction.</li> <li>• Thermomechanical coupling such as thermoelasticity and lossy acoustic waves.</li> <li>• Thermoelectric coupling, for example pyroelectricity.</li> <li>• Phenomena with electromagnetic-thermal-mechanical coupling such as the photoacoustic effect. In addition to the description of the effects, analogies as well as similarities and differences are considered and aspects of numerical simulation are discussed.</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending the course, students will be able to</p> <ul style="list-style-type: none"> <li>• describe the discussed physical effects phenomenologically and with differential equations.</li> <li>• interpret the results of numerical simulations of coupled fields and check them for plausibility.</li> <li>• select suitable components for sensor and actuator applications of coupled fields.</li> <li>• infer an acting physical effect from observations.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table> <p>Within the first three weeks of the lecture period each respective lecturer will specify the manner in which the examination will be conducted.</p>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Leander Claes</p>								

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><b>Module Homepage</b> <a href="https://emt.upb.de">https://emt.upb.de</a></p> <p><b>Implementation</b> Lectures and exercises (including some computer simulations)</p> <p><b>Teaching Material, Literature</b> Lecture slides and exercises will be provided. Additional literature references will be given throughout the course.</p>
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4 General Elective Area

<b>Regelung elektrischer Antriebe</b>						
Control of Electric Drives						
<b>Module number:</b> M.048.27013	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.048.27013 Control of Electric Drives	2L 2Ex, SS	60	120	C	40/40
2	<b>Options within the module:</b> None					
3	<b>Admission requirements:</b> None  <i>Prerequisites of course Regelung elektrischer Antriebe:</i> <b>Recommended:</b> <ul style="list-style-type: none"> <li>• basics of control theory</li> <li>• basics of electrical machines</li> <li>• basics of power electronics</li> </ul>					
4	<b>Contents:</b> <i>Contents of the course Regelung elektrischer Antriebe:</i> <b>Short Description</b> The course teaches the closed-loop control for electrical machines (DC machines, synchronous machines, and induction machines). The presented content is tested in simulations within the exercises. <b>Contents</b> <ul style="list-style-type: none"> <li>• machine models (dc machines, synchronous machines, and induction machines)</li> <li>• coordinate transformations</li> <li>• field-oriented control for particular machines</li> <li>• observers</li> <li>• advanced control techniques (direct torque control, model-predictive control)</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After successfully completing the module, students will be able to</p> <ul style="list-style-type: none"> <li>• explain the functionality of electric drives (based on dc machines, synchronous machines, and induction machines)</li> <li>• set up a simulation model for the target application</li> <li>• design a suitable control system for the particular electric machines and implement it as a simulation model</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Jakub Kucka</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Regelung elektrischer Antriebe:</i></p> <p><b>Implementation</b> Parts of the course are organized as computer-based simulation exercises.</p> <p><b>Teaching Material</b> Lecture slides.</p>										

4 General Elective Area

Technische Akustik							
Technical Acoustics							
<b>Module number:</b> M.048.27022	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.048.27022 Technical Acoustics	2L 2Ex, WS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Technische Akustik:</i> None						
4	<b>Contents:</b> <i>Contents of the course Technische Akustik:</i> <b>Short Description</b> The course Technical Acoustics concentrates on teaching the basics of acoustics with a focus on modelling and simulation of sound propagation. <b>Contents:</b> The lecture Technical Acoustics is structured as follows <ul style="list-style-type: none"> <li>• Acoustic and sound field characteristics</li> <li>• Fundamentals of wave propagation</li> <li>• Hearing acoustics</li> <li>• Wave equations</li> <li>• Modelling</li> <li>• Electro-acoustic as well as acoustic-electric couplings</li> <li>• Material data</li> <li>• Technical sound sources (properties)</li> <li>• Sound field visualisation (for verification)</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Domain competence:</b> After attending the course, students are able to</p> <ul style="list-style-type: none"> <li>• describe sound propagation processes in solids, liquids and gases mathematically and analyse them by means of analytical or numerical simulation.</li> </ul> <p><b>Key qualifications:</b> The Students</p> <ul style="list-style-type: none"> <li>• can apply the acquired knowledge and skills in an interdisciplinary manner and with complex issues,</li> <li>• are able to develop targeted solutions based on systematic problem analysis,</li> <li>• are capable of familiarising themselves with relevant fields of work due to the method-oriented knowledge transfer.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bernd Henning</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Technische Akustik:</i></p> <p><b>Course Homepage</b> <a href="http://emt.upb.de">http://emt.upb.de</a></p> <p><b>Implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with slide presentation of extensive contexts,</li><li>• Practical work in groups using measurement techniques in the laboratory</li></ul> <p><b>Teaching Material, Literature</b></p> <ul style="list-style-type: none"><li>• Provision of a script; references to textbooks from the textbook collection will be announced.</li></ul>
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4 General Elective Area

<b>Ultraschallmesstechnik</b>							
Ultrasonic measurement technology							
<b>Module number:</b> M.048.27015	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> de	<b>Semester number:</b> 1.-3. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.048.27015 Ultrasonic Measurement Technology	2L 2Ex, SS	60	120	C	40/40	
2	<b>Options within the module:</b> None						
3	<b>Admission requirements:</b> None <i>Prerequisites of course Ultraschallmesstechnik:</i> None						
4	<b>Contents:</b> <i>Contents of the course Ultraschallmesstechnik:</i> <b>Short description</b> The course Ultrasonic Measurement Technology deals with the phenomena of propagation of mechanical waves in solids, liquids and gases. Based on this the most important acoustic measurement principles for the determination of acoustic material parameters, geometric and technical process parameters as well as their application in process and production engineering are described. The application of sound and ultrasound for non-destructive material diagnostics as well as for ultrasonic tomography are covered in detail. <b>Contents</b> The Ultrasonic Metrology lecture covers the following topics: <ul style="list-style-type: none"> <li>• Acoustic and sound field characteristics.</li> <li>• Fundamentals of wave propagation</li> <li>• Ultrasonic sensor design (experimental realization)</li> <li>• Methods for measurement and visualization of ultrasonic fields (needle and membrane hydrophone, schlieren measuring station, laser vibrometry. . .)</li> <li>• Metrological methods for acoustic material data determination (sound velocity, sound characteristic impedance. . .)</li> <li>• Application of ultrasound for non-destructive testing (NDT) and acoustic emission analysis</li> <li>• Application of ultrasound and in process measurement technology (distance, flow, level. . .)</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p><b>Specialized competence:</b> After attending the course, students will be able to,</p> <ul style="list-style-type: none"> <li>• use ultrasound to determine acoustic and non-acoustic quantities.</li> </ul> <p><b>Cross-disciplinary competencies:</b> The students</p> <ul style="list-style-type: none"> <li>• are able to apply the knowledge and skills across disciplines and to complex problems,</li> <li>• are able to develop targeted solutions on the basis of systematic problem analysis,</li> <li>• are able to familiarize themselves with tangential fields of work due to the method-oriented knowledge transfer.</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or Oral Examination or Presentation</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%
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a)	Written or Oral Examination or Presentation	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination (MAP) was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>BF Automatisierungstechnik Lehramt BK affine Fächer Master v5, Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Elektrotechnik v4 (EMA v4), Masterstudiengang Elektrotechnik v5 (EMA v5), Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik, Masterstudiengang Wirtschaftsingenieurwesen Studienrichtung Elektrotechnik V4</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Bernd Henning</p>								

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Ultraschallmesstechnik:</i></p> <p><b>Course Homepage</b> <a href="http://emt.upb.de">http://emt.upb.de</a></p> <p><b>Methodical implementation</b></p> <ul style="list-style-type: none"><li>• Lectures with slide presentation of extensive correlations</li><li>• Practical work in groups using measurement techniques in the laboratory</li></ul> <p><b>Learning materials, references</b></p> <ul style="list-style-type: none"><li>• Provision of a script; references to textbooks from the textbook collection will be announced.</li></ul>
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## 4.7 Computer Science Focus Area Classical and Quantum Algorithm Design

Advanced Algorithms							
Advanced Algorithms							
<b>Module number:</b> M.079.4002	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05801 Advanced Algorithms	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Advanced Algorithms:</i> <b>Recommended Proficiencies</b> Willingness and ability to learn the creative process of algorithm design and efficiency analysis using mathematical methods. Basic Knowledge of some basic algorithms and data structures and their analyses is assumed.						
4	<b>Contents:</b>  <i>Contents of the course Advanced Algorithms:</i> This course presents advanced algorithms and algorithmic paradigms for fundamental problems. More precisely, methods like randomization and derandomization as well as concepts for approximation and online algorithms will be presented by illustrating their usefulness for important algorithmic problems. In all cases, the correctness and runtime will be rigorously analyzed. <ul style="list-style-type: none"> <li>• Randomized algorithms and derandomization, for example, randomized rounding</li> <li>• Online algorithms, for example, scheduling algorithms</li> <li>• Approximation algorithms, for example, NP-hard problems</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply basic analytical techniques,</li> <li>• explain and apply basic algorithmic approaches,</li> <li>• judge which effects these approaches have, and</li> <li>• know the limits of using these approaches.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Assignments, course paper or progress reports		CA								
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9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Christian Scheideler</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Algorithms:</i></p> <p><b>Implementation Method</b></p> <p>The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Slides of the lecture; exercise sheets</li> <li>• Additional literature will be announced in the course</li> </ul>										

4 General Elective Area

Advanced Distributed Algorithms and Data Structures						
Advanced Distributed Algorithms and Data Structures						
<b>Module number:</b> M.079.4006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05700 Advanced Distributed Algorithms and Data Structures	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Advanced Distributed Algorithms and Data Structures:</i> <b>Recommended Proficiencies</b> Algorithms and data structures, distributed algorithms and data structures					
4	<b>Contents:</b>  <i>Contents of the course Advanced Distributed Algorithms and Data Structures:</i> After a short introduction of the foundations of graph and network theory as well as distributed programs, the lecture presents advanced methods in the area of distributed algorithms and data structures. Topics covered in the course are access control, synchronization, consensus, information dissemination, hybrid networks, scheduling, and optimization. In addition to presenting solutions to these topics, also concrete applications will be presented. The lecture gives an introduction to state-of-the-art advanced distributed algorithms and data structures. In addition to the presentation of the corresponding protocols, their correctness and efficiency will be shown in a rigorous way. The lecture is structured as follows:  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations of graph and network theory</li> <li>• Access control</li> <li>• Synchronization</li> <li>• Consensus</li> <li>• Information dissemination</li> <li>• Hybrid networks</li> <li>• Scheduling</li> <li>• Optimization</li> </ul> In addition to presenting solution to these topics, also concrete applications will be presented.					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply basic analytical techniques,</li> <li>• explain and use basic algorithmic approaches,</li> <li>• judge which effects these approaches have, and</li> <li>• know the limits of using these approaches.</li> </ul>										
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Christian Scheideler</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Distributed Algorithms and Data Structures:</i></p> <p><b>Implementation Method</b></p> <p>The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Slides of the lecture; exercise sheets</li> <li>• Additional literature will be announced in the course</li> </ul>										

4 General Elective Area

<b>Algorithms for Highly Complex Virtual Scenes</b>							
Algorithms for Highly Complex Virtual Scenes							
<b>Module number:</b> M.079.4009	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b>	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05756 Algorithms for Highly Complex Virtual Scenes	L3 Ex2	75	105	CE	40/20	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> <i>Prerequisites of course Algorithms for Highly Complex Virtual Scenes:</i> <b>Recommended Proficiencies</b> Willingness and ability to learn the creative process of algorithm design and efficiency analysis using mathematical methods. Basic Knowledge of some basic algorithms and data structures and their analyses is assumed.						
4	<b>Contents:</b> <i>Contents of the course Algorithms for Highly Complex Virtual Scenes:</i> Walkthrough systems allow viewing and walking through a virtual 3D scene and finds application in architecture programs, simulations or games. The efficiency of real-time rendering algorithms is crucial for a smooth and fast visualization of the virtual 3D scene in a walkthrough system. There are different algorithmic approaches to reduce highly complex 3D geometric data and to achieve a rendering of the scene in real time. The lecture introduces algorithmic approaches in the areas of visibility culling, simplification, level of detail, image-based rendering and further approaches. <ul style="list-style-type: none"> <li>• Introduction: walkthrough problem</li> <li>• Data structures: kd-tree, BSP-tree, octree, loose octree</li> <li>• Level of detail: adaptive LOD management, mesh simplification, progressive meshes</li> <li>• Visibility culling: view frustum culling, potentially visible sets (PVS), dynamic analysis of PVS, Hierarchical z-buffer, hierarchical occlusion maps, aspect graph, visibility space partition</li> <li>• Replacement: color cubes, randomized z-buffer, hierarchical image caching</li> <li>• Parallel rendering: classification and modeling, parallel rendering as a sorting problem, hybrid sort-first/sort-last rendering</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students can apply fundamental techniques in the area of real time rendering of virtual 3D scenes. They can decide in which virtual 3D scene which algorithm is most appropriate. They can adapt algorithms to a new situation.</p> <p><b>Non-cognitive Skills</b></p> <ul style="list-style-type: none"> <li>• Attitude</li> <li>• Self-monitoring</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination</td> <td style="text-align: center;">90-120 min or 40 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table> <p>The responsible lecturer announces type and duration of assessment modalities in the first three weeks of the lecture period at latest.</p>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination	90-120 min or 40 min	100%
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zu	Type of achievement	Duration or Scope	SL / QT						
a)	Written exercises		CA						
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>								
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>keine</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr. Matthias Fischer</p>								

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Algorithms for Highly Complex Virtual Scenes:</i></p> <p><b>Implementation method</b></p> <ul style="list-style-type: none"><li>• Lecture with beamer and blackboard</li><li>• Practice in small groups</li><li>• Expected activities of the students: Collaboration in presence exercises Homework</li><li>• exercise sheets, sample solutions are presented in central exercises</li><li>• In exercises and homework sheets and teh analysis of algorithms of selected examples are practiced.</li></ul> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Standard textbooks, slides of the lecture, exercise sheets</li><li>• Real-Time Rendering; Tomas Akenine-Möller, Eric Haines; AK Peters, 2002.</li><li>• Level of Detail for 3D Graphics; David Luebke, Martin Reddy, Jonathan D. Cohen; Morgan Kaufmann Publishers, 2002.</li></ul>
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4 General Elective Area

Foundations of Cryptography							
Foundations of Cryptography							
<b>Module number:</b> M.079.4020	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05801 Foundations of Cryptography	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Foundations of Cryptography:</i> <b>Recommended Proficiencies</b> Basic Knowledge in IT-Security and cryptography useful but not necessary, basic concepts of complexity theory and probability theory						
4	<b>Contents:</b>  <i>Contents of the course Foundations of Cryptography:</i> Cryptography is an important basic technique in IT security. Internet protocols such as TLS are based on cryptographic primitives such as key exchange, encryption and signatures. In this lecture, important basic concepts of modern cryptography will be introduced. These include encryption schemes, digital signatures, identification protocols, and multiparty computations. In all cases, formal security definitions are presented and, starting from mathematically precise assumptions, provably secure constructions are developed. An essential aspect of the lecture is the construction of efficient and secure cryptographic methods from assumptions that are as general as possible. Contents include:  <ul style="list-style-type: none"> <li>• Symmetric and asymmetric encryption.</li> <li>• Pseudorandom functions, one-way functions, permutations with trapdoors</li> <li>• Hash functions and authentication codes</li> <li>• Digital signatures, one-time signatures and random oracles.</li> <li>• Identification protocols, <math>\Sigma</math> protocols.</li> <li>• Security concepts such as unforgeable signatures and CPA- and CCA-secure encryption schemes.</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of the module, students will be able to:</p> <ul style="list-style-type: none"> <li>• understand, explain and apply concepts and methods of modern cryptography.</li> <li>• select appropriate cryptographic methods according to the security requirements of an application, e.g. distinguish where encryption methods and where authentication methods are appropriate.</li> <li>• combine primitives of cryptography according to application requirements and prove the security of the combination.</li> <li>• define new security concepts and design cryptographic methods that satisfy those concepts.</li> <li>• understand and independently develop security proofs.</li> <li>• acquire latest research results in the field of cryptography by reading scientific papers.</li> </ul>										
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)                <input type="checkbox"/>Module exam (MP)                <input type="checkbox"/>Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Johannes Blömer</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Foundations of Cryptography:</i></p> <p><b>Implementation method</b></p> <p>Basic concepts are presented in a lecture. In addition, theoretical concepts are deepened in tutorials in small groups. Written exercises and reading groups will be used to practice the practical application of these concepts.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Oded Goldreich, Foundations of Cryptography I,II,</li><li>• Jonathan Katz, Yehuda Lindell, Introduction to Modern Cryptography</li><li>• Slides from the lectures</li><li>• Lecture notes</li></ul>
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4 General Elective Area

Foundational Methods for Knowledge Representation and Reasoning							
Foundational Methods for Knowledge Representation and Reasoning							
<b>Module number:</b> M.079.4210	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05811 Parameterized Algorithms and Complexity	L3 Ex2	75	105	C	35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Parameterized Algorithms and Complexity:</i> <b>Recommended Proficiencies</b> Basic knowledge of algorithms and their analysis, complexity theory (complexity classes, Turing machines, NP-completeness) as well as first-order logic						
4	<b>Contents:</b>  <i>Contents of the course Parameterized Algorithms and Complexity:</i> The course gives an introduction to parameterized complexity theory. It <ul style="list-style-type: none"> <li>• gives an introduction to the foundational ideas of parameterized complexity</li> <li>• covers several algorithmic techniques that help constructing efficient algorithms (with respect to parameterized complexity), such as kernelization and bounded search trees,</li> <li>• covers the central concepts and tools of the hardness theory in parameterized complexity, in particular fpt-reductions as well as the W-hierarchy and A-hierarchy, and</li> <li>• gives a brief introduction to fine-grained complexity.</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• use the technical notions covered in the lecture competently and explain these notions</li> <li>• reproduce and explain the proofs and proof techniques covered in the lecture, including algorithmic techniques</li> <li>• in particular, reproduce and explain proofs of correctness and running time of the covered algorithms</li> <li>• apply constructive proofs and formal methods from the lecture to specific instances</li> <li>• apply the proof techniques and algorithmic techniques on new, similar problems</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or presentation</td> <td>90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or presentation	90-120 min or 30-45 min or 30 min	100%
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9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Anni-Yasmin Turhan</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Parameterized Algorithms and Complexity:</i></p> <p><b>Implementation Method</b></p> <p>Content is presented via beamer presentation or on white board. Exercises provide opportunities to deepen the understanding of the content and train the competences obtained in the course. The lecture in most part follows select chapters of the following literature:</p> <ul style="list-style-type: none"><li>• <i>Parameterized Algorithms</i> by M. Cygan, F. V. Fomin, L. Kowalik, D. Lokshtanov, D. Marx, M. Pilipczuk, M. Pilipczuk, S. Saurabh (Springer 2015).</li><li>• <i>Parameterized Complexity Theory</i> by J. Flum, M. Grohe (Springer 2006).</li><li>• Additional literature might be announced in the course.</li></ul>
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4 General Elective Area

Introduction to Quantum Computation							
Introduction to Quantum Computation							
<b>Module number:</b> M.079.4059	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05706 Introduction to Quantum Computation	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Introduction to Quantum Computation:</i> <b>Recommended Proficiencies</b> Linear Algebra, algorithms						
4	<b>Contents:</b>  <i>Contents of the course Introduction to Quantum Computation:</i> This lecture introduces the fundamental concepts of quantum computation and information from a computer science perspective. This includes an introduction to quantum mechanics, quantum entanglement, quantum algorithms, quantum error correction, and quantum information theory.  <ul style="list-style-type: none"> <li>• Quantum mechanics</li> <li>• Quantum entanglement</li> <li>• Quantum algorithms</li> <li>• Quantum error correction</li> <li>• Quantum information</li> </ul>						
5	<b>Learning outcomes and competences:</b> Students are able to: <ul style="list-style-type: none"> <li>• Describe and apply the postulates of quantum mechanics</li> <li>• Understand the use of entanglement as a resource</li> <li>• Design and analyze fundamental quantum algorithms</li> <li>• Apply the theory of error-correcting codes</li> <li>• Understand and apply basic quantum information theory concepts such as entropy</li> </ul>						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>	
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%	
7	<b>Study Achievement:</b>			
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>	
a)	Assignments, course paper or progress reports		CA	
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr. Sevag Gharibian			
13	<b>Other Notes:</b> <i>Remarks of course Introduction to Quantum Computation:</i> <b>Implementation method</b> Slides and blackboard writing. All important concepts and techniques are further deepened with examples in exercises. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press</li> <li>• Lecture slides, exercises</li> </ul>			

4 General Elective Area

Post-Quantum Cryptography							
Post-Quantum Cryptography							
<b>Module number:</b> M.079.4089	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05732 Post-Quantum Cryptography	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Post-Quantum Cryptography:</i> <b>Recommended Proficiencies</b> Basics of cryptography and complexity theory						
4	<b>Contents:</b>  <i>Contents of the course Post-Quantum Cryptography:</i> IT security is largely based on modern cryptographic methods. These include many methods of so-called public-key cryptography such as the RSA and Elgamal encryption methods, the RSA signature method, and the various variants of the Digital Signature Algorithm (DSA). In 1994, Peter Shor presented an efficient algorithm for computing prime factorization of integers and for computing discrete logarithms in finite groups. Thus, all the aforementioned methods of public-key cryptography are insecure if quantum computers of sufficient size and complexity can be realized. It is therefore important to develop alternatives to classical public-key methods that, at least according to current research, cannot be broken by quantum computers. Important candidates (and some close to standardization) for such post-quantum secure methods rely on techniques of error-correcting codes and the geometry of numbers. In this lecture, we will present and discuss important candidates for post-quantum secure methods. The course includes the following contents:  <ul style="list-style-type: none"> <li>• introduction to codes, lattices and discretised Gaussian distributions</li> <li>• lattice and code based encryption</li> <li>• lattice based signatures</li> <li>• lattices and zero-knowledge proofs</li> <li>• lattice based group signatures</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and explain the difference between classical and post-quantum security.</li> <li>• explain the importance of post-quantum cryptography for selected applications.</li> <li>• explain and apply concepts from the field of geometry of numbers and error-correcting codes.</li> <li>• explain important constructions from post-quantum cryptography and prove their security.</li> <li>• explain security assumptions from post-quantum cryptography and apply them to new post-quantum primitives.</li> </ul>										
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12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Johannes Blömer</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Post-Quantum Cryptography:</i></p> <p><b>Implementation Method</b> Basic concepts are presented in a lecture. In addition, theoretical concepts are deepened in tutorials in small groups as well as in written exercises.</p> <p><b>Learning Material, Literature</b> References to current learning materials will be given in the lectures.</p>										

4 General Elective Area

Quantum Algorithms							
Quantum Algorithms							
<b>Module number:</b> M.079.4072	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05830 Quantum Algorithms	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none <i>Prerequisites of course Quantum Algorithms:</i> <b>Recommended Proficiencies</b> Linear Algebra, Quantum Computing						
4	<b>Contents:</b> <i>Contents of the course Quantum Algorithms:</i> This lecture covers quantum algorithms from a computer science perspective. Topics include quantum circuits (e.g. Solovay-Kitaev theorem), quantum algorithms for algebraic problems (e.g. Hidden Subgroup problem), quantum walks, quantum query complexity, and adiabatic quantum computing. <ul style="list-style-type: none"><li>• Quantum circuits</li><li>• Algebraic problems</li><li>• Quantum walks</li><li>• Query complexity</li><li>• Adiabatic computation</li></ul>						
5	<b>Learning outcomes and competences:</b> Students are able to: <ul style="list-style-type: none"><li>• Describe universal gate sets</li><li>• Develop Quantum Fourier-Transform based algorithms</li><li>• Develop quantum walk-based algorithms</li><li>• Apply the quantum adiabatic theorem</li><li>• Give quantum query lower bounds</li></ul>						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>	
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%	
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a)	Assignments, course paper or progress reports		CA	
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11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr. Sevag Gharibian			
13	<b>Other Notes:</b> <i>Remarks of course Quantum Algorithms:</i> <b>Implementation method</b> Slides and blackboard writing. All important concepts and techniques are further deepened with examples in exercises. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press</li> <li>• Andrew M. Childs, Wim van Dam, Quantum algorithms for algebraic problems, Reviews of Modern Physics, volume 82, 2010</li> <li>• Lecture slides, exercises</li> </ul>			

#### 4 General Elective Area

Quantum Complexity Theory							
Quantum Complexity Theory							
<b>Module number:</b> M.079.4063	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05811 Quantum Complexity Theory	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Quantum Complexity Theory:</i> <b>Recommended Proficiencies</b> Linear Algebra, Quantum Computing						
4	<b>Contents:</b>  <i>Contents of the course Quantum Complexity Theory:</i> This lecture provides a brief review of introductory quantum computation, and subsequently moves into quantum complexity theory. Beginning to advanced topics will be covered, including quantum analogues of P and NP (denoted BQP, QCMA, and QMA), quantum satisfiability problems, quantum interactive proofs, and tensor networks. Along the way, semidefinite programming will be introduced as an important tool.  <ul style="list-style-type: none"> <li>• Complexity classes BQP, QCMA, QMA</li> <li>• Quantum algorithms for linear system solving</li> <li>• Quantum Satisfiability Problems</li> <li>• Quantum Interactive Proofs</li> <li>• Semidefinite Programming</li> </ul>						
5	<b>Learning outcomes and competences:</b>  Students will be able to  <ul style="list-style-type: none"> <li>• Distinguish language classes from promise classes</li> <li>• Define fundamental quantum complexity classes, such as BQP and QMA</li> <li>• Prove BQP-hardness results via polynomial-time reductions</li> <li>• Prove QMA-hardness results via polynomial-time reductions</li> <li>• Apply semidefinite programming to analyze quantum interactive proofs</li> </ul>						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
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11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Sevag Gharibian		
13	<b>Other Notes:</b> <i>Remarks of course Quantum Complexity Theory:</i> <b>Implementation method</b> Slides and blackboard writing. All important concepts and techniques are further deepened with examples in exercises. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press</li> <li>• S. Gharibian, Y. Huang, Z. Landau, S. W. Shin, Quantum Hamiltonian Complexity, Foundations and Trends in Theoretical Computer Science</li> <li>• Lecture slides, assignments</li> </ul>		

#### 4 General Elective Area

Quantum Information							
Quantum Information							
<b>Module number:</b> M.079.4090	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05805 Quantum Information	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Quantum Information:</i> <b>Recommended Proficiencies</b> Linear Algebra						
4	<b>Contents:</b>  <i>Contents of the course Quantum Information:</i> Over the last century, Quantum mechanics has had profound impacts on both fundamental science and technology. The emerging field of Quantum Information Theory studies a paradigm for information processing empowered by quantum mechanics. This field has demonstrated that quantum information processing can outperform its classical counterpart and is a revolutionary direction to investigate future information technologies. Quantum Information Science incorporates techniques from computer science, mathematics, and physics. Of particular interest is quantum entanglement, which is the phenomenon that occurs when a group of particles is generated or interacts in a way such that the state of each particle cannot be described independently of the others, even when the particles are separated by arbitrarily large distances. Entanglement is a primary feature of quantum mechanics not present in classical physics and it is a resource behind most modern quantum technologies, such as quantum computers. This lecture introduces the advance concepts of quantum communication and information. The contents include:  <ul style="list-style-type: none"> <li>• Entanglement of two- and many-body systems</li> <li>• Quantum information processing and applications</li> <li>• Measures of Entanglement, Distance and Fidelity</li> <li>• Higher local dimensions (qubits vs qudits)</li> <li>• Quantum channels</li> <li>• Classical and quantum error correcting codes and their differences</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students learn cutting-edge concepts at the intersection of computer science and quantum mechanics. This lecture equips students with advanced, interdisciplinary technical proficiency, enabling them to pursue careers in analysis intensive industries, technology start-ups, or research and development roles in leading technology companies or academia. To achieve this, the students get familiar with the basics of quantum mechanics and the related algebra. Furthermore, they will be able to:</p> <ul style="list-style-type: none"> <li>• understand the underlying concepts of entangled systems (two-body and many-body),</li> <li>• understand the fundamental idea of maximally entangled systems, classify and characterise them for practical applications,</li> <li>• describe the basic notion of higher local dimension particles (qubits vs qudits),</li> <li>• apply the theory of classical and quantum error correcting codes, and study their differences,</li> <li>• to work on interdisciplinary topics and, in particular, to acquire the basics of different disciplines.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">ZU</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			ZU	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">ZU</th> <th style="width: 45%;">Type of achievement</th> <th style="width: 20%;">Duration or Scope</th> <th style="width: 25%;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			ZU	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr. Zahra Raissi</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Quantum Information:</i></p> <p><b>Implementation Method</b> Theoretical foundations and concepts will be taught in the form of lectures and deepened in practical exercise courses, group work as well as individual assignments.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press, 2000.</li><li>• F. J. MacWilliams and N. J. A. Sloane. The Theory of Error-Correcting Codes, North-Holland Mathematical Library. North-Holland, Amsterdam, 1977. ISBN 9780444851932.</li><li>• Ingemar Bengtsson and Karol Zyczkowski, Geometry of quantum states: an introduction to quantum entanglement, Cambridge university press, 2006, ISBN 9780511535048.</li><li>• Lecture slides</li><li>• Exercises</li></ul>
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## 4.8 Computer Science Focus Area Computer and Communication Systems

Advanced Distributed Algorithms and Data Structures							
Advanced Distributed Algorithms and Data Structures							
<b>Module number:</b> M.079.4006	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05700 Advanced Distributed Algorithms and Data Structures	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Advanced Distributed Algorithms and Data Structures:</i> <b>Recommended Proficiencies</b> Algorithms and data structures, distributed algorithms and data structures						

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Advanced Distributed Algorithms and Data Structures:</i>            After a short introduction of the foundations of graph and network theory as well as distributed programs, the lecture presents advanced methods in the area of distributed algorithms and data structures. Topics covered in the course are access control, synchronization, consensus, information dissemination, hybrid networks, scheduling, and optimization. In addition to presenting solutions to these topics, also concrete applications will be presented.            The lecture gives an introduction to state-of-the-art advanced distributed algorithms and data structures. In addition to the presentation of the corresponding protocols, their correctness and efficiency will be shown in a rigorous way. The lecture is structured as follows:</p> <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations of graph and network theory</li> <li>• Access control</li> <li>• Synchronization</li> <li>• Consensus</li> <li>• Information dissemination</li> <li>• Hybrid networks</li> <li>• Scheduling</li> <li>• Optimization</li> </ul> <p>In addition to presenting solution to these topics, also concrete applications will be presented.</p>										
5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply basic analytical techniques,</li> <li>• explain and use basic algorithmic approaches,</li> <li>• judge which effects these approaches have, and</li> <li>• know the limits of using these approaches.</li> </ul>										
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">ZU</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			ZU	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%								
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ZU	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										

#### 4 General Elective Area

9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4
12	<b>Module coordinator:</b> Prof. Dr. Christian Scheideler
13	<b>Other Notes:</b> <i>Remarks of course Advanced Distributed Algorithms and Data Structures:</i> <b>Implementation Method</b> The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors. <b>Learning Material, Literature</b> <ul style="list-style-type: none"><li>• Slides of the lecture; exercise sheets</li><li>• Additional literature will be announced in the course</li></ul>

#### 4 General Elective Area

<b>Approximate Computing</b>						
Approximate Computing						
<b>Module number:</b> M.079.4068	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.09758 Approximate Computing	L3 Ex2	75	105	C	100/30
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Approximate Computing:</i> <b>Recommended Proficiencies</b> Knowledge of the Bachelor-level courses Digital Design and Computer Architecture are beneficial.					
4	<b>Contents:</b>  <i>Contents of the course Approximate Computing:</i> Approximate Computing is an emerging paradigm that trades-off computational accuracy for a significant reduction in energy, execution time, or chip area. This research-oriented course introduces to the field of Approximate Computing and its most remarkable aspects, and explains the main methods used to implement efficient computing systems by reducing accuracy. The course discusses approximations at all levels of a computing system, from applications down to hardware technologies. In exercise/tutorial sessions the efficiency of these techniques in various domains are examined, including deep learning and digital signal processing.  <ul style="list-style-type: none"> <li>• Introduction and motivation for approximate computing</li> <li>• Approximation at the application level, e.g., in machine learning and digital signal processing</li> <li>• Programming languages/compilers for approximate computing</li> <li>• Approximate microarchitectures</li> <li>• Automated synthesis of approximate circuits</li> <li>• Inexact arithmetic components and performance optimization via accuracy trade-offs</li> <li>• Approximation techniques at the technology level</li> <li>• Exercises/tutorial: Approximating deep learning and digital signal processing algorithms</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After attending this course, the students are able</p> <ul style="list-style-type: none"> <li>• to name and explain approximation techniques at all levels of a computing system,</li> <li>• to identify major engineering/research problems when building approximate computing systems,</li> <li>• to judge the suitability of approximation techniques for different application domains, and</li> <li>• to apply approximation techniques to realize efficient hardware accelerators, in particular for deep learning and digital signal processing</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of achievement</th> <th style="text-align: center;">Duration or Scope</th> <th style="text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Marco Platzner</p>										

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Approximate Computing:</i></p> <p><b>Implementation method</b></p> <ul style="list-style-type: none"><li>• Lecture with projector and black/white board</li><li>• Interactive exercises/discussions in the lecture room</li><li>• Computer-based tutorials</li></ul> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides, exercise sheets, and tutorial assignments</li><li>• Adrian Sampson, Luis Ceze, and Dan Grossman: Good-Enough Computing. <i>IEEE Spectrum</i>, 50(10):54-59, 2013</li><li>• Ravi Nair. Big Data Needs Approximate Computing: Technical Perspective. <i>Communications of the ACM</i>, 58(1): 104, 2015.</li><li>• Sparsh Mittal. A Survey of Techniques for Approximate Computing. <i>ACM Computing Surveys</i>, 48(4), 2016.</li><li>• Qiang Xu, Todd Mytkowitz, and Nam Sung Kim. Approximate Computing: A Survey. <i>IEEE Design &amp; Test</i>, 33(1):8-22, 2016.</li><li>• Weiqiang Liu and Fabrizio Lombardi (Editors), <i>Approximate Computing</i>. Springer, 2022.</li><li>• Additional resources and links to current research papers are provided in the lecture.</li></ul>
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#### 4 General Elective Area

<b>Reconfigurable Computing</b>							
Reconfigurable Computing							
<b>Module number:</b> M.079.4043	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	a)	L.079.05703 Reconfigurable Computing	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Reconfigurable Computing:</i> <b>Recommended Proficiencies</b> Knowledge of the Bachelor-level courses Digital Design, Programming, and Data Structures and Algorithms are beneficial.						
4	<b>Contents:</b>  <i>Contents of the course Reconfigurable Computing:</i> The course Reconfigurable Computing introduces into the field of computing with reprogrammable hardware structures. Computing systems built from reprogrammable hardware structures do not rely on a fixed hardware, but adapt their hardware architecture to the application under execution. The field was formed in the early 1990s when Field-programmable Gate Arrays (FPGAs) became commercially available that were powerful enough to be used for computing. Today, FPGA-based high-performance systems have outperformed state-of-the-art computers for many problems including database search, genomic sequence scanning, and cryptography. In embedded systems, FPGAs accelerate system functions, reduce system cost and energy consumption, and enable hardware-on-demand functionality. The course covers the following topics:  <ul style="list-style-type: none"> <li>• Introduction to reconfigurable computing</li> <li>• Evolution of programmable hardware devices</li> <li>• FPGA architectures</li> <li>• Computer-aided design for FPGAs</li> <li>• High-level languages for programming FPGAs</li> <li>• Application domains for FPGAs</li> <li>• Comparison of devices, technologies, and reconfigurable systems</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• compare different reprogrammable hardware devices and describe their historical development,</li> <li>• name the design steps and problems when designing with FPGAs,</li> <li>• analyse algorithms for the design steps and apply them to examples,</li> <li>• compare and evaluate current approaches to programming FPGAs,</li> <li>• justify the suitability of different reprogrammable hardware components for different areas of application, and</li> <li>• implement functions of medium complexity with modern FPGA design tools.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 25%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Marco Platzner</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Reconfigurable Computing:</i></p> <p><b>Implementation Method</b></p> <p>The course consists of a lecture, and pencil&amp;paper as well as practical exercises. The lecture is held with a beamer and blackboard. In the pencil&amp;paper exercises, problems are handed out and their solutions are presented and discussed in a practice session. In addition, quizzes are offered for self-assessments. In the practical exercises, a tutorial on the design with FPGAs is carried out and then tasks are handed out, which are implemented as design or programming examples in groups of one to three participants.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lecture slides, assignment sheets for paper&amp;pencil exercises, quizzes</li><li>• Tutorial, assignment sheets for design and programming examples, technical documentation</li><li>• Selected scientific articles</li><li>• Additional literature will be announced in the course.</li></ul>
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## 4.9 Computer Science Focus Area Data Science and Intelligent Systems

Advanced Algorithms						
Advanced Algorithms						
<b>Module number:</b> M.079.4002	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05801 Advanced Algorithms	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none <i>Prerequisites of course Advanced Algorithms:</i> <b>Recommended Proficiencies</b> Willingness and ability to learn the creative process of algorithm design and efficiency analysis using mathematical methods. Basic Knowledge of some basic algorithms and data structures and their analyses is assumed.					
4	<b>Contents:</b> <i>Contents of the course Advanced Algorithms:</i> This course presents advanced algorithms and algorithmic paradigms for fundamental problems. More precisely, methods like randomization and derandomization as well as concepts for approximation and online algorithms will be presented by illustrating their usefulness for important algorithmic problems. In all cases, the correctness and runtime will be rigorously analyzed. <ul style="list-style-type: none"><li>• Randomized algorithms and derandomization, for example, randomized rounding</li><li>• Online algorithms, for example, scheduling algorithms</li><li>• Approximation algorithms, for example, NP-hard problems</li></ul>					
5	<b>Learning outcomes and competences:</b> Students will be able to <ul style="list-style-type: none"><li>• understand and apply basic analytical techniques,</li><li>• explain and apply basic algorithmic approaches,</li><li>• judge which effects these approaches have, and</li><li>• know the limits of using these approaches.</li></ul>					

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>	
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%	
7	<b>Study Achievement:</b>			
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>	
a)	Assignments, course paper or progress reports		CA	
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr. Christian Scheideler			
13	<b>Other Notes:</b> <i>Remarks of course Advanced Algorithms:</i> <b>Implementation Method</b> The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Slides of the lecture; exercise sheets</li> <li>• Additional literature will be announced in the course</li> </ul>			

4 General Elective Area

Advanced Distributed Algorithms and Data Structures						
Advanced Distributed Algorithms and Data Structures						
<b>Module number:</b> M.079.4006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05700 Advanced Distributed Algorithms and Data Structures	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Advanced Distributed Algorithms and Data Structures:</i> <b>Recommended Proficiencies</b> Algorithms and data structures, distributed algorithms and data structures					
4	<b>Contents:</b>  <i>Contents of the course Advanced Distributed Algorithms and Data Structures:</i> After a short introduction of the foundations of graph and network theory as well as distributed programs, the lecture presents advanced methods in the area of distributed algorithms and data structures. Topics covered in the course are access control, synchronization, consensus, information dissemination, hybrid networks, scheduling, and optimization. In addition to presenting solutions to these topics, also concrete applications will be presented. The lecture gives an introduction to state-of-the-art advanced distributed algorithms and data structures. In addition to the presentation of the corresponding protocols, their correctness and efficiency will be shown in a rigorous way. The lecture is structured as follows:  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations of graph and network theory</li> <li>• Access control</li> <li>• Synchronization</li> <li>• Consensus</li> <li>• Information dissemination</li> <li>• Hybrid networks</li> <li>• Scheduling</li> <li>• Optimization</li> </ul> In addition to presenting solution to these topics, also concrete applications will be presented.					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply basic analytical techniques,</li> <li>• explain and use basic algorithmic approaches,</li> <li>• judge which effects these approaches have, and</li> <li>• know the limits of using these approaches.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Christian Scheideler</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Advanced Distributed Algorithms and Data Structures:</i></p> <p><b>Implementation Method</b></p> <p>The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Slides of the lecture; exercise sheets</li> <li>• Additional literature will be announced in the course</li> </ul>										

4 General Elective Area

Data Science for Software Engineering							
Data Science for Software Engineering							
<b>Module number:</b> M.079.4101	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05817 Data Science for Software Engineering	L2 Ex3	75	105	C	30	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> <i>Prerequisites of course Data Science for Software Engineering:</i> <b>Recommended Proficiencies</b> Good programming skills using Java and/or Python is helpful to make the assignments. Basic background on machine learning is helpful to understand some of the Data Science concepts.						
4	<b>Contents:</b> <i>Contents of the course Data Science for Software Engineering:</i> Software engineers deal with software repositories in their daily work, such as when they develop source code in version control systems, or post issues in issue trackers, or communicate through emails in mailing lists, or discuss in forums and blogs. The big amount of data in software repositories, their continuous evolution, complexity and heterogeneity present a challenge for software engineers. In the past years, researchers proposed approaches that use techniques from the data science to support software engineers. This course will explain the application of data science techniques on software repositories to achieve common software engineering tasks. The course includes the following topics: <ul style="list-style-type: none"> <li>• Types and structure of software repositories.</li> <li>• Clustering of source code.</li> <li>• Natural language processing pipeline.</li> <li>• Topic modeling.</li> <li>• Word embedding.</li> <li>• Information retrieval.</li> <li>• Supervised machine learning.</li> <li>• Statistical analysis.</li> </ul> <p>Concepts are discussed in the lectures and applied using a set of group assignments to analyze opensource systems, and achieve certain software architecture and maintenance tasks.</p>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• Clarify and discuss types and structure of software repositories.</li> <li>• Clarify and discuss main concepts of data science techniques, and their application on software repositories.</li> <li>• Apply data science techniques on large-scale software repositories.</li> <li>• Derive useful implications from the analysis results.</li> <li>• Summarize and report analysis results in a scientific format.</li> <li>• Work in teams.</li> <li>• Write scientific reports</li> <li>• Present research results</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination</td> <td>90-120 min or 30-45 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination	90-120 min or 30-45 min	100%
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr. Mohamed Aboubakr Mohamed Soliman</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Data Science for Software Engineering:</i></p> <p><b>Implementation Method</b> The course focus on the application of data science methods in software engineering more than the mathematical background of data science methods. The main concepts of methods are conveyed through a presentation as part of a lecture and the application of methods is further investigated through group assignments and presentations.</p> <p><b>Learning Material, Literature</b> Beside the slides, further learning materials from prominent publications in the software engineering literature will be provided for each topic.</p>
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#### 4 General Elective Area

Data Science in Industrial Applications						
Data Science in Industrial Applications						
<b>Module number:</b> M.079.4075	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05795 Data Science in Industrial Applications	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Data Science in Industrial Applications:</i> The course “Data Science in Industrial Applications” deals with the methods and techniques of data analysis in an industrial context. Students learn basic concepts of data analysis and how to apply them in practice.</p> <p>The increasing networking of machines, sensors and IT systems in the context of Industry 4.0 has led to a rapid increase in the amount of available data. The analysis of data offers enormous potential for the automation of cognitive tasks, the optimization of processes and the further creation of value from data. The lecture will provide an overview of the challenges and solution approaches for the industrial application of Data Science. This includes the integration of industrial data sources from the field, the IT landscape in manufacturing companies and the setup of (Big Data) infrastructure, typical algorithms in the area of time series processing, optimization or image processing as well as the embedding in business processes. Theoretical and methodological basics, concepts and tools are introduced during the lecture and applied in small groups based on a case study as well as deepened in home exercises. The theoretical concepts for the planning, introduction and implementation of Industrial Data Science in theory are supplemented by practical real-life examples.</p>					

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> Students <ul style="list-style-type: none"> <li>• understand the challenges of applying Data Science in industrial applications,</li> <li>• have an overview of typical application examples,</li> <li>• are able to apply methods of signal processing, machine learning, and statistics to industrial problems,</li> <li>• are able to plan the implementation of data acquisition, data architecture, and integration into business processes,</li> <li>• are able to develop solutions on their own as well as in cooperation,</li> <li>• are proficient in basic project management skills.</li> </ul>										
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)										
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>							
	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%							
7	<b>Study Achievement:</b> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 5%; text-align: center;">zu</th> <th style="width: 55%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 20%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement										
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.										
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).										
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4										
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Roman Dumitrescu										

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Data Science in Industrial Applications:</i></p> <p><b>Implementation Method</b></p> <p>The course includes lectures (slide-based), exercises (interactive), and project work. In the lectures, the theoretical basics of data analysis in an industrial context are taught. In the exercises, students have the opportunity to apply what they have learned by means of practical tasks. The project work offers the students the opportunity to apply the learned knowledge in a larger context. In the exercise, knowledge transfer and application of the concepts take place in a case study in the form of workshops and implementation of an industrial analytics application in independent group work.</p> <p><b>Learning Material, Literature</b></p> <p>A more detailed list of the lecture materials and references will be given in the first course. A good first insight into the subject area is given by:</p> <ul style="list-style-type: none"> <li>• Wiendahl, Hans-Peter; Wiendahl, Hans-Hermann (2019): Betriebsorganisation für Ingenieure. 9., vollständig überarbeitete Auflage. München: Hanser (Hanser eLibrary).</li> <li>• Zahn, Erich; Schmid, Uwe (1996): Grundlagen und operatives Produktionsmanagement. Mit 42 Tabellen. Stuttgart: Lucius &amp; Lucius (Grundwissen der Ökonomik Betriebswirtschaftslehre, 1).</li> <li>• Günther Schuh; Achim Kampker: Strategie und Management produzierender Unternehmen: Handbuch Produktion und Management 1 (VDI-Buch) (German Edition).</li> <li>• Schuh, Günther; Riesener, Michael (2018): Produktkomplexität managen. Strategien - Methoden - Tools. 3., vollständig überarbeitete Auflage. München: Hanser (Hanser eLibrary). Online verfügbar unter <a href="http://www.hanser-elibrary.com/doi/book/10.3139/9783446453340">http://www.hanser-elibrary.com/doi/book/10.3139/9783446453340</a>.</li> <li>• Schuh, Günther; Schmidt, Carsten (2014): Produktionsmanagement. DOI: 10.1007/978-3-642-54288-6.</li> <li>• Bishop, Christopher M. (2006): Pattern recognition and machine learning. New York: Springer (Information science and statistics).</li> <li>• Cao, Longbing (2018): Data Science. In: ACM Comput. Surv. 50 (3), S. 1–42. DOI: 10.1145/3076253.</li> <li>• Geron, Aurelien (2019): Hands-On Machine Learning with Scikit-Learn and TensorFlow: O'Reilly Media.</li> <li>• Goodfellow, Ian; Bengio, Yoshua; Courville, Aaron (2016): Deep Learning. MIT Press.</li> <li>• James, Gareth; Witten, Daniela; Hastie, Trevor; Tibshirani, Robert (2013): An Introduction to Statistical Learning. New York, NY: Springer New York (103).</li> <li>• Mitchell, Tom M. (1997): Machine Learning. New York: McGraw-Hill (McGraw-Hill series in computer science).</li> <li>• Runkler, Thomas A. (2016): Data Analytics. Wiesbaden: Springer Fachmedien Wiesbaden.</li> <li>• Russell, Stuart (2009): Artificial Intelligence: A Modern Approach. 3rd Edition. Pearson.</li> <li>• Schutt, Rachel; O'Neil, Cathy (2013): Doing data science. Straight talk from the frontline. 1. ed. Beijing: O'Reilly.</li> </ul>
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4 General Elective Area

Explainable Artificial Intelligence							
Explainable Artificial Intelligence							
<b>Module number:</b> M.079.4091	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05807 Explainable Artificial Intelligence	L2 Ex1 P2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Explainable Artificial Intelligence:</i> <b>Recommended Proficiencies</b> Basic knowledge in machine learning and programming						
4	<b>Contents:</b>  <i>Contents of the course Explainable Artificial Intelligence:</i> Explaining the predictions of machine learning models is important in an increasing number of applications. For example, bank customers would like to know why their loan was denied; machine learning engineers would like to debug and improve their models; managers would like to ensure regulatory compliance. This course aims to explain the predictions of machine learning models and introduces different explanation methods to do so. Explanation methods can be distinguished whether they are specific to a certain model or model-agnostic and whether they explain an individual prediction or the entire model.  <ul style="list-style-type: none"> <li>• Introduction (e.g., importance of interpretability, evaluation of interpretability, datasets used in case studies)</li> <li>• Interpretable models (e.g., linear regression, logistic regression, decision trees, decision rules)</li> <li>• Global model-agnostic methods (e.g., partial dependence plots, permutation feature importance, global surrogate models)</li> <li>• Local model-agnostic methods (e.g., LIME, Anchors, SHAP, counterfactual explanations)</li> <li>• Model-specific methods (e.g., for neural networks)</li> </ul>						

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> After completing the module, students will be able to <ul style="list-style-type: none"> <li>• recognize and discuss the importance of interpretability</li> <li>• explain and apply important explanation methods (e.g., interpretable models, model-agnostic methods, and model-specific methods)</li> <li>• recognize characteristics of datasets, machine learning tasks, and machine learning models in application problems and argue which explanation method is appropriate for a given problem</li> <li>• implement simple explanation methods from scratch</li> <li>• extend and modify existing explanation methods</li> <li>• discuss problems and proposed solutions with experts in the field</li> <li>• read and discuss research literature in the area of XAI</li> </ul>										
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)										
	ZU	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>							
	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%							
7	<b>Study Achievement:</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">ZU</th> <th style="text-align: center;">Type of achievement</th> <th style="text-align: center;">Duration or Scope</th> <th style="text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			ZU	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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a)	Assignments, course paper or progress reports		CA								
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement										
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.										
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).										
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4										
12	<b>Module coordinator:</b> Dr. Stefan Heindorf										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Explainable Artificial Intelligence:</i></p> <p><b>Implementation method</b> Slides and blackboard writing. Important concepts and techniques will be practiced through exercises in the lecture room and tutorials, and applied in a mini-project.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Slides</li><li>• Exercises</li><li>• Book: Christoph Molnar. Interpretable machine learning. 2020.</li><li>• Additional material and literature will be announced in the course.</li></ul>
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4 General Elective Area

Foundational Methods for Knowledge Representation and Reasoning							
Foundational Methods for Knowledge Representation and Reasoning							
<b>Module number:</b> M.079.4210	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer- / winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05811 Parameterized Algorithms and Complexity	L3 Ex2	75	105	C	35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Parameterized Algorithms and Complexity:</i> <b>Recommended Proficiencies</b> Basic knowledge of algorithms and their analysis, complexity theory (complexity classes, Turing machines, NP-completeness) as well as first-order logic						
4	<b>Contents:</b>  <i>Contents of the course Parameterized Algorithms and Complexity:</i> The course gives an introduction to parameterized complexity theory. It <ul style="list-style-type: none"> <li>• gives an introduction to the foundational ideas of parameterized complexity</li> <li>• covers several algorithmic techniques that help constructing efficient algorithms (with respect to parameterized complexity), such as kernelization and bounded search trees,</li> <li>• covers the central concepts and tools of the hardness theory in parameterized complexity, in particular fpt-reductions as well as the W-hierarchy and A-hierarchy, and</li> <li>• gives a brief introduction to fine-grained complexity.</li> </ul>						

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> Students will be able to <ul style="list-style-type: none"> <li>• use the technical notions covered in the lecture competently and explain these notions</li> <li>• reproduce and explain the proofs and proof techniques covered in the lecture, including algorithmic techniques</li> <li>• in particular, reproduce and explain proofs of correctness and running time of the covered algorithms</li> <li>• apply constructive proofs and formal methods from the lecture to specific instances</li> <li>• apply the proof techniques and algorithmic techniques on new, similar problems</li> </ul>			
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or oral examination or presentation	90-120 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b>			
	zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
	a)	Assignments, course paper or presentation		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Anni-Yasmin Turhan			

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Parameterized Algorithms and Complexity:</i></p> <p><b>Implementation Method</b></p> <p>Content is presented via beamer presentation or on white board. Exercises provide opportunities to deepen the understanding of the content and train the competences obtained in the course. The lecture in most part follows select chapters of the following literature:</p> <ul style="list-style-type: none"><li>• <i>Parameterized Algorithms</i> by M. Cygan, F. V. Fomin, L. Kowalik, D. Lokshtanov, D. Marx, M. Pilipczuk, M. Pilipczuk, S. Saurabh (Springer 2015).</li><li>• <i>Parameterized Complexity Theory</i> by J. Flum, M. Grohe (Springer 2006).</li><li>• Additional literature might be announced in the course.</li></ul>
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#### 4 General Elective Area

Foundations of Knowledge Graphs						
Foundations of Knowledge Graphs						
<b>Module number:</b> M.079.4054	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05806 Foundations of Knowledge Graphs	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Foundations of Knowledge Graphs:</i> <b>Recommended Proficiencies</b> Knowledge of Graph theory and logics is beneficial.					
4	<b>Contents:</b>  <i>Contents of the course Foundations of Knowledge Graphs:</i> Knowledge graphs are used in an increasing number of applications. Large organisations such as Google, Yahoo! and the BBC rely on these technologies to organise and manage the access to the the large amounts of data they manage. This lecture aims to present approaches for building, storing, querying and using knowledge graphs, especially in intelligent applications. We will being by studying knowledge representation techniques for knowledge graphs. Technologies for querying and storing knowledge (e.g., graph databases) will be presented subsequently. Machine learning techniques for embeddings knowledge graphs and making them amenable to applications driven by classical machine learning will follow. Finally, we will study dedicated approaches for learning on knowledge graphs. The key topics of the module are hence as follows:  <ul style="list-style-type: none"> <li>• Semantic networks</li> <li>• RDF(S) graphs</li> <li>• Property graphs</li> <li>• Description logics and OWL</li> <li>• Knowledge graph embeddings</li> <li>• Explainable machine learning on knowledge graphs</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students can carry out the following after the completion of the module:</p> <ul style="list-style-type: none"> <li>• Model knowledge graphs including their semantics</li> <li>• Query knowledge graphs</li> <li>• Compute embeddings for knowledge graphs</li> <li>• Use knowledge graphs in intelligent applications</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of achievement</th> <th style="width: 20%;">Duration or Scope</th> <th style="width: 20%;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Axel-Cyrille Ngonga Ngomo</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Foundations of Knowledge Graphs:</i></p> <p><b>Implementation method</b></p> <p>2 SWS of lectures within which the students will be presented with novel content weekly. The lecture will be self-contained with the students being presented with the premises for understanding particular aspects of knowledge graphs as well as with the corresponding conclusions and approaches derived from these premises. 1 SWS of exercises allow the students to deal with the concepts presented in the lecture through formal analysis and programming. The 2 SWS of mini-projects ensure that the students obtain a holistic understanding of the concepts learned by applying them to a more complex task than the one addressed in the exercises.</p> <p><b>Learning Material, Literature</b></p> <p>Slides, homework assignments</p>
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4 General Elective Area

Machine Learning 1							
Machine Learning 1							
<b>Module number:</b> M.079.4032	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05701 Machine Learning 1	L3 Ex2	75	105	C	75/25	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Machine Learning 1:</i> <b>Recommended Proficiencies</b> Basic knowledge in mathematics (linear algebra, statistics), programming and algorithms.						
4	<b>Contents:</b>  <i>Contents of the course Machine Learning 1:</i> Due to the ever increasing amount of data that is routinely produced in our information society, the topic of machine learning has become increasingly important in the recent years, not only as a scientific discipline but also as a key technology of modern software and intelligent systems. This lecture provides an introduction to the topic of machine learning, with a specific focus on supervised learning for classification and regression. The lecture covers theoretical foundations of generalisation as well as practical topics and concrete learning algorithms.  <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations (e.g., the learning problem, generalization theory, bias-variance tradeoff)</li> <li>• Techniques (e.g., The linear model, non-linear techniques, SVM, tree-based methods, ensembles, deep learning)</li> <li>• Validation and practical implementations (e.g., metrics, training vs testing, cross-validation, AutoML)</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• understand the statistical foundations of generalisation, i.e., the induction of models from data, and the complete machine learning pipeline.</li> <li>• implement basic methods of supervised learning for classification and regression, apply them to real-world examples, and make necessary adaptations.</li> <li>• understand and use practical tools for model validation and automated machine learning pipelines.</li> <li>• assess the advantages and disadvantages of using various machine learning techniques in various situations.</li> </ul>										
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning 1:</i></p> <p><b>Implementation method</b> Theoretical foundations and concepts of machine learning will be taught in the form of a lecture and deepened in practical exercise courses, group work as well as individual homework.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Script</li><li>• Y.S. Abu-Mostafa, M. Magdon-Ismail, H.T. Lin. Learning from Data, AMLBook, 2012.</li><li>• P. Flach. Machine Learning, Cambridge Univ. Press, 2012.</li><li>• E. Alpaydin. Machine Learning, Oldenbourg, 2008.</li><li>• C.M. Bishop. Pattern Recognition and Machine Learning, Springer, 2006.</li></ul>
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4 General Elective Area

Machine Learning for Biometrics						
Machine Learning for Biometrics						
<b>Module number:</b> M.079.4088	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05709 Machine Learning for Biometrics	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Machine Learning for Biometrics:</i> Biometric verification is defined as the automated recognition of individuals based on their behavioral or biological characteristics. The course will give an overview of modern biometric systems and specifically address their functionality and challenges. For this purpose, various approaches of machine learning will be introduced, which aim at enabling reliable biometric recognition (e.g. by means of face recognition). At the same time, biometric applications place very specific requirements on the underlying algorithms. The course will specifically address these requirements and how they can be met algorithmically and in the algorithmic learning process. This includes the topics of privacy, fairness, explainability, uncertainties, efficiency, attacks and their automated detection.</p> <p>The course includes the following content:</p> <ul style="list-style-type: none"> <li>• Biometric systems, operation modes, and evaluation</li> <li>• Recap on traditional and deep learning</li> <li>• Face, Iris, and fingerprint recognition</li> <li>• Soft-biometrics and privacy</li> <li>• Fairness and bias in biometric systems</li> <li>• Explainability and confidence in biometric systems</li> <li>• Biometric sample quality</li> <li>• Efficient biometric systems</li> <li>• Presentation attacks and detection</li> <li>• Multi-biometric fusion</li> <li>• Biometric indexing</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• independently evaluate biometric systems,</li> <li>• train biometric recognition models for different modalities,</li> <li>• automatically detect biometric attacks and make systems robust against such attacks,</li> <li>• explain various challenges of biometric systems and name solution strategies to counter them,</li> <li>• name and explain open research questions in biometrics.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <p>none</p>										
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4)</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Philipp Terhörst</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning for Biometrics:</i></p> <p><b>Implementation Method</b></p> <p>First, students are given an overview of biometrics and its applications and basic functionalities. Then, required concepts of machine learning are introduced in a compact way. These will be applied and developed in context when dealing with specific biometric requirements. Parallel to the lecture, the theoretical concepts are practiced in the exercises using facial data. This is done in the form of short hand-written and implementation tasks.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Anil K. Jain, Patrick Flynn, and Arun A. Ross. 2010. Handbook of Biometrics (1st. ed.). Springer Publishing Company, Incorporated.</li><li>• Further literature will be announced in the lecture.</li></ul>
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#### 4 General Elective Area

Multi-Objective Optimisation							
Multi-Objective Optimisation							
<b>Module number:</b> M.079.4095	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05821 Multi-Objective Optimisation	L3 Ex2	75	105	C	30/15	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> <i>Prerequisites of course Multi-Objective Optimisation:</i> <b>Recommended Proficiencies</b> Solid basic knowledge of algorithms and data structures, mathematics, as well as basic knowledge of optimization are beneficial.						
4	<b>Contents:</b> <i>Contents of the course Multi-Objective Optimisation:</i> Optimization problems are ubiquitous, and we all (approximately) solve them in everyday life, such as when finding routes with Google Maps to quickly get from point A to point B or deciding on a checkout lane with the shortest waiting queue (shortest expected waiting time) at the supermarket. However, optimization problems are rarely single-criteria. Instead, they are typically multi-criteria in nature, with the individual objectives usually conflicting with each other. For example, in route planning, the distance traveled may be relevant (shorter is better), and fuel consumption may also be a consideration (lower is better). The shortest route may lead through the city center with many stop-and-go maneuvers at red lights, especially during peak hours. On the other hand, a longer route around the city may consume less fuel. Accordingly, the goal in multi-objective optimization is to find a set of optimal compromise solutions. This course provides a comprehensive introduction to multi-objective optimization and the associated challenges. In addition to classical general approaches, exact methods for selected combinatorial optimization problems are presented, along with heuristic (nature-inspired) methods. The course also covers heuristic solution approaches for problems with more than three criteria (many-objective optimization).						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• Explain, implement, and apply important exact algorithms for multi-criteria minimum spanning tree problems and multi-criteria shortest path problems</li> <li>• Understand the limitations of exact algorithms for multi-criteria problems</li> <li>• Explain and apply biologically inspired heuristics for multi-objective problems</li> <li>• Assess, evaluate, and visualize the quality of computed results from multi-criteria algorithms</li> <li>• Understand the challenges of problems with more than three criteria and explain solution approaches</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Multi-Objective Optimisation:</i></p> <p><b>Implementation Method</b> Slide-based lecture with interspersed assignments. In the tutorial, the knowledge transfer and application of what has been learned takes place in both theoretical and practical assignments.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Deb, Kalyanmoy. „Multi-Objective Optimization Using Evolutionary Algorithms“.</li><li>• Ehrgott, Matthias. Multicriteria Optimization. Bd. 491. Lecture Notes in Economics and Mathematical Systems. Berlin, Heidelberg: Springer, 2000.</li><li>• Additional literature will be announced in the course.</li></ul>
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4 General Elective Area

Unsupervised Learning and Evolutionary Optimisation Using R							
Unsupervised Learning and Evolutionary Optimisation Using R							
<b>Module number:</b> M.079.4093	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05829 Unsupervised Learning and Evolutionary Optimisation Using R	L3 Ex2	75	105	C	70	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Unsupervised Learning and Evolutionary Optimisation Using R:</i> <b>Recommended Proficiencies</b> <ul style="list-style-type: none"> <li>• Basic knowledge and interest in mathematics, statistics and probability theory</li> <li>• Basic knowledge of programming</li> </ul>						
4	<b>Contents:</b>  <i>Contents of the course Unsupervised Learning and Evolutionary Optimisation Using R:</i> The course includes the formal and applied concepts of unsupervised machine learning and its implementation in the statistical programming language R. In particular, the following topics are covered in a theoretical and applied manner: <ul style="list-style-type: none"> <li>• Introduction to the statistical programming language R</li> <li>• Data pre-processing and quality aspects of data</li> <li>• (Stream) clustering techniques</li> <li>• Dimensionality reduction techniques</li> <li>• Basic principles of evolutionary optimisation, both single- and multi-objective</li> <li>• Practical application of the methods using R in individual and group work</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>After completing the module, students will be able to . . .</p> <ul style="list-style-type: none"> <li>• properly assess data quality and select suitable techniques for data pre-processing</li> <li>• explain and apply core methods of unsupervised learning</li> <li>• understand the basic principles of evolutionary optimisation methods</li> <li>• competently apply techniques to assess the quality of optimisation procedures</li> <li>• use the statistical software R for statistical data analysis, unsupervised learning and evolutionary optimisation in a competent manner</li> <li>• analyse problems in a team and present practice-relevant solutions</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 20%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 20%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Heike Trautmann</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Unsupervised Learning and Evolutionary Optimisation Using R:</i></p> <p><b>Implementation Method</b></p> <p>An introduction to the statistical programming language R is given compactly in the first weeks of the course. Methods of unsupervised machine learning are covered within lecture presentations interleaved with interactive exercises. Methods understanding will be further deepened in tutorials focusing both on theory as well as application-oriented tasks using R.</p> <p><b>Learning Material, Literature</b></p> <p>Recommended for the statistical programming language R:</p> <ul style="list-style-type: none"><li>• Hadley Wickham &amp; Garrett Golemund (2023). R for Data Science: Import, Tidy, Transform, Visualize, and Model Data. 2nd ed. O'Reilly</li><li>• Torsten Hothorn and Brian S. Everitt (2014). A Handbook of Statistical Analyses Using R. Chapman &amp; Hall/CRC Press, 3rd edition, 2014.</li><li>• C. Heumann, M. Schomaker, and Shalabh. Introduction to Statistics and Data Analysis With Exercises, Solutions and Applications in R. Springer, 2017.</li></ul> <p>The methods sections are based on a variety of references which will be announced in the lecture.</p>
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## 4.10 Computer Science Focus Area Security

Advanced Distributed Algorithms and Data Structures						
Advanced Distributed Algorithms and Data Structures						
<b>Module number:</b> M.079.4006	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05700 Advanced Distributed Algorithms and Data Structures	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none <i>Prerequisites of course Advanced Distributed Algorithms and Data Structures:</i> <b>Recommended Proficiencies</b> Algorithms and data structures, distributed algorithms and data structures					

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Advanced Distributed Algorithms and Data Structures:</i>            After a short introduction of the foundations of graph and network theory as well as distributed programs, the lecture presents advanced methods in the area of distributed algorithms and data structures. Topics covered in the course are access control, synchronization, consensus, information dissemination, hybrid networks, scheduling, and optimization. In addition to presenting solutions to these topics, also concrete applications will be presented.            The lecture gives an introduction to state-of-the-art advanced distributed algorithms and data structures. In addition to the presentation of the corresponding protocols, their correctness and efficiency will be shown in a rigorous way. The lecture is structured as follows:</p> <ul style="list-style-type: none"> <li>• Introduction</li> <li>• Foundations of graph and network theory</li> <li>• Access control</li> <li>• Synchronization</li> <li>• Consensus</li> <li>• Information dissemination</li> <li>• Hybrid networks</li> <li>• Scheduling</li> <li>• Optimization</li> </ul> <p>In addition to presenting solution to these topics, also concrete applications will be presented.</p>										
5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply basic analytical techniques,</li> <li>• explain and use basic algorithmic approaches,</li> <li>• judge which effects these approaches have, and</li> <li>• know the limits of using these approaches.</li> </ul>										
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">ZU</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			ZU	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										

#### 4 General Elective Area

9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4
12	<b>Module coordinator:</b> Prof. Dr. Christian Scheideler
13	<b>Other Notes:</b> <i>Remarks of course Advanced Distributed Algorithms and Data Structures:</i> <b>Implementation Method</b> The lecture uses a blackboard and slides as well as small exercises for the students during the lecture. It will be supported by tutorial groups. Students have the opportunity in tutorial groups to work on problems in a group and to discuss solutions of the exercise sheets with the tutors. <b>Learning Material, Literature</b> <ul style="list-style-type: none"><li>• Slides of the lecture; exercise sheets</li><li>• Additional literature will be announced in the course</li></ul>

#### 4 General Elective Area

Designing code analyses for large-scale software systems 1							
Designing code analyses for large-scale software systems 1							
<b>Module number:</b> M.079.4070	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05781 Designing code analyses for large-scale software systems 1	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Designing code analyses for large-scale software systems 1:</i> <b>Recommended Proficiencies</b> A mature understanding of the Java programming languages and object-oriented programming will be helpful.						

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Designing code analyses for large-scale software systems 1:</i>          Static code analysis is frequently used to find programming mistakes automatically, by searching for suspicious anti-patterns in a program's code. This course will explain how to design static code analysis that are inter-procedural, i.e., consider the whole program, across procedure boundaries. Designing such analyses is challenging, as they need to handle millions of program statements efficiently and precisely. Example applications are drawn from the area of IT security. This course is part of a combination DECA 1/2. In DECA 2 we will be covering current approaches directly out of research. We strongly recommend attending DECA 1 before DECA 2.</p> <p><b>Topics covered include:</b></p> <ul style="list-style-type: none"> <li>• Type systems and flow-insensitive, constraint-based analysis</li> <li>• Lattices and fixed points</li> <li>• Intra-procedural flow-sensitive static code analysis</li> <li>• Interval analysis, widening and narrowing</li> <li>• Call-graph construction</li> <li>• Pointer Analysis</li> <li>• Inter-procedural program analysis</li> <li>• Call-strings approach to context-sensitive analysis</li> <li>• Functional approach to context-sensitive analysis</li> <li>• Value-based termination, VASCO</li> <li>• Distributive analyses using IFDS</li> <li>• Sensible arrangements of Flow Functions</li> <li>• Distributive analyses using IDE</li> </ul> <p>Throughout, we will discuss applications to software security.</p>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of the module, students will be able to</p> <ul style="list-style-type: none"> <li>• name and distinguish the most important concepts and algorithms in the field of static program analysis</li> <li>• explain the effects of various alternative design decisions when designing a static program analysis</li> <li>• implement and apply simple static program analyses themselves and illustrate their function</li> <li>• contrast and compare data structures and algorithms for static program analysis</li> <li>• evaluate and justify the applicability of certain analysis procedures to specific application contexts, and</li> <li>• develop tools for static program analysis by composing several analysis methods.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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#### 4 General Elective Area

7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Eric Bodden		
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Designing code analyses for large-scale software systems 1:</i></p> <p><b>Implementation method</b> Lectures and group exercises as well as practical programming labs using worldwide leading frameworks for static code analysis</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Thomas Reps, Susan Horwitz, and Mooly Sagiv. 1995. Precise interprocedural dataflow analysis via graph reachability. POPL '95</li> <li>• Shmuel Sagiv, Thomas W. Reps, and Susan Horwitz. 1995. Precise Interprocedural Dataflow Analysis with Applications to Constant Propagation. TAPSOFT '95</li> <li>• Akash Lal, Thomas Reps, and Gogul Balakrishnan. 2005. Extended weighted pushdown systems. CAV 2005</li> <li>• Nomair A. Naeem, Ondrej Lhoták, and Jonathan Rodriguez. 2010. Practical extensions to the IFDS algorithm. CC 2010</li> <li>• Yannis Smaragdakis, Martin Bravenboer, and Ondrej Lhoták. 2011. Pick your contexts well: understanding object-sensitivity. POPL 2011</li> <li>• Eric Bodden. 2012. Inter-procedural data-flow analysis with IFDS/IDE and Soot. SOAP 2012</li> <li>• Rohan Padhye, Uday P. Khedker. Interprocedural Data Flow Analysis in Soot using Value Contexts. SOAP 2013</li> </ul>		

4 General Elective Area

<b>Designing code analyses for large-scale software systems 2</b>							
Designing code analyses for large-scale software systems 2							
<b>Module number:</b> M.079.4071	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05800 Designing code analyses for large-scale software systems 2	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Designing code analyses for large-scale software systems 2:</i> <b>Recommended Proficiencies</b> We strongly recommend that attendees have completed DECA 1 beforehand. A mature understanding of the Java and/or C++ programming languages and object-oriented programming will be helpful.						

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Designing code analyses for large-scale software systems 2:</i>          Static code analysis has the goal of finding programming mistakes automatically, by searching for suspicious anti-patterns in a program's code. This course will explain how to design static code analysis that are inter-procedural, i.e., consider the whole program, across procedure boundaries. Designing such analyses is challenging, as they need to handle millions of program statements efficiently and precisely. Example applications are drawn from the area of IT security.          This course builds on the DECA 1 course. In DECA 2, we discuss novel concepts directly from research, for example so-called demand-driven analyses, which are characterized by a more precise and at the same time more efficient analysis, but also pushdown systems, which provide a allow elegant modeling and at the same time fast execution of program analyses. Last but not least, we explain current solutions to practical problems in static analysis, such as the use of reflection and native code.</p> <p><b>Topics covered include:</b></p> <ul style="list-style-type: none"> <li>• Program analysis of software product lines</li> <li>• Modeling call stacks and field accesses with Pushdown Systems</li> <li>• Modeling auxiliary analysis information with Weighted Pushdown Systems</li> <li>• Efficiency and precision gains through Demand-driven Program Analysis</li> <li>• Synchronized Pushdown Systems in the Boomerang framework</li> <li>• Applied Android code analysis with FlowDroid</li> <li>• Dealing with Reflection through TamiFlex</li> <li>• Hybrid static and dynamic analysis with Harvester</li> <li>• Learning source, sink and sanitizer definitions with SWAN and SWAN Assist</li> <li>• Explainable static analysis</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of the module, students will be able to</p> <ul style="list-style-type: none"> <li>• name and explain the most important challenges in inter-procedural static program analysis</li> <li>• name and distinguish current methods in the field of inter-procedural static program analysis</li> <li>• explain the implications of various alternative design decisions in the design of an inter-procedural static program analysis.</li> <li>• contrast and compare data structures and algorithms for inter-procedural static program analysis and</li> <li>• evaluate and justify the applicability of current analysis techniques in a broad range of application contexts.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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#### 4 General Elective Area

7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Eric Bodden		
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Designing code analyses for large-scale software systems 2:</i></p> <p><b>Implementation method</b> Lectures and group exercises as well as programming exercises using widely used real-world static analysis frameworks (e.g. Soot, Phasar, FlowDroid)</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Context-, Flow-, and Field-sensitive Data-flow Analysis Using Synchronized Pushdown Systems (Johannes Späth, Karim Ali, Eric Bodden), In Proceedings of the ACM SIGPLAN Symposium on Principles of Programming Languages, pages 48:1–48:29, 3(POPL), 2019.</li> <li>• FlowDroid: Precise Context, Flow, Field, Object-sensitive and Lifecycle-aware Taint Analysis for Android Apps (Steven Arzt, Siegfried Rasthofer, Christian Fritz, Eric Bodden, Alexandre Bartel, Jacques Klein, Yves Le Traon, Damien Ochteau, Patrick McDaniel), In Proceedings of the 35th ACM SIGPLAN Conference on Programming Language Design and Implementation, pages 259–269, PLDI '14, ACM, 2014.</li> <li>• Codebase-Adaptive Detection of Security-Relevant Methods (Goran Piskachev, Lisa Nguyen Quang Do, Eric Bodden), In ACM SIGSOFT International Symposium on Software Testing and Analysis (ISSTA), 2019.</li> <li>• Taming Reflection: Aiding Static Analysis in the Presence of Reflection and Custom Class Loaders (Eric Bodden, Andreas Sewe, Jan Sinschek, Hela Oueslati, Mira Mezini), In ICSE '11: International Conference on Software Engineering, pages 241–250, ACM, 2011.</li> </ul>		

4 General Elective Area

Foundations of Cryptography						
Foundations of Cryptography						
<b>Module number:</b> M.079.4020	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05801 Foundations of Cryptography	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Foundations of Cryptography:</i> <b>Recommended Proficiencies</b> Basic Knowledge in IT-Security and cryptography useful but not necessary, basic concepts of complexity theory and probability theory					
4	<b>Contents:</b>  <i>Contents of the course Foundations of Cryptography:</i> Cryptography is an important basic technique in IT security. Internet protocols such as TLS are based on cryptographic primitives such as key exchange, encryption and signatures. In this lecture, important basic concepts of modern cryptography will be introduced. These include encryption schemes, digital signatures, identification protocols, and multiparty computations. In all cases, formal security definitions are presented and, starting from mathematically precise assumptions, provably secure constructions are developed. An essential aspect of the lecture is the construction of efficient and secure cryptographic methods from assumptions that are as general as possible. Contents include:  <ul style="list-style-type: none"> <li>• Symmetric and asymmetric encryption.</li> <li>• Pseudorandom functions, one-way functions, permutations with trapdoors</li> <li>• Hash functions and authentication codes</li> <li>• Digital signatures, one-time signatures and random oracles.</li> <li>• Identification protocols, <math>\Sigma</math> protocols.</li> <li>• Security concepts such as unforgeable signatures and CPA- and CCA-secure encryption schemes.</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of the module, students will be able to:</p> <ul style="list-style-type: none"> <li>• understand, explain and apply concepts and methods of modern cryptography.</li> <li>• select appropriate cryptographic methods according to the security requirements of an application, e.g. distinguish where encryption methods and where authentication methods are appropriate.</li> <li>• combine primitives of cryptography according to application requirements and prove the security of the combination.</li> <li>• define new security concepts and design cryptographic methods that satisfy those concepts.</li> <li>• understand and independently develop security proofs.</li> <li>• acquire latest research results in the field of cryptography by reading scientific papers.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Johannes Blömer</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Foundations of Cryptography:</i></p> <p><b>Implementation method</b></p> <p>Basic concepts are presented in a lecture. In addition, theoretical concepts are deepened in tutorials in small groups. Written exercises and reading groups will be used to practice the practical application of these concepts.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Oded Gorldreich, Foundations of Cryptography I,II,</li><li>• Jonathan Katz, Yehuda Lindell, Introduction to Modern Cryptography</li><li>• Slides from the lectures</li><li>• Lecture notes</li></ul>
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4 General Elective Area

Hardware Security						
Hardware Security						
<b>Module number:</b> M.079.4211	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	K.079.42111 Hardware Security	L3 Ex2	75	105	C	30
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<b>Contents:</b> <i>Contents of the course Hardware Security:</i> Description of the content in English as a text of about 500 to 1000 characters The course includes the following contents: <ul style="list-style-type: none"> <li>• First</li> <li>• ...</li> <li>• 6 to 12 bullet points</li> </ul>					
5	<b>Learning outcomes and competences:</b> Students will be able to <ul style="list-style-type: none"> <li>• Learning outcome 1</li> <li>• ...</li> <li>• 4 to 6 learning outcomes</li> </ul>					
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)					
	<b>zu</b>	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>		
a)		Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%		

#### 4 General Elective Area

7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Jan Jancar		
13	<b>Other Notes:</b> <i>Remarks of course Hardware Security:</i> <b>Implementation Method</b> Text of about 200 to 500 characters <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Literature 1</li> <li>• Literature 2</li> <li>• ...</li> <li>• Additional literature will be announced in the course.</li> </ul>		

4 General Elective Area

Human Factors in Security and Privacy						
Human Factors in Security and Privacy						
<b>Module number:</b> M.079.4092	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05733 Human Factors in Security and Privacy	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Human Factors in Security and Privacy:</i> Humans are important actors in security. A provable secure system is only useful if it can be actually used by users, and system designers need to account for human behavior if they wish to have both security and usability. In this class, we will examine factors of usability of security and privacy through a research-based, project-driven examination. We will cover core areas of security and privacy, as well as cover methods in human interaction (HCI) that can be used to measure the usability of security and privacy. Students are expected to complete problem sets on the topic and complete a research-based project. The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• How to write a scientific research paper?</li> <li>• How to conduct an independent scientific study in the field of Human Factors in Security and Privacy?</li> <li>• Methodology: qualitative, quantitative and “mixed” methods.</li> <li>• Introduction to research and scientific ethics</li> <li>• Introduction to literature research</li> <li>• Presenting scientific results at a conference</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply research methods in human factors in usable security and privacy.</li> <li>• develop relevant hypotheses and research questions in the space of usable security and privacy.</li> <li>• design and deploy a research study and analyze the results.</li> <li>• describe, support, and effectively argue a result using the best practices of scientific writing.</li> <li>• understand ethical issues related to human factors research in security and privacy.</li> <li>• understand the major topics and themes of usable security and privacy.</li> <li>• present research results in class.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Yasemin Acar</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Human Factors in Security and Privacy:</i></p> <p><b>Implementation Method</b></p> <p>The contents are presented and elaborated in the lecture. In the accompanying tutorial, the lecture topics are deepened and discussed both in plenary and in small groups. In addition, students are presenting and discussing their progress on the projects.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Current freely available research papers will be provided in the course.</li><li>• Redmiles, Elissa M., Yasemin Acar, Sascha Fahl, and Michelle L. Mazurek. A summary of survey methodology best practices for security and privacy researchers. 2017. <a href="https://drum.lib.umd.edu/bitstream/handle/1903/19227/CS-TR-5055.pdf">https://drum.lib.umd.edu/bitstream/handle/1903/19227/CS-TR-5055.pdf</a></li><li>• Additional literature will be announced in the course.</li></ul>
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4 General Elective Area

Introduction to Quantum Computation							
Introduction to Quantum Computation							
<b>Module number:</b> M.079.4059	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05706 Introduction to Quantum Computation	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Introduction to Quantum Computation:</i> <b>Recommended Proficiencies</b> Linear Algebra, algorithms						
4	<b>Contents:</b>  <i>Contents of the course Introduction to Quantum Computation:</i> This lecture introduces the fundamental concepts of quantum computation and information from a computer science perspective. This includes an introduction to quantum mechanics, quantum entanglement, quantum algorithms, quantum error correction, and quantum information theory.  <ul style="list-style-type: none"> <li>• Quantum mechanics</li> <li>• Quantum entanglement</li> <li>• Quantum algorithms</li> <li>• Quantum error correction</li> <li>• Quantum information</li> </ul>						
5	<b>Learning outcomes and competences:</b> Students are able to: <ul style="list-style-type: none"> <li>• Describe and apply the postulates of quantum mechanics</li> <li>• Understand the use of entanglement as a resource</li> <li>• Design and analyze fundamental quantum algorithms</li> <li>• Apply the theory of error-correcting codes</li> <li>• Understand and apply basic quantum information theory concepts such as entropy</li> </ul>						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Sevag Gharibian		
13	<b>Other Notes:</b> <i>Remarks of course Introduction to Quantum Computation:</i> <b>Implementation method</b> Slides and blackboard writing. All important concepts and techniques are further deepened with examples in exercises. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press</li> <li>• Lecture slides, exercises</li> </ul>		

4 General Elective Area

Machine Learning for Biometrics						
Machine Learning for Biometrics						
<b>Module number:</b> M.079.4088	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05709 Machine Learning for Biometrics	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Machine Learning for Biometrics:</i> Biometric verification is defined as the automated recognition of individuals based on their behavioral or biological characteristics. The course will give an overview of modern biometric systems and specifically address their functionality and challenges. For this purpose, various approaches of machine learning will be introduced, which aim at enabling reliable biometric recognition (e.g. by means of face recognition). At the same time, biometric applications place very specific requirements on the underlying algorithms. The course will specifically address these requirements and how they can be met algorithmically and in the algorithmic learning process. This includes the topics of privacy, fairness, explainability, uncertainties, efficiency, attacks and their automated detection.</p> <p>The course includes the following content:</p> <ul style="list-style-type: none"> <li>• Biometric systems, operation modes, and evaluation</li> <li>• Recap on traditional and deep learning</li> <li>• Face, Iris, and fingerprint recognition</li> <li>• Soft-biometrics and privacy</li> <li>• Fairness and bias in biometric systems</li> <li>• Explainability and confidence in biometric systems</li> <li>• Biometric sample quality</li> <li>• Efficient biometric systems</li> <li>• Presentation attacks and detection</li> <li>• Multi-biometric fusion</li> <li>• Biometric indexing</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• independently evaluate biometric systems,</li> <li>• train biometric recognition models for different modalities,</li> <li>• automatically detect biometric attacks and make systems robust against such attacks,</li> <li>• explain various challenges of biometric systems and name solution strategies to counter them,</li> <li>• name and explain open research questions in biometrics.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%								
7	<p><b>Study Achievement:</b></p> <p>none</p>										
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v3 (CEMA v3), Masterstudiengang Computer Engineering v3 (CEMA v3), englisch, Masterstudiengang Computer Engineering v4 (CEMA v4)</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr.-Ing. Philipp Terhörst</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Machine Learning for Biometrics:</i></p> <p><b>Implementation Method</b></p> <p>First, students are given an overview of biometrics and its applications and basic functionalities. Then, required concepts of machine learning are introduced in a compact way. These will be applied and developed in context when dealing with specific biometric requirements. Parallel to the lecture, the theoretical concepts are practiced in the exercises using facial data. This is done in the form of short hand-written and implementation tasks.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Anil K. Jain, Patrick Flynn, and Arun A. Ross. 2010. Handbook of Biometrics (1st. ed.). Springer Publishing Company, Incorporated.</li><li>• Further literature will be announced in the lecture.</li></ul>
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#### 4 General Elective Area

<b>Post-Quantum Cryptography</b>						
Post-Quantum Cryptography						
<b>Module number:</b> M.079.4089	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05732 Post-Quantum Cryptography	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Post-Quantum Cryptography:</i> <b>Recommended Proficiencies</b> Basics of cryptography and complexity theory					
4	<b>Contents:</b>  <i>Contents of the course Post-Quantum Cryptography:</i> IT security is largely based on modern cryptographic methods. These include many methods of so-called public-key cryptography such as the RSA and Elgamal encryption methods, the RSA signature method, and the various variants of the Digital Signature Algorithm (DSA). In 1994, Peter Shor presented an efficient algorithm for computing prime factorization of integers and for computing discrete logarithms in finite groups. Thus, all the aforementioned methods of public-key cryptography are insecure if quantum computers of sufficient size and complexity can be realized. It is therefore important to develop alternatives to classical public-key methods that, at least according to current research, cannot be broken by quantum computers. Important candidates (and some close to standardization) for such post-quantum secure methods rely on techniques of error-correcting codes and the geometry of numbers. In this lecture, we will present and discuss important candidates for post-quantum secure methods. The course includes the following contents:  <ul style="list-style-type: none"> <li>• introduction to codes, lattices and discretised Gaussian distributions</li> <li>• lattice and code based encryption</li> <li>• lattice based signatures</li> <li>• lattices and zero-knowledge proofs</li> <li>• lattice based group signatures</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and explain the difference between classical and post-quantum security.</li> <li>• explain the importance of post-quantum cryptography for selected applications.</li> <li>• explain and apply concepts from the field of geometry of numbers and error-correcting codes.</li> <li>• explain important constructions from post-quantum cryptography and prove their security.</li> <li>• explain security assumptions from post-quantum cryptography and apply them to new post-quantum primitives.</li> </ul>										
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 50%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 20%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Johannes Blömer</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Post-Quantum Cryptography:</i></p> <p><b>Implementation Method</b>          Basic concepts are presented in a lecture. In addition, theoretical concepts are deepened in tutorials in small groups as well as in written exercises.</p> <p><b>Learning Material, Literature</b>          References to current learning materials will be given in the lectures.</p>										

4 General Elective Area

Privacy and Technology						
Privacy and Technology						
<b>Module number:</b> M.079.4087	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05705 Privacy and Technology	L2 Ex3	75	105	CE	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Privacy and Technology:</i></p> <p>This course provides students with a basic understanding of privacy risks and principles, the most common technologies for addressing them and the human factors that shape their design. The course will analyze the adversary models and evaluation metrics underlying the design of privacy-enhancing technologies. Moreover a quick overview of usable security as well as identity management and dedicated case studies will be given. For that, a superficial knowledge of HCI basics is desirable. By reviewing relevant papers and giving presentations, the students will get familiar with the latest research in the field and gain knowledge about how to work scientifically. The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• Privacy metrics and adversary models</li> <li>• Anonymous communications</li> <li>• Data-perturbative privacy-enhancing technologies</li> <li>• Anonymization algorithms for databases</li> <li>• Homomorphic encryption and zero knowledge proofs</li> <li>• Selective disclosure for identity management</li> <li>• Usable privacy</li> <li>• Applying privacy principles and case studies</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"> <li>• are able to reason critically about privacy,</li> <li>• gain knowledge in the evaluation of privacy risks,</li> <li>• understand the design aspects of privacy-enhancing technologies,</li> <li>• get familiar with the latest research in the field and</li> <li>• analyze and discuss the space of solutions to a given privacy problem</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Patricia Arias Cabarcos</p>										
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Privacy and Technology:</i></p> <p><b>Implementation Method</b></p> <p>The contents are taught through a presentation in the form of a lecture. In addition, they are deepened in presence exercises in small groups, as well as through individual presentations. Through practical exercise, methods are implemented and applied.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Lecture slides, scientific literature and specific readings will be provided during the course.</li> </ul>										

4 General Elective Area

Quantum Complexity Theory							
Quantum Complexity Theory							
<b>Module number:</b> M.079.4063	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05811 Quantum Complexity Theory	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none <i>Prerequisites of course Quantum Complexity Theory:</i> <b>Recommended Proficiencies</b> Linear Algebra, Quantum Computing						
4	<b>Contents:</b> <i>Contents of the course Quantum Complexity Theory:</i> This lecture provides a brief review of introductory quantum computation, and subsequently moves into quantum complexity theory. Beginning to advanced topics will be covered, including quantum analogues of P and NP (denoted BQP, QCMA, and QMA), quantum satisfiability problems, quantum interactive proofs, and tensor networks. Along the way, semidefinite programming will be introduced as an important tool.  <ul style="list-style-type: none"> <li>• Complexity classes BQP, QCMA, QMA</li> <li>• Quantum algorithms for linear system solving</li> <li>• Quantum Satisfiability Problems</li> <li>• Quantum Interactive Proofs</li> <li>• Semidefinite Programming</li> </ul>						
5	<b>Learning outcomes and competences:</b> Students will be able to  <ul style="list-style-type: none"> <li>• Distinguish language classes from promise classes</li> <li>• Define fundamental quantum complexity classes, such as BQP and QMA</li> <li>• Prove BQP-hardness results via polynomial-time reductions</li> <li>• Prove QMA-hardness results via polynomial-time reductions</li> <li>• Apply semidefinite programming to analyze quantum interactive proofs</li> </ul>						

#### 4 General Elective Area

6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)		
zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Sevag Gharibian		
13	<b>Other Notes:</b> <i>Remarks of course Quantum Complexity Theory:</i> <b>Implementation method</b> Slides and blackboard writing. All important concepts and techniques are further deepened with examples in exercises. <b>Learning Material, Literature</b> <ul style="list-style-type: none"> <li>• Michael A. Nielsen, Isaac L. Chuang, Quantum Computation and Quantum Information, Cambridge University Press</li> <li>• S. Gharibian, Y. Huang, Z. Landau, S. W. Shin, Quantum Hamiltonian Complexity, Foundations and Trends in Theoretical Computer Science</li> <li>• Lecture slides, assignments</li> </ul>		

4 General Elective Area

Real World Crypto Engineering							
Real World Crypto Engineering							
<b>Module number:</b> M.079.4067	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05704 Real World Crypto Engineering	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Real World Crypto Engineering:</i> <b>Recommended Proficiencies</b> Knowledge in programming, IT security and basic knowledge in cryptography						
4	<b>Contents:</b>  <i>Contents of the course Real World Crypto Engineering:</i> Strong cryptography is not always sufficient to protect primary security goals. Even if strong cryptographic algorithms are used, a lot can go wrong when they are implemented. This lecture will dive into the most important protocols and cryptographic protection mechanisms (e.g., TLS, SSH, WPA) and show their basic concepts. Then, we will present prominent attacks that ultimately break the desired security goals. Based on many cases, we will learn what is essential when designing and implementing cryptographic applications. The course includes the following contents:  <ul style="list-style-type: none"> <li>• Brief introduction to cryptography</li> <li>• TLS (Transport Layer Security)</li> <li>• Attacks on TLS (e.g., ROBOT, DROWN, or Invalid Curve)</li> <li>• Evaluation of implementations with systematic methods (e.g., with fuzzing or state learning)</li> <li>• SSH (Secure Shell)</li> <li>• Signal</li> <li>• Cryptocurrencies</li> </ul>						

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> Students will be able to <ul style="list-style-type: none"> <li>• Understand concepts behind major cryptographic protocols</li> <li>• Understand and prevent common attacks on cryptographic protocols</li> <li>• Conduct analyses of cryptographic implementations using systematic methods and standard tools</li> <li>• Identify and assess implementation errors and security issues in cryptographic protocols</li> </ul>										
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)										
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>							
	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%							
7	<b>Study Achievement:</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of achievement</th> <th style="text-align: center;">Duration or Scope</th> <th style="text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement										
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.										
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).										
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4										
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Juraj Somorovsky										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Real World Crypto Engineering:</i></p> <p><b>Implementation method:</b> The topics are conveyed through lecture presentations. They are further deepened through individual practical tasks.</p> <p><b>Learning Material, Literature:</b></p> <ul style="list-style-type: none"><li>• Lecture slides and exercise sheets</li><li>• Scientific literature</li><li>• Additional literature will be announced in the course.</li></ul>
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4 General Elective Area

Usable Security and Privacy						
Usable Security and Privacy						
<b>Module number:</b> M.079.4086	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05804 Usable Security and Privacy	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Usable Security and Privacy:</i> Human factors and usability issues have traditionally played a limited role in security research and secure systems development. Usability issues have been largely disregarded by security experts due to their failure to acknowledge their significance and their insufficient knowledge to tackle them. Today there is consensus on the importance of understanding users behavior and improving usability to achieve true security. This course provides practical and research-oriented knowledge about usable security and privacy. Students will gain practical experience through focused presence exercises and work in small teams to conduct a semester-wide research project with the goal of designing and pretesting a user study on human-centered security and privacy. For that, the course will present research methods and give an introduction into HCI and usability concepts. The course will also address foundational and state-of-the-art research topics in the area, such as privacy and transparency enhancing tools, usable authentication, and developer-centered security. By reviewing relevant papers and giving presentations, the students will get familiar with the latest research in the field and gain knowledge about how to work scientifically. The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• Security and privacy concepts</li> <li>• Foundations of cryptography</li> <li>• Privacy and transparency enhancing tools</li> <li>• HCI and usability research methods</li> <li>• Ethics in technology</li> <li>• Quantitative and qualitative data analysis</li> <li>• Usable authentication</li> <li>• Usable privacy</li> <li>• Developer-centered security</li> </ul>					

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> Students will <ul style="list-style-type: none"> <li>• gain an appreciation for the importance of usable security and privacy</li> <li>• learn about the history of the field and main research areas and challenges</li> <li>• are able to apply methodologies to conduct user research in security and privacy</li> <li>• get familiar with the latest research in the field</li> </ul>										
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)										
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>							
	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%							
7	<b>Study Achievement:</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of achievement</th> <th style="text-align: center;">Duration or Scope</th> <th style="text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td>CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement										
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.										
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).										
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4										
12	<b>Module coordinator:</b> Prof. Dr. Patricia Arias Cabarcos										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Usable Security and Privacy:</i></p> <p><b>Implementation method</b> Basic concepts are presented in a lecture style format. By engaging in presence exercises and conducting a research project in small groups focused on a user-study for usable security and privacy research throughout the semester, students can acquire more profound theoretical and practical knowledge.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Lazar, J., Feng, J.H. and Hochheiser, H., 2017. Research methods in human-computer interaction. Morgan Kaufmann.</li><li>• Redmiles, E.M., Acar, Y., Fahl, S. and Mazurek, M.L., 2017. A summary of survey methodology best practices for security and privacy researchers.</li><li>• Slides and scientific literature references will be given during the course.</li></ul>
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#### 4 General Elective Area

Web Security							
Web Security							
<b>Module number:</b> M.079.4073		<b>Workload (h):</b> 180		<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term	
<b>Language:</b> en		<b>Semester number:</b> 1-3		<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P	
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05809 Web Security	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Web Security:</i> <b>Recommended Proficiencies</b> Knowledge in programming, IT security and basic knowledge in cryptography						

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Web Security:</i></p> <p>Modern web applications and web services usually consist of multiple layers. They are based on different (often complex) technologies that are constantly being developed. Their complexity is often the reason for new types of attacks that can be observed on the web every day.</p> <p>In this lecture, we will focus on the most important technologies and learn what you have to consider while securing your web applications. We will introduce prominent and widespread attacks and show how to prevent them. These range from typical attacks from the OWASP Top 10 list, such as XSS or SQL Injection, to attacks on web services and Single Sign-On standards (e.g., on SAML and OpenID Connect). Based on many cases, we will learn what is important in the design and implementation of secure web applications.</p> <p>The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• Introduction to web technologies</li> <li>• Web Attacks             <ul style="list-style-type: none"> <li>– Cross-Site Scripting (XSS)</li> <li>– Cross-Site Request Forgery (CSRF)</li> <li>– Clickjacking</li> <li>– SQL injection</li> </ul> </li> <li>• XML and SAML             <ul style="list-style-type: none"> <li>– Attacks on XML parsers</li> <li>– Attacks on XML Signature</li> </ul> </li> <li>• JSON and OpenID Connect (OIDC)             <ul style="list-style-type: none"> <li>– Attacks on OIDC</li> </ul> </li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• Understand security concepts behind web applications</li> <li>• Understand and prevent common attacks on web applications</li> <li>• Carry out practical analyses of web applications with common tools</li> <li>• Identify and assess implementation errors and security problems in web applications</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                  <input type="checkbox"/> Module exam (MP)                  <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%						

#### 4 General Elective Area

7	<b>Study Achievement:</b>			
	zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
	a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4			
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Juraj Somorovsky			
13	<b>Other Notes:</b> <i>Remarks of course Web Security:</i> <b>Implementation method:</b> The topics are conveyed through lecture presentations. They are further deepened through individual practical tasks. <b>Learning Material, Literature:</b> <ul style="list-style-type: none"> <li>• Lecture slides and exercise sheets</li> <li>• Scientific literature</li> <li>• Additional literature will be announced in the course.</li> </ul>			

## 4.11 Computer Science Focus Area Software Engineering

Concepts of Computer Science							
Concepts of Computer Science							
<b>Module number:</b> M.079.4203	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
a)	L.079.05721 Concepts of Computer Science	L2 Ex3	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none <i>Prerequisites of course Concepts of Computer Science:</i> <b>Recommended Proficiencies</b> <ul style="list-style-type: none"> <li>• Good general education</li> <li>• Ability to read and analyze longer and complex texts from computer science as well as the humanities</li> </ul>						

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Concepts of Computer Science:</i></p> <p>The course examines the interplay of digital artifacts and cognitive performance. For this purpose, the epistemological and methodological foundations will be developed that allow the diverse relationships between computer science systems and their application to be addressed in such a way that the computer science-specific consequences become apparent. In the class, these concepts will be systematically analyzed and evaluated historically, technically, and with respect to their potentials.</p> <p>The lecture discusses the relevant theoretical and conceptual foundations of computer science. Special attention will be paid to differentiate between technical concepts and the sphere of usage. Against this background, theories of interactive systems will be explored in order to examine which role technical artifacts play with respect to processes of the mind. When developing computer systems, relevant data and processes need to be anticipated to a certain degree and modeled as formal systems. This raises issues like the question under which conditions such a formal description can be made in an adequate way and with which consequences regarding the reliability and responsible use of computer systems.</p> <p>The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• Basic concepts of computer science</li> <li>• Historical background of developments in computer science</li> <li>• Digital media and mental processes</li> <li>• Theories of digital media and interactive systems</li> <li>• Paradigms of support and replacement of mental processes</li> <li>• Modeling and formalization of data and processes</li> <li>• Trustworthiness of systems</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• examine the characteristics of computer systems in a theory-based manner,</li> <li>• explain cognitive-psychological, sociological and system-theoretical basics of computer science</li> <li>• differentiate technical and non-technical issues and relate them adequately to each other,</li> <li>• evaluate and compare current technological developments</li> <li>• assess innovation potentials in the field of digital technologies,</li> <li>• weigh risks and potentials for successful use of information technology systems.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/>Final module exam (MAP)              <input type="checkbox"/>Module exam (MP)              <input type="checkbox"/>Partial module exams (MTP)       </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr style="background-color: #f2f2f2;"> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%						

#### 4 General Elective Area

7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4)		
12	<b>Module coordinator:</b> Dr. Harald Selke		
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Concepts of Computer Science:</i></p> <p><b>Implementation method</b> The lecture follows a flipped classroom concept in which students learn about topics based on their reading of scientific literature as well as individual research. They will then present these topics in short presentations in the tutorials in the style of a mini-seminar. Building on this, the lecture then conveys connections between the literature covered in the tutorials and adds further facets.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Lecture slides</li> <li>• Wardrip-Fruin, N.; Montfort, N. (eds.): The New Media Reader. Cambridge, Ma.: MIT Press, 2003.</li> <li>• Additional scientific literature will be announced in the lectures.</li> </ul>		

#### 4 General Elective Area

Data-Driven Engineering							
Data-Driven Engineering							
<b>Module number:</b> M.079.4204	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05722 Data-Driven Engineering	L2 Ex3	75	105	C	60/30	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none						
4	<p><b>Contents:</b></p> <p><i>Contents of the course Data-Driven Engineering:</i></p> <p>The goal of the lecture is to provide a comprehensive overview of the potentials and use cases in data-driven engineering. Important fundamentals and concepts from the fields of engineering and artificial intelligence are introduced and explained using meaningful practical examples. The acquired knowledge is deepened and implemented in exercises. As part of a group project, participants will develop their own functional engineering assistant.</p> <p>Data is the oil of the 21st century. Data is also becoming increasingly important in product development. Both field data and development data can be processed using modern data analysis methods and AI processes to increase the efficiency and effectiveness of product development. The lecture provides an overview of the challenges and possible solutions of Data-driven Engineering. Theoretical principles and concepts are introduced and exemplary applications from practice are presented. The process is considered from data acquisition to possibilities for data evaluation and the development of innovative assistance systems. The acquired knowledge is deepened and implemented in the exercises.</p> <p>Contents of the course are:</p> <ul style="list-style-type: none"> <li>• Motivation and definition of terms</li> <li>• Potentials of data-driven engineering</li> <li>• Engineering IT and data management along the product life cycle</li> <li>• Fundamentals of data analytics and AI (in particular generative AI)</li> <li>• Data structures and formats in product development</li> <li>• Application examples and assistance systems (co-pilots) along the product life cycle (from requirements engineering to production planning)</li> <li>• Methods for planning and implementing Data-driven Engineering use cases</li> <li>• Technical development of assistance systems (co-pilots) in Data-driven Engineering</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• recognize and evaluate the potential of Data-driven Engineering</li> <li>• evaluate prerequisites for the application of Data-driven product development concepts</li> <li>• analyze and design Engineering IT infrastructures</li> <li>• plan and implement use cases for Data-driven product development</li> <li>• design assistance systems (co-pilots) for Data-driven use cases</li> </ul>								
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
zu	Type of examination	Duration or scope	Weighting for the module grade						
a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr.-Ing. Roman Dumitrescu</p>								
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Data-Driven Engineering:</i></p> <p><b>Implementation Method</b></p> <p>The course consists of three components: In the lecture, basic concepts of data-driven engineering are introduced using slides and underlined with practical examples. In the accompanying exercise, the concepts are applied by the students. The project allows students to apply what they have learned in group work.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Literature will be announced in the course.</li> </ul>								

4 General Elective Area

<b>Data-Driven Innovation</b>						
Data-Driven Innovation						
<b>Module number:</b> M.079.4076	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05822 Data-Driven Innovation	L2 Ex3	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Data-Driven Innovation:</i></p> <p>Innovations arise when companies successfully launch new or improved products and services on the market. Innovations are the prerequisite for prosperity, economic growth and competitive advantages. The increasing digitalization of all aspects of our lives has given rise to numerous new approaches to innovation and how it is created. The Data-Driven Innovation lecture provides a basic overview of this: The basics of innovation and data management are discussed, new digitized market services and business models are discussed and the impact on the innovation process is examined and analyzed. Building on this, the course deals with how organizations can implement data-driven innovations.</p> <p>The module includes the following content:</p> <ul style="list-style-type: none"><li>• Innovation and Data<ul style="list-style-type: none"><li>– Fundamentals of Innovation Management</li><li>– Fundamentals of Data in Organizations</li></ul></li><li>• Data-Driven Offerings<ul style="list-style-type: none"><li>– Smart Products</li><li>– Smart Services</li><li>– Digital Platforms</li><li>– Data Spaces &amp; Digital Business Models</li></ul></li><li>• Innovation Processes<ul style="list-style-type: none"><li>– Innovation Management Methods and Tools</li><li>– Data-infused Innovation Processes</li></ul></li><li>• Organizing the data-driven Transformation<ul style="list-style-type: none"><li>– Digital Transformation</li><li>– Using data for Sustainability</li></ul></li></ul>
5	<p><b>Learning outcomes and competences:</b></p> <p>The students</p> <ul style="list-style-type: none"><li>• become familiar with the basics of innovation and data management as well as key concepts and approaches</li><li>• understand the influence of digitalization on the market performance of manufacturing companies in particular</li><li>• be able to understand and reflect on innovation processes in practice</li><li>• understand how the transformation to a data-driven company can take place</li><li>• be able to apply various approaches to analyze problems and find solutions.</li><li>• be able to systematically find, conceptualize, test and develop ideas towards a market approach.</li></ul>

#### 4 General Elective Area

6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/> Final module exam (MAP)      <input type="checkbox"/> Module exam (MP)      <input type="checkbox"/> Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%						
7	<p><b>Study Achievement:</b></p> <p>none</p>								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>none</p>								
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>								
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>								
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)</p>								
12	<p><b>Module coordinator:</b></p> <p>Dr. Christian Koldewey, Prof. Dr.-Ing. Roman Dumitrescu</p>								
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Data-Driven Innovation:</i></p> <p><b>Implementation method</b></p> <p>The module consists of two parts 1. lecture with slides: basics and concepts are explained in the lecture and illustrated with examples. 2. exercises (tutorial): In the exercises, knowledge is transferred and the concepts are applied. The exercises have to be prepared by the students themselves.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Gausemeier, Jürgen &amp; Dumitrescu, Roman &amp; Echterfeld, Julian &amp; Pfänder, Tomas &amp; Steffen, Daniel &amp; Thielemann, Frank. (2018). Innovationen für die Märkte von morgen: Strategische Planung von Produkten, Dienstleistungen und Geschäftsmodellen. 10.3139/9783446429727. (<a href="https://www.hanser-fachbuch.de/fachbuch/artikel/9783446428249#content-desc">https://www.hanser-fachbuch.de/fachbuch/artikel/9783446428249#content-desc</a>)</li> <li>• Beverungen, Daniel &amp; Dumitrescu, Roman &amp; Kühn, Arno &amp; Plass, Christoph. (2024). Digitale Plattformen im industriellen Mittelstand Strategien, Methoden, Umsetzungsbeispiele. <a href="https://ki-marktplatz.com/wp-content/uploads/2021/02/KI-MP_Whitepaper.pdf">https://ki-marktplatz.com/wp-content/uploads/2021/02/KI-MP_Whitepaper.pdf</a></li> <li>• <a href="https://www.advanced-systems-engineering.de/#studie">https://www.advanced-systems-engineering.de/#studie</a></li> </ul>								

#### 4 General Elective Area

Data Science for Software Engineering							
Data Science for Software Engineering							
<b>Module number:</b> M.079.4101	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> summer term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05817 Data Science for Software Engineering	L2 Ex3	75	105	C	30	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> <i>Prerequisites of course Data Science for Software Engineering:</i> <b>Recommended Proficiencies</b> Good programming skills using Java and/or Python is helpful to make the assignments. Basic background on machine learning is helpful to understand some of the Data Science concepts.						
4	<b>Contents:</b> <i>Contents of the course Data Science for Software Engineering:</i> Software engineers deal with software repositories in their daily work, such as when they develop source code in version control systems, or post issues in issue trackers, or communicate through emails in mailing lists, or discuss in forums and blogs. The big amount of data in software repositories, their continuous evolution, complexity and heterogeneity present a challenge for software engineers. In the past years, researchers proposed approaches that use techniques from the data science to support software engineers. This course will explain the application of data science techniques on software repositories to achieve common software engineering tasks. The course includes the following topics: <ul style="list-style-type: none"> <li>• Types and structure of software repositories.</li> <li>• Clustering of source code.</li> <li>• Natural language processing pipeline.</li> <li>• Topic modeling.</li> <li>• Word embedding.</li> <li>• Information retrieval.</li> <li>• Supervised machine learning.</li> <li>• Statistical analysis.</li> </ul> Concepts are discussed in the lectures and applied using a set of group assignments to analyze opensource systems, and achieve certain software architecture and maintenance tasks.						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• Clarify and discuss types and structure of software repositories.</li> <li>• Clarify and discuss main concepts of data science techniques, and their application on software repositories.</li> <li>• Apply data science techniques on large-scale software repositories.</li> <li>• Derive useful implications from the analysis results.</li> <li>• Summarize and report analysis results in a scientific format.</li> <li>• Work in teams.</li> <li>• Write scientific reports</li> <li>• Present research results</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">zu</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination</td> <td>90-120 min or 30-45 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination	90-120 min or 30-45 min	100%
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10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v3, Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr. Mohamed Aboubakr Mohamed Soliman</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Data Science for Software Engineering:</i></p> <p><b>Implementation Method</b> The course focus on the application of data science methods in software engineering more than the mathematical background of data science methods. The main concepts of methods are conveyed through a presentation as part of a lecture and the application of methods is further investigated through group assignments and presentations.</p> <p><b>Learning Material, Literature</b> Beside the slides, further learning materials from prominent publications in the software engineering literature will be provided for each topic.</p>
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#### 4 General Elective Area

Data Science in Industrial Applications						
Data Science in Industrial Applications						
<b>Module number:</b> M.079.4075	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) L.079.05795 Data Science in Industrial Applications	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Data Science in Industrial Applications:</i> The course “Data Science in Industrial Applications” deals with the methods and techniques of data analysis in an industrial context. Students learn basic concepts of data analysis and how to apply them in practice.</p> <p>The increasing networking of machines, sensors and IT systems in the context of Industry 4.0 has led to a rapid increase in the amount of available data. The analysis of data offers enormous potential for the automation of cognitive tasks, the optimization of processes and the further creation of value from data. The lecture will provide an overview of the challenges and solution approaches for the industrial application of Data Science. This includes the integration of industrial data sources from the field, the IT landscape in manufacturing companies and the setup of (Big Data) infrastructure, typical algorithms in the area of time series processing, optimization or image processing as well as the embedding in business processes. Theoretical and methodological basics, concepts and tools are introduced during the lecture and applied in small groups based on a case study as well as deepened in home exercises. The theoretical concepts for the planning, introduction and implementation of Industrial Data Science in theory are supplemented by practical real-life examples.</p>					

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> Students <ul style="list-style-type: none"> <li>• understand the challenges of applying Data Science in industrial applications,</li> <li>• have an overview of typical application examples,</li> <li>• are able to apply methods of signal processing, machine learning, and statistics to industrial problems,</li> <li>• are able to plan the implementation of data acquisition, data architecture, and integration into business processes,</li> <li>• are able to develop solutions on their own as well as in cooperation,</li> <li>• are proficient in basic project management skills.</li> </ul>										
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)										
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>							
	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%							
7	<b>Study Achievement:</b> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 5px;"> <thead> <tr> <th style="width: 5%; text-align: center;">zu</th> <th style="width: 55%; text-align: center;">Type of achievement</th> <th style="width: 20%; text-align: center;">Duration or Scope</th> <th style="width: 20%; text-align: center;">SL / QT</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Assignments, course paper or progress reports</td> <td></td> <td style="text-align: center;">CA</td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Assignments, course paper or progress reports		CA
zu	Type of achievement	Duration or Scope	SL / QT								
a)	Assignments, course paper or progress reports		CA								
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement										
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.										
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).										
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4										
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Roman Dumitrescu										

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Data Science in Industrial Applications:</i></p> <p><b>Implementation Method</b></p> <p>The course includes lectures (slide-based), exercises (interactive), and project work. In the lectures, the theoretical basics of data analysis in an industrial context are taught. In the exercises, students have the opportunity to apply what they have learned by means of practical tasks. The project work offers the students the opportunity to apply the learned knowledge in a larger context. In the exercise, knowledge transfer and application of the concepts take place in a case study in the form of workshops and implementation of an industrial analytics application in independent group work.</p> <p><b>Learning Material, Literature</b></p> <p>A more detailed list of the lecture materials and references will be given in the first course. A good first insight into the subject area is given by:</p> <ul style="list-style-type: none"> <li>• Wiendahl, Hans-Peter; Wiendahl, Hans-Hermann (2019): Betriebsorganisation für Ingenieure. 9., vollständig überarbeitete Auflage. München: Hanser (Hanser eLibrary).</li> <li>• Zahn, Erich; Schmid, Uwe (1996): Grundlagen und operatives Produktionsmanagement. Mit 42 Tabellen. Stuttgart: Lucius &amp; Lucius (Grundwissen der Ökonomik Betriebswirtschaftslehre, 1).</li> <li>• Günther Schuh; Achim Kampker: Strategie und Management produzierender Unternehmen: Handbuch Produktion und Management 1 (VDI-Buch) (German Edition).</li> <li>• Schuh, Günther; Riesener, Michael (2018): Produktkomplexität managen. Strategien - Methoden - Tools. 3., vollständig überarbeitete Auflage. München: Hanser (Hanser eLibrary). Online verfügbar unter <a href="http://www.hanser-elibrary.com/doi/book/10.3139/9783446453340">http://www.hanser-elibrary.com/doi/book/10.3139/9783446453340</a>.</li> <li>• Schuh, Günther; Schmidt, Carsten (2014): Produktionsmanagement. DOI: 10.1007/978-3-642-54288-6.</li> <li>• Bishop, Christopher M. (2006): Pattern recognition and machine learning. New York: Springer (Information science and statistics).</li> <li>• Cao, Longbing (2018): Data Science. In: ACM Comput. Surv. 50 (3), S. 1–42. DOI: 10.1145/3076253.</li> <li>• Geron, Aurelien (2019): Hands-On Machine Learning with Scikit-Learn and TensorFlow: O'Reilly Media.</li> <li>• Goodfellow, Ian; Bengio, Yoshua; Courville, Aaron (2016): Deep Learning. MIT Press.</li> <li>• James, Gareth; Witten, Daniela; Hastie, Trevor; Tibshirani, Robert (2013): An Introduction to Statistical Learning. New York, NY: Springer New York (103).</li> <li>• Mitchell, Tom M. (1997): Machine Learning. New York: McGraw-Hill (McGraw-Hill series in computer science).</li> <li>• Runkler, Thomas A. (2016): Data Analytics. Wiesbaden: Springer Fachmedien Wiesbaden.</li> <li>• Russell, Stuart (2009): Artificial Intelligence: A Modern Approach. 3rd Edition. Pearson.</li> <li>• Schutt, Rachel; O'Neil, Cathy (2013): Doing data science. Straight talk from the frontline. 1. ed. Beijing: O'Reilly.</li> </ul>
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#### 4 General Elective Area

Designing code analyses for large-scale software systems 1							
Designing code analyses for large-scale software systems 1							
<b>Module number:</b> M.079.4070	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05781 Designing code analyses for large-scale software systems 1	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Designing code analyses for large-scale software systems 1:</i> <b>Recommended Proficiencies</b> A mature understanding of the Java programming languages and object-oriented programming will be helpful.						

## 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Designing code analyses for large-scale software systems 1:</i>            Static code analysis is frequently used to find programming mistakes automatically, by searching for suspicious anti-patterns in a program's code. This course will explain how to design static code analysis that are inter-procedural, i.e., consider the whole program, across procedure boundaries. Designing such analyses is challenging, as they need to handle millions of program statements efficiently and precisely. Example applications are drawn from the area of IT security. This course is part of a combination DECA 1/2. In DECA 2 we will be covering current approaches directly out of research. We strongly recommend attending DECA 1 before DECA 2.</p> <p><b>Topics covered include:</b></p> <ul style="list-style-type: none"> <li>• Type systems and flow-insensitive, constraint-based analysis</li> <li>• Lattices and fixed points</li> <li>• Intra-procedural flow-sensitive static code analysis</li> <li>• Interval analysis, widening and narrowing</li> <li>• Call-graph construction</li> <li>• Pointer Analysis</li> <li>• Inter-procedural program analysis</li> <li>• Call-strings approach to context-sensitive analysis</li> <li>• Functional approach to context-sensitive analysis</li> <li>• Value-based termination, VASCO</li> <li>• Distributive analyses using IFDS</li> <li>• Sensible arrangements of Flow Functions</li> <li>• Distributive analyses using IDE</li> </ul> <p>Throughout, we will discuss applications to software security.</p>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of the module, students will be able to</p> <ul style="list-style-type: none"> <li>• name and distinguish the most important concepts and algorithms in the field of static program analysis</li> <li>• explain the effects of various alternative design decisions when designing a static program analysis</li> <li>• implement and apply simple static program analyses themselves and illustrate their function</li> <li>• contrast and compare data structures and algorithms for static program analysis</li> <li>• evaluate and justify the applicability of certain analysis procedures to specific application contexts, and</li> <li>• develop tools for static program analysis by composing several analysis methods.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 10%; text-align: center;">zu</th> <th style="width: 45%; text-align: center;">Type of examination</th> <th style="width: 20%; text-align: center;">Duration or scope</th> <th style="width: 25%; text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%						

#### 4 General Elective Area

7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Eric Bodden		
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Designing code analyses for large-scale software systems 1:</i></p> <p><b>Implementation method</b> Lectures and group exercises as well as practical programming labs using worldwide leading frameworks for static code analysis</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Thomas Reps, Susan Horwitz, and Mooly Sagiv. 1995. Precise interprocedural dataflow analysis via graph reachability. POPL '95</li> <li>• Shmuel Sagiv, Thomas W. Reps, and Susan Horwitz. 1995. Precise Interprocedural Dataflow Analysis with Applications to Constant Propagation. TAPSOFT '95</li> <li>• Akash Lal, Thomas Reps, and Gogul Balakrishnan. 2005. Extended weighted pushdown systems. CAV 2005</li> <li>• Nomair A. Naeem, Ondrej Lhoták, and Jonathan Rodriguez. 2010. Practical extensions to the IFDS algorithm. CC 2010</li> <li>• Yannis Smaragdakis, Martin Bravenboer, and Ondrej Lhoták. 2011. Pick your contexts well: understanding object-sensitivity. POPL 2011</li> <li>• Eric Bodden. 2012. Inter-procedural data-flow analysis with IFDS/IDE and Soot. SOAP 2012</li> <li>• Rohan Padhye, Uday P. Khedker. Interprocedural Data Flow Analysis in Soot using Value Contexts. SOAP 2013</li> </ul>		

4 General Elective Area

<b>Designing code analyses for large-scale software systems 2</b>							
Designing code analyses for large-scale software systems 2							
<b>Module number:</b> M.079.4071	<b>Workload (h):</b> 180	<b>Credits:</b> 6		<b>Regular Cycle:</b> summer term			
<b>Language:</b> en	<b>Semester number:</b> 2-3	<b>Duration (in sem.):</b> 1		<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05800 Designing code analyses for large-scale software systems 2	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Designing code analyses for large-scale software systems 2:</i> <b>Recommended Proficiencies</b> We strongly recommend that attendees have completed DECA 1 beforehand. A mature understanding of the Java and/or C++ programming languages and object-oriented programming will be helpful.						

#### 4 General Elective Area

4	<p><b>Contents:</b></p> <p><i>Contents of the course Designing code analyses for large-scale software systems 2:</i>          Static code analysis has the goal of finding programming mistakes automatically, by searching for suspicious anti-patterns in a program's code. This course will explain how to design static code analysis that are inter-procedural, i.e., consider the whole program, across procedure boundaries. Designing such analyses is challenging, as they need to handle millions of program statements efficiently and precisely. Example applications are drawn from the area of IT security.          This course builds on the DECA 1 course. In DECA 2, we discuss novel concepts directly from research, for example so-called demand-driven analyses, which are characterized by a more precise and at the same time more efficient analysis, but also pushdown systems, which provide a allow elegant modeling and at the same time fast execution of program analyses. Last but not least, we explain current solutions to practical problems in static analysis, such as the use of reflection and native code.</p> <p><b>Topics covered include:</b></p> <ul style="list-style-type: none"> <li>• Program analysis of software product lines</li> <li>• Modeling call stacks and field accesses with Pushdown Systems</li> <li>• Modeling auxiliary analysis information with Weighted Pushdown Systems</li> <li>• Efficiency and precision gains through Demand-driven Program Analysis</li> <li>• Synchronized Pushdown Systems in the Boomerang framework</li> <li>• Applied Android code analysis with FlowDroid</li> <li>• Dealing with Reflection through TamiFlex</li> <li>• Hybrid static and dynamic analysis with Harvester</li> <li>• Learning source, sink and sanitizer definitions with SWAN and SWAN Assist</li> <li>• Explainable static analysis</li> </ul>								
5	<p><b>Learning outcomes and competences:</b></p> <p>Upon completion of the module, students will be able to</p> <ul style="list-style-type: none"> <li>• name and explain the most important challenges in inter-procedural static program analysis</li> <li>• name and distinguish current methods in the field of inter-procedural static program analysis</li> <li>• explain the implications of various alternative design decisions in the design of an inter-procedural static program analysis.</li> <li>• contrast and compare data structures and algorithms for inter-procedural static program analysis and</li> <li>• evaluate and justify the applicability of current analysis techniques in a broad range of application contexts.</li> </ul>								
6	<p><b>Assessments:</b></p> <p> <input checked="" type="checkbox"/> Final module exam (MAP)                <input type="checkbox"/> Module exam (MP)                <input type="checkbox"/> Partial module exams (MTP)         </p> <table border="1" style="width: 100%; border-collapse: collapse; margin-top: 10px;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>120-180 min or 30-45 min or 30 min</td> <td style="text-align: center;">100%</td> </tr> </tbody> </table>	zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	120-180 min or 30-45 min or 30 min	100%
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#### 4 General Elective Area

7	<b>Study Achievement:</b>		
zu	<b>Type of achievement</b>	<b>Duration or Scope</b>	<b>SL / QT</b>
a)	Assignments, course paper or progress reports		CA
8	<b>Prerequisites for participation in examinations:</b> Passing of course achievement		
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.		
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).		
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4		
12	<b>Module coordinator:</b> Prof. Dr. Eric Bodden		
13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Designing code analyses for large-scale software systems 2:</i></p> <p><b>Implementation method</b> Lectures and group exercises as well as programming exercises using widely used real-world static analysis frameworks (e.g. Soot, Phasar, FlowDroid)</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"> <li>• Context-, Flow-, and Field-sensitive Data-flow Analysis Using Synchronized Pushdown Systems (Johannes Späth, Karim Ali, Eric Bodden), In Proceedings of the ACM SIGPLAN Symposium on Principles of Programming Languages, pages 48:1–48:29, 3(POPL), 2019.</li> <li>• FlowDroid: Precise Context, Flow, Field, Object-sensitive and Lifecycle-aware Taint Analysis for Android Apps (Steven Arzt, Siegfried Rasthofer, Christian Fritz, Eric Bodden, Alexandre Bartel, Jacques Klein, Yves Le Traon, Damien Ochteau, Patrick McDaniel), In Proceedings of the 35th ACM SIGPLAN Conference on Programming Language Design and Implementation, pages 259–269, PLDI '14, ACM, 2014.</li> <li>• Codebase-Adaptive Detection of Security-Relevant Methods (Goran Piskachev, Lisa Nguyen Quang Do, Eric Bodden), In ACM SIGSOFT International Symposium on Software Testing and Analysis (ISSTA), 2019.</li> <li>• Taming Reflection: Aiding Static Analysis in the Presence of Reflection and Custom Class Loaders (Eric Bodden, Andreas Sewe, Jan Sinschek, Hela Oueslati, Mira Mezini), In ICSE '11: International Conference on Software Engineering, pages 241–250, ACM, 2011.</li> </ul>		

4 General Elective Area

Human Factors in Security and Privacy						
Human Factors in Security and Privacy						
<b>Module number:</b> M.079.4092	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term			
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
a)	L.079.05733 Human Factors in Security and Privacy	L3 Ex2	75	105	C	70/35
2	<b>Options within the module:</b> none					
3	<b>Admission requirements:</b> none					
4	<p><b>Contents:</b></p> <p><i>Contents of the course Human Factors in Security and Privacy:</i> Humans are important actors in security. A provable secure system is only useful if it can be actually used by users, and system designers need to account for human behavior if they wish to have both security and usability. In this class, we will examine factors of usability of security and privacy through a research-based, project-driven examination. We will cover core areas of security and privacy, as well as cover methods in human interaction (HCI) that can be used to measure the usability of security and privacy. Students are expected to complete problem sets on the topic and complete a research-based project. The course includes the following contents:</p> <ul style="list-style-type: none"> <li>• How to write a scientific research paper?</li> <li>• How to conduct an independent scientific study in the field of Human Factors in Security and Privacy?</li> <li>• Methodology: qualitative, quantitative and “mixed” methods.</li> <li>• Introduction to research and scientific ethics</li> <li>• Introduction to literature research</li> <li>• Presenting scientific results at a conference</li> </ul>					

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• understand and apply research methods in human factors in usable security and privacy.</li> <li>• develop relevant hypotheses and research questions in the space of usable security and privacy.</li> <li>• design and deploy a research study and analyze the results.</li> <li>• describe, support, and effectively argue a result using the best practices of scientific writing.</li> <li>• understand ethical issues related to human factors research in security and privacy.</li> <li>• understand the major topics and themes of usable security and privacy.</li> <li>• present research results in class.</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 50%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 20%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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a)	Assignments, course paper or progress reports		CA								
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>Passing of course achievement</p>										
9	<p><b>Prerequisites for assigning credits:</b></p> <p>The credit points are awarded after the module examination was passed.</p>										
10	<p><b>Weighing for overall grade:</b></p> <p>The module is weighted according to the number of credits (factor 1).</p>										
11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Prof. Dr. Yasemin Acar</p>										

#### 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Human Factors in Security and Privacy:</i></p> <p><b>Implementation Method</b></p> <p>The contents are presented and elaborated in the lecture. In the accompanying tutorial, the lecture topics are deepened and discussed both in plenary and in small groups. In addition, students are presenting and discussing their progress on the projects.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Current freely available research papers will be provided in the course.</li><li>• Redmiles, Elissa M., Yasemin Acar, Sascha Fahl, and Michelle L. Mazurek. A summary of survey methodology best practices for security and privacy researchers. 2017. <a href="https://drum.lib.umd.edu/bitstream/handle/1903/19227/CS-TR-5055.pdf">https://drum.lib.umd.edu/bitstream/handle/1903/19227/CS-TR-5055.pdf</a></li><li>• Additional literature will be announced in the course.</li></ul>
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4 General Elective Area

Model-Based Systems Engineering							
Model-Based Systems Engineering							
<b>Module number:</b> M.079.4062	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P				
1	<b>Module structure:</b>						
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>	
	a) L.079.05810 Model-Based Systems Engineering	L3 Ex2	75	105	C	70/35	
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Model-Based Systems Engineering:</i> <b>Recommended Proficiencies</b> Basics of Systems Engineerings						
4	<b>Contents:</b>  <i>Contents of the course Model-Based Systems Engineering:</i> Due to the technical change from mechatronic to intelligent technical systems (ITS), companies and development teams are facing many challenges. A key factor is the increase in complexity and networking of systems (products). Existing approaches in product development cannot cover this efficiently and effectively. Model-based Systems Engineering (MBSE) presents itself as a promising approach to solve these challenges. MBSE sees itself as a further development of systems engineering and builds on its foundations. Systems engineering, which is primarily based on documents, is extended by the introduction of models. The course includes the following content:  <ul style="list-style-type: none"> <li>• Intelligent Engineering Systems</li> <li>• Model-based Systems Engineering 101</li> <li>• Systems Modeling Fundamentals</li> <li>• Languages and Methods - CONSENS, SysML</li> <li>• Systems Architecting</li> <li>• IT Tools for MBSE</li> </ul>						

#### 4 General Elective Area

5	<b>Learning outcomes and competences:</b> The students <ul style="list-style-type: none"> <li>• acquire a solid understanding of Model-Based System Engineering</li> <li>• know different methods, languages, and tools</li> <li>• are able to apply the knowledge they have gained</li> <li>• are able to work out solutions independently and communicate them to the lecturers.</li> </ul>			
6	<b>Assessments:</b> <input checked="" type="checkbox"/> Final module exam (MAP) <input type="checkbox"/> Module exam (MP) <input type="checkbox"/> Partial module exams (MTP)			
	zu	<b>Type of examination</b>	<b>Duration or scope</b>	<b>Weighting for the module grade</b>
	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
7	<b>Study Achievement:</b> none			
8	<b>Prerequisites for participation in examinations:</b> none			
9	<b>Prerequisites for assigning credits:</b> The credit points are awarded after the module examination was passed.			
10	<b>Weighing for overall grade:</b> The module is weighted according to the number of credits (factor 1).			
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4, Master's Program Electrical Systems Engineering (ESEMA v2), Master's Program Electrical Systems Engineering v3 (ESEMA v3)			
12	<b>Module coordinator:</b> Prof. Dr.-Ing. Roman Dumitrescu			

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Model-Based Systems Engineering:</i></p> <p><b>Implementation Method</b></p> <p>The module consists of two parts</p> <ol style="list-style-type: none"><li>1. lecture with slides: basics and concepts are explained in the lecture and illustrated with examples.</li><li>2. exercises (tutorial): In the exercises, knowledge is transferred and the concepts are applied. The exercises have to be prepared by the students themselves.</li></ol> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Gausemeier, J.; Dumitrescu, R.; Steffen, D.; Czaja, A.; Wiederkehr, O.; Tschirner, C.: Systems Engineering in industrial practice. Heinz Nixdorf Institute, University Paderborn, 2013, Under: <a href="https://www.hni.uni-paderborn.de/en/spe/systemsengineering/">https://www.hni.uni-paderborn.de/en/spe/systemsengineering/</a></li><li>• Dumitrescu, R.; Albers, A.; Riedel, O.; Stark, R.; Gausemeier, J. (Eds): Engineering in Germany – Status quo in Business and Science. Federal Ministry of Education and Research, 2021 Under: <a href="https://www.advanced-systems-engineering.de/#studie">https://www.advanced-systems-engineering.de/#studie</a></li><li>• Additional literature will be announced in the course.</li></ul>
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#### 4 General Elective Area

<b>Software Architecture Design and Recovery</b>							
Software Architecture Design and Recovery							
<b>Module number:</b> M.079.4094	<b>Workload (h):</b> 180	<b>Credits:</b> 6	<b>Regular Cycle:</b> winter term				
<b>Language:</b> en	<b>Semester number:</b> 1-3	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> WP				
1	<b>Module structure:</b>						
	a)	L.079.05708 Software Architecture Design and Recovery	form of teaching L2 Ex3	contact-time (h) 75	self-study (h) 105	status (C/CE) C	group size (TN) 30
2	<b>Options within the module:</b> none						
3	<b>Admission requirements:</b> none  <i>Prerequisites of course Software Architecture Design and Recovery:</i> <b>Recommended Proficiencies</b> A good understanding of Java and the principle of object-oriented programming is helpful.						
4	<b>Contents:</b>  <i>Contents of the course Software Architecture Design and Recovery:</i> Software architecture is concerned with the principal design decisions of a software system. These decisions have significant impact on the system's quality, such as maintainability, performance and security. This course will explain fundamental concepts of the software architecture field, as well as how to apply techniques to recover design decisions from existing software repositories. The course includes the following topics from software architecture field:  <ul style="list-style-type: none"> <li>• Types of design decisions.</li> <li>• Architectural components and recovery.</li> <li>• Architectural solutions such as patterns, tactics and technologies.</li> <li>• Architectural documentation.</li> <li>• Software repositories.</li> <li>• Architectural knowledge.</li> <li>• Design processes.</li> </ul> Furthermore, the course discusses and applies common research methods:  <ul style="list-style-type: none"> <li>• Grounded theory</li> <li>• Case studies</li> </ul>						

#### 4 General Elective Area

5	<p><b>Learning outcomes and competences:</b></p> <p>Students will be able to</p> <ul style="list-style-type: none"> <li>• clarify and discuss main concepts in the software architecture field, such as architectural solutions, components, and design processes</li> <li>• analyze large-scale software systems for architectural design decisions</li> <li>• execute design processes to make design decisions</li> <li>• apply common research methods on software architecture problems</li> <li>• summarize and report research results in a scientific format</li> <li>• work in teams</li> <li>• present their results to the audience</li> </ul>										
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="text-align: center;">ZU</th> <th style="text-align: center;">Type of examination</th> <th style="text-align: center;">Duration or scope</th> <th style="text-align: center;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">a)</td> <td>Written or oral examination or report</td> <td>90-120 min or 30-45 min or 30 min</td> <td>100%</td> </tr> </tbody> </table>			ZU	Type of examination	Duration or scope	Weighting for the module grade	a)	Written or oral examination or report	90-120 min or 30-45 min or 30 min	100%
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11	<p><b>Reuse in degree courses or degree course versions :</b></p> <p>Masterstudiengang Computer Engineering v4 (CEMA v4), Masterstudiengang Informatik v4</p>										
12	<p><b>Module coordinator:</b></p> <p>Dr. Mohamed Aboubakr Mohamed Soliman</p>										

## 4 General Elective Area

13	<p><b>Other Notes:</b></p> <p><i>Remarks of course Software Architecture Design and Recovery:</i></p> <p><b>Implementation Method</b> Lectures and group assignments on large open-source software systems, as well as presentations. Concepts are discussed in the lectures and applied using a set of group assignments on real open-source software systems.</p> <p><b>Learning Material, Literature</b></p> <ul style="list-style-type: none"><li>• Bass, L., Clements, P., Kazman, R. (2012). Software Architecture in Practice. 3rd Edition, Addison-Wesley Professional.</li><li>• Kruchten P, Lago P, van Vliet H (2006) Building Up and Reasoning About Architectural Knowledge. In: Quality of Software Architectures, Springer Berlin Heidelberg.</li><li>• Additional literature will be announced in the course.</li></ul>
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## 5 Thesis

Abschlussarbeit						
Final Degree Module						
<b>Module number:</b> A.048.17002	<b>Workload (h):</b> 900	<b>Credits:</b> 30	<b>Regular Cycle:</b> summer- / winter term			
<b>Language:</b> de / en	<b>Semester number:</b> 4. Semester	<b>Duration (in sem.):</b> 1	<b>Module status (P=C/WP=CE)</b> P			
1	<b>Module structure:</b>					
	<b>Course</b>	<b>form of teaching</b>	<b>contact-time (h)</b>	<b>self-study (h)</b>	<b>status (C/CE)</b>	<b>group size (TN)</b>
	a) Working Plan (CEMA)		15	135	C	
	b) Master Thesis (CE)		30	720	C	
2	<b>Options within the module:</b> None					
3	<p><b>Admission requirements:</b></p> <p>The final thesis module can only be started when modules amounting to 45 credit points have been successfully completed. In the case of enrollment with conditions, the passing of the associated examinations must also be proven.</p> <p><i>Prerequisites of course Arbeitsplan (CEMA):</i> Recommended: Depending on the chosen topic, knowledge from the chosen specialization module.</p> <p><i>Prerequisites of course Masterarbeit (CE):</i> Recommended: Depending on the topic chosen, knowledge of the chosen area of specialization.</p>					

## 5 Thesis

4	<p><b>Contents:</b></p> <p><b>Work plan:</b> After the topic has been agreed upon with the supervisor, an initial rough draft is made. On this basis and a first literature research, the student has to submit a work plan documenting the results to be achieved including milestones for the work.</p> <p><b>Master thesis:</b> In the master thesis a problem is worked on according to scientific methods within a certain period of time. The thesis is thematically embedded in the scientific environment of the faculty and can make use of the multifaceted close cooperation with companies and industry. In addition to practical relevance, a master's thesis ensures in particular the suitability for methodical scientific work.</p>														
5	<p><b>Learning outcomes and competences:</b></p> <p>Within the framework of their final thesis, students work on a problem according to scientific methods within a certain period of time. The subject-specific methodological and interdisciplinary competences acquired in the course of the studies are to be applied accordingly. This includes in particular the structuring and planning of the individual work steps as well as the presentation of the results after completion of the work.</p> <p><b>Non-cognitive competencies</b></p> <ul style="list-style-type: none"> <li>• Commitment and engagement</li> <li>• Learning competence</li> <li>• Motivation to learn</li> <li>• Motivational and volitional skills</li> <li>• Writing and reading skills (academic)</li> <li>• Self-direction skills</li> </ul>														
6	<p><b>Assessments:</b></p> <p><input checked="" type="checkbox"/>Final module exam (MAP)      <input type="checkbox"/>Module exam (MP)      <input type="checkbox"/>Partial module exams (MTP)</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of examination</th> <th style="width: 20%;">Duration or scope</th> <th style="width: 25%;">Weighting for the module grade</th> </tr> </thead> <tbody> <tr> <td>a) - b)</td> <td>Masters Thesis incl. final presentation</td> <td>max. 120 DIN A4-Pages incl. 45-60 min</td> <td>100%</td> </tr> </tbody> </table>			zu	Type of examination	Duration or scope	Weighting for the module grade	a) - b)	Masters Thesis incl. final presentation	max. 120 DIN A4-Pages incl. 45-60 min	100%				
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a) - b)	Masters Thesis incl. final presentation	max. 120 DIN A4-Pages incl. 45-60 min	100%												
7	<p><b>Study Achievement:</b></p> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 10%;">zu</th> <th style="width: 45%;">Type of achievement</th> <th style="width: 20%;">Duration or Scope</th> <th style="width: 25%;">SL / QT</th> </tr> </thead> <tbody> <tr> <td>a)</td> <td>Working Plan</td> <td>150h</td> <td>QP</td> </tr> <tr> <td>b)</td> <td></td> <td></td> <td></td> </tr> </tbody> </table>			zu	Type of achievement	Duration or Scope	SL / QT	a)	Working Plan	150h	QP	b)			
zu	Type of achievement	Duration or Scope	SL / QT												
a)	Working Plan	150h	QP												
b)															
8	<p><b>Prerequisites for participation in examinations:</b></p> <p>None</p>														

## 5 Thesis

9	<b>Prerequisites for assigning credits:</b> Credits are awarded when the work plan has been verified and the master's thesis has been passed.
10	<b>Weighing for overall grade:</b> The module is weighted according to the twice the number of its credits (factor 2).
11	<b>Reuse in degree courses or degree course versions :</b> Masterstudiengang Computer Engineering v4 (CEMA v4)
12	<b>Module coordinator:</b> Dr.-Ing. Carsten Balewski
13	<b>Other Notes:</b> <b>Learning Materials, References</b> <ul style="list-style-type: none"><li>• Work plan: Depending on the chosen topic in consultation with the supervisor.</li><li>• Master thesis: Depending on the chosen topic in consultation with the supervisor.</li></ul> <b>Methodological implementation</b> <ul style="list-style-type: none"><li>• Work plan: Direct consultation with supervisor.</li><li>• Master thesis: Independent work supported by individual supervision.</li></ul>

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Erzeugt am 5. März 2026 um 17:12.